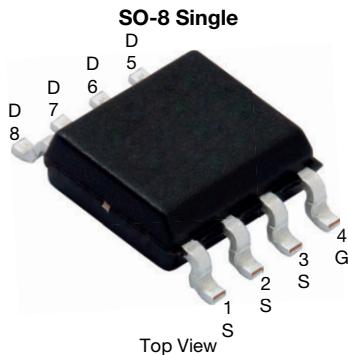


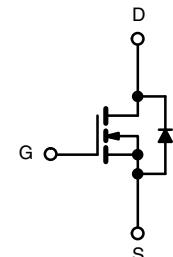
Automotive N-Channel 40 V (D-S) 175 °C MOSFET



FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified ^d
- 100 % R_g and UIS tested
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE


N-Channel MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	40
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.009
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	0.012
I_D (A)	20.7
Configuration	Single

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ4840EY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	40	V
Gate-source voltage		V_{GS}	± 20	
Continuous drain current	$T_C = 25$ °C	I_D	20.7	
	$T_C = 125$ °C		12	
Continuous source current (diode conduction)		I_S	6.5	A
Pulsed drain current ^a		I_{DM}	82	
Single pulse avalanche current	$L = 0.1$ mH	I_{AS}	30	
Single pulse avalanche energy		E_{AS}	45	
Maximum power dissipation ^a	$T_C = 25$ °C	P_D	7.1	W
	$T_C = 125$ °C		2.4	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to + 175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB mount ^b	R_{thJA}	85	°C/W
Junction-to-case (drain)		R_{thJF}	21	

Notes

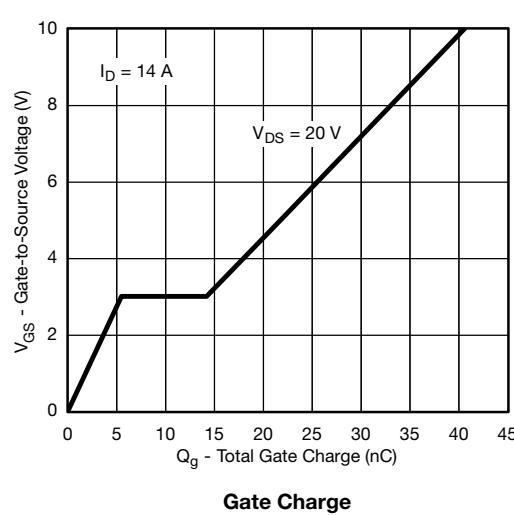
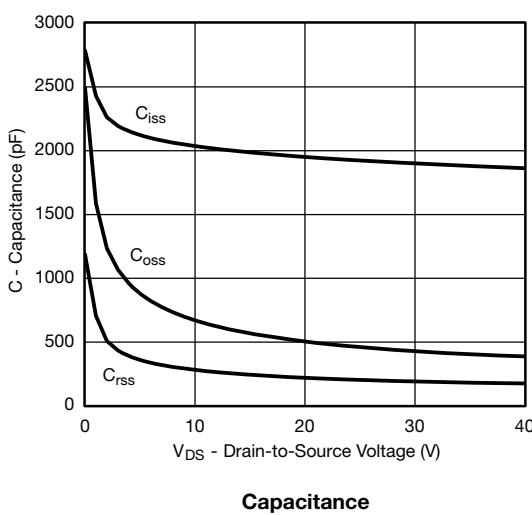
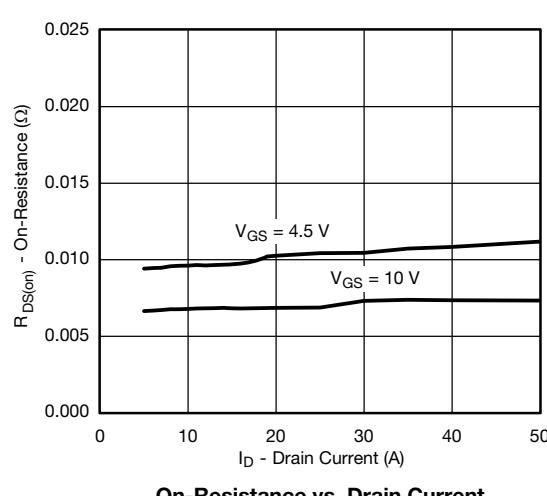
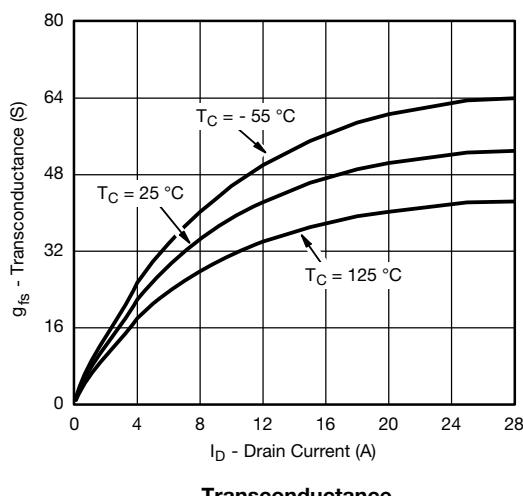
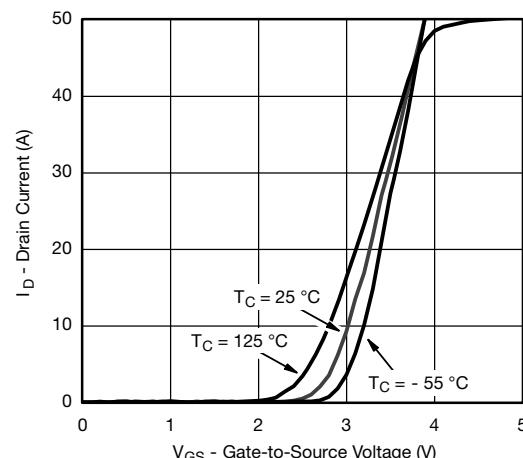
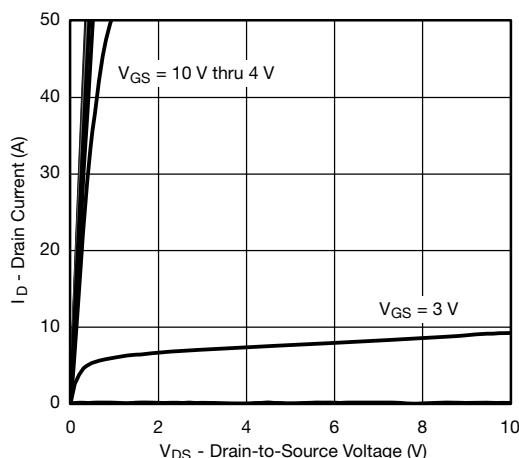
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %
- When mounted on 1" square PCB (FR-4 material)

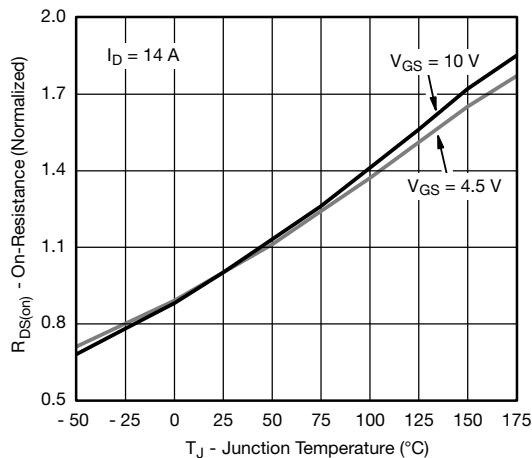
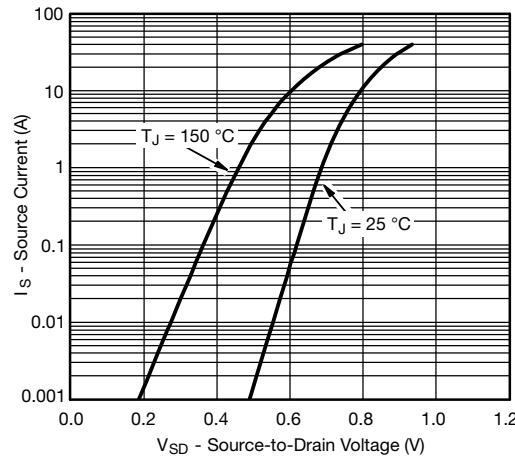
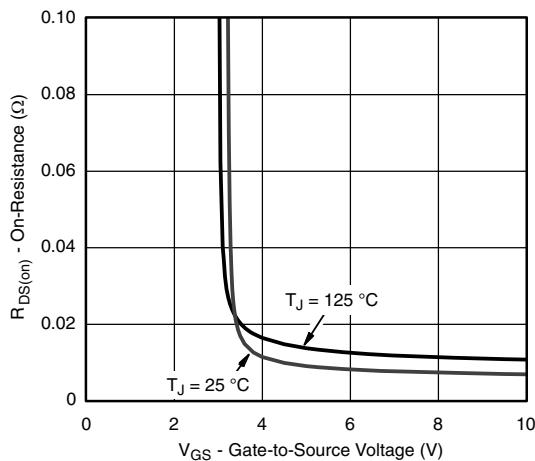
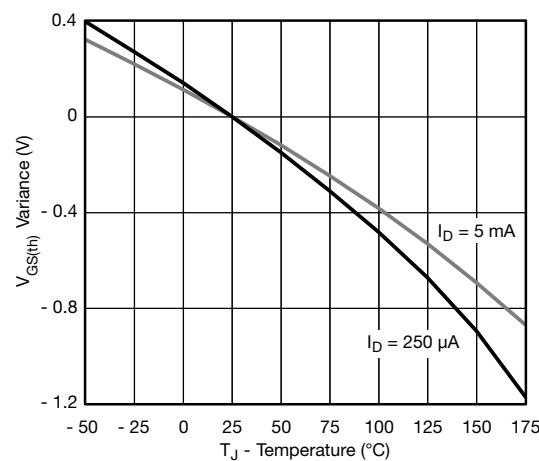
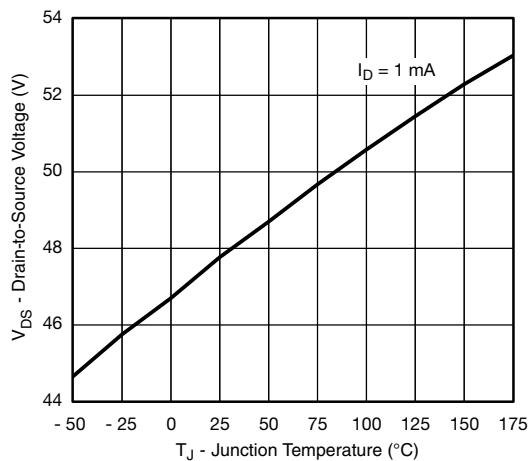
SPECIFICATIONS ($T_C = 25^\circ\text{C}$, unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0$, $I_D = 250 \mu\text{A}$		40	-	-	V	
Gate-source threshold voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$		1.5	2.0	2.5		
Gate-source leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero gate voltage drain current	I_{DSS}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 40 \text{ V}$	-	-	1.0	μA	
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 40 \text{ V}$, $T_J = 125^\circ\text{C}$	-	-	50		
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 40 \text{ V}$, $T_J = 175^\circ\text{C}$	-	-	150		
On-state drain current ^a	$I_{D(\text{on})}$	$V_{GS} = 10 \text{ V}$	$V_{DS} \geq 5 \text{ V}$	30	-	-	A	
Drain-source on-state resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$	$I_D = 14 \text{ A}$	-	0.0075	0.009	Ω	
		$V_{GS} = 10 \text{ V}$	$I_D = 14 \text{ A}$, $T_C = 125^\circ\text{C}$	-	-	0.014		
		$V_{GS} = 10 \text{ V}$	$I_D = 14 \text{ A}$, $T_C = 175^\circ\text{C}$	-	-	0.018		
		$V_{GS} = 4.5 \text{ V}$	$I_D = 12 \text{ A}$	-	0.010	0.012		
Forward transconductance ^b	g_{fs}	$V_{DS} = 15 \text{ V}$, $I_D = 14 \text{ A}$		-	45	-	S	
Dynamic^b								
Input capacitance	C_{iss}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 20 \text{ V}$, $f = 1 \text{ MHz}$	-	1950	2440	pF	
Output capacitance	C_{oss}			-	505	630		
Reverse transfer capacitance	C_{rss}			-	220	280		
Total gate charge ^c	Q_g	$V_{GS} = 10 \text{ V}$	$V_{DS} = 20 \text{ V}$, $I_D = 14 \text{ A}$	-	41	62	nC	
Gate-source charge ^c	Q_{gs}			-	5.5	-		
Gate-drain charge ^c	Q_{gd}			-	8.7	-		
Gate resistance	R_g	$f = 1 \text{ MHz}$		0.2	-	1.6	Ω	
Turn-on delay time ^c	$t_{d(\text{on})}$	$V_{DD} = 20 \text{ V}$, $R_L = 20 \Omega$ $I_D \geq 1 \text{ A}$, $V_{GEN} = 10 \text{ V}$, $R_g = 6 \Omega$		-	14	21	ns	
Rise time ^c	t_r			-	11	17		
Turn-off delay time ^c	$t_{d(\text{off})}$			-	45	68		
Fall time ^c	t_f			-	17	26		
Source-Drain Diode Ratings and Characteristics^b								
Pulsed current ^a	I_{SM}			-	-	82	A	
Forward voltage	V_{SD}	$I_F = 2.8 \text{ A}$, $V_{GS} = 0$		-	0.75	1.1	V	

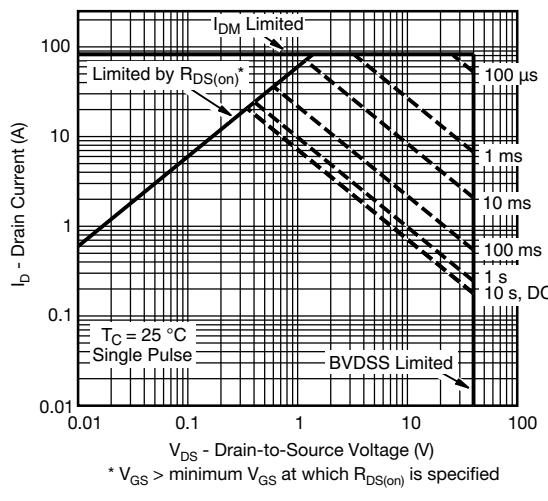
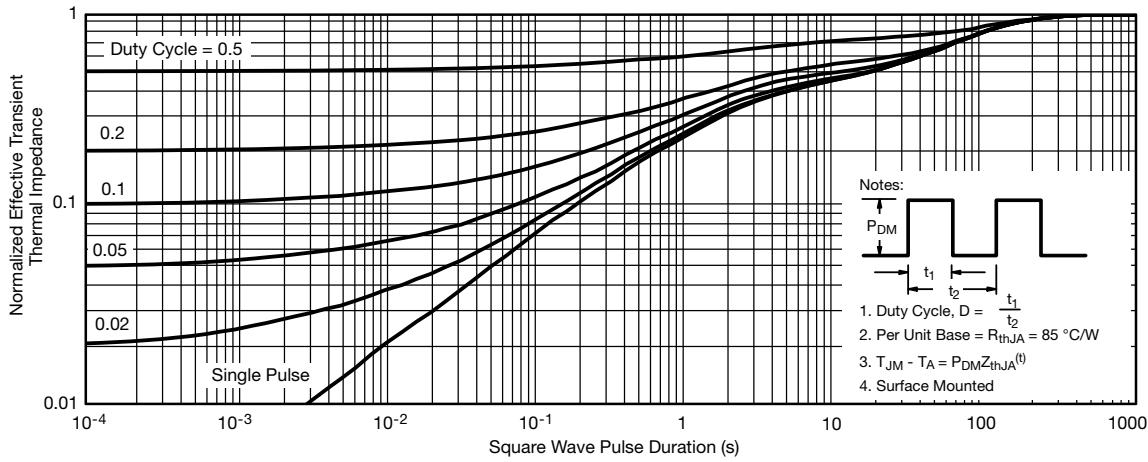
Notes

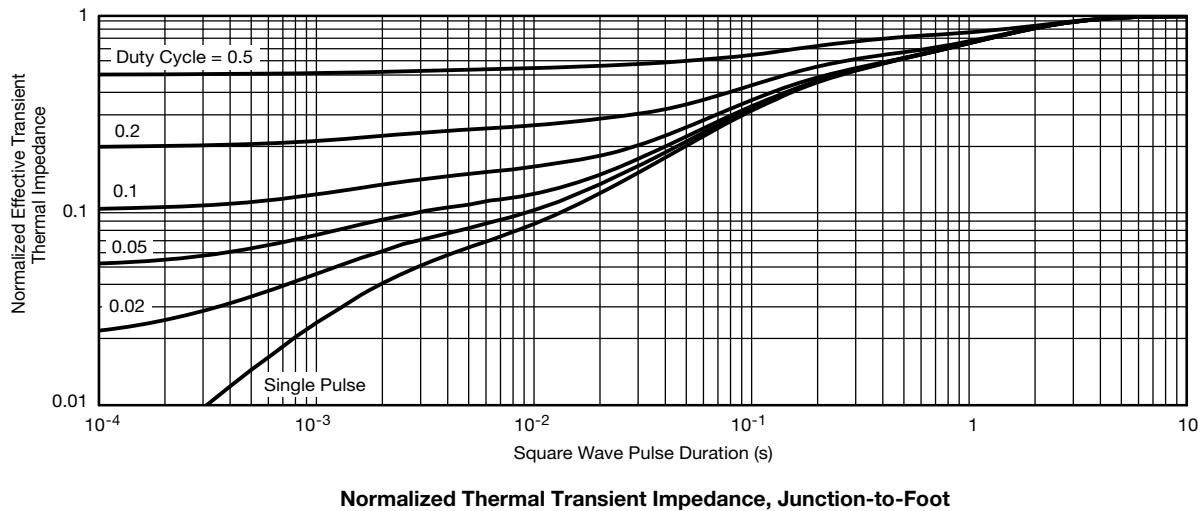
- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)


TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

On-Resistance vs. Junction Temperature

Source Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

Threshold Voltage

Drain Source Breakdown vs. Junction Temperature

THERMAL RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Safe Operating Area

Normalized Thermal Transient Impedance, Junction-to-Ambient

THERMAL RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Normalized Thermal Transient Impedance, Junction-to-Foot
Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25°C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25°C)

are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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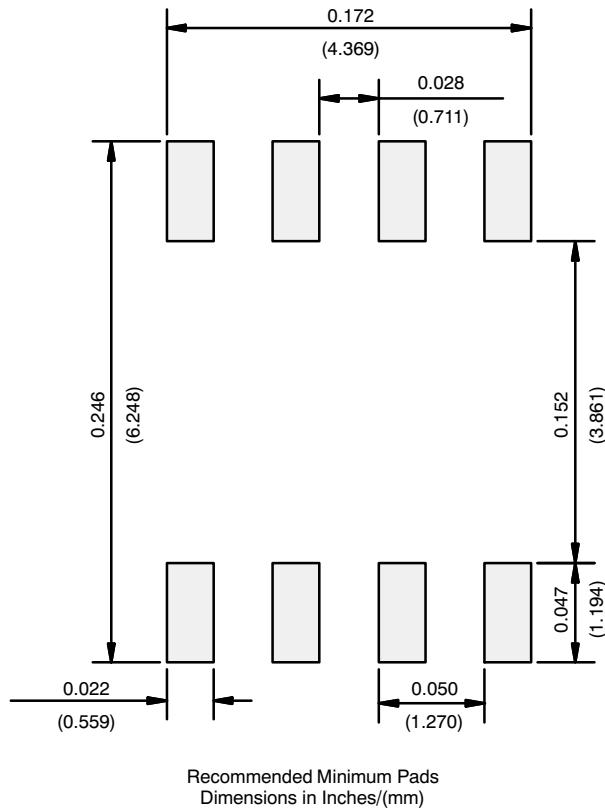
SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

RECOMMENDED MINIMUM PADS FOR SO-8



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