PrimeSTACK™

# 6PS18012E4FG38393



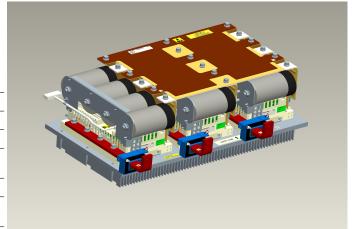
## **Preliminary data**

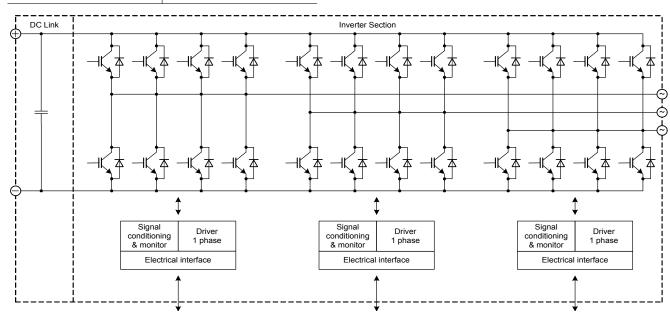
### **General information**

### IGBT Stack for typical voltages of up to 400 $V_{\text{RMS}}$ Rated output current 800 A<sub>RMS</sub>

- · Solar power
- · Motor drives
- · High power converter
- $\begin{array}{l} \cdot \ 62mm \ power \ module \\ \cdot \ Trenchstop^{\mathsf{TM}} \ IGBT4 \end{array}$

Topology	B6I
Application	Inverter
Load type	Resistive, inductive
Semiconductor (Inverter Section)	12x FF450R12KE4
DC Link	4.8 mF
Heatsink	Forced air cooled (fan not included)
Implemented sensors	Current, temperature
Driver signals IGBT	Electrical
Approvals	UL 508C
Sales - name	6PS18012E4FG38393
SP - No.	SP001054242





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### **Preliminary data**

Absolute maximum rated values

	I			
Collector-emitter voltage	IGBT; T <sub>vj</sub> = 25°C	V <sub>CES</sub>	1200	V
Repetitive peak reverse voltage	Diode; T <sub>vj</sub> = 25°C	V <sub>RRM</sub>	1200	V
DC link voltage		V <sub>DC</sub>	1000	V
Insulation management	according to installation height of 2000 m	V <sub>line</sub>	500	V <sub>RMS</sub>
Insulation test voltage	according to EN 50178, f = 50 Hz, t = 1 s	V <sub>ISOL</sub>	2.5	kV <sub>RMS</sub>
Repetitive peak collector current inverter section (IGBT)	$t_p = 1 \text{ ms}$	I <sub>CRM2</sub>	2560	А
Repetitive peak forward current inverter section (Diode)	$t_p = 1 \text{ ms}$	I <sub>FRM2</sub>	2440	А
Continuous current inverter section		I <sub>AC2</sub>	820	A <sub>RMS</sub>
Junction temperature	under switching conditions	T <sub>vjop</sub>	150	°C
Switching frequency inverter section	limited due to snubber caps	f <sub>sw2</sub>	3	kHz

### Notes

Further maximum ratings are specified in the following dedicated sections

### **Characteristic values**

DC Link			min.	typ.	max.	
Rated voltage		V <sub>DC</sub>		650	1000	V
Capacitor	1 s, 12 p, rated tol. 10 %	C <sub>DC</sub>		4.8		mF
Maximum ripple current	per device, T <sub>amb</sub> = 55 °C	I <sub>ripple</sub>			49	A <sub>RMS</sub>

### Notes

Activ clamping diodes not implemented, max. DC link voltage for short circuit protection 500V Max. DC link voltage under switching conditions 1000V up to 300A

Inverter Section			min.	typ.	max.	
Rated continuous current	$ \begin{vmatrix} V_{DC} = 650 \text{ V}, \ V_{AC} = 400 \ V_{RMS}, \ cos(\phi) = 0.85, \\ f_{AC \text{ sine}} = 50 \ Hz, \ f_{sw} = 3000 \ Hz, \ T_{inlet} = 40^{\circ}C, \ T_{j} \leq 125 \ ^{\circ}C $	I <sub>AC</sub>			800	ARMS
Continuous current at low frequency	$V_{DC}$ = 650 V, $f_{AC  sine}$ = 0 Hz, $f_{sw}$ = 3000 Hz, $T_{inlet}$ = 40 °C, $T_{j} \le$ 125 °C	I <sub>AC low</sub>			360	Arms
Rated continuous current for 150% overload capability	I <sub>AC 150%</sub> = 826 A <sub>RMS</sub> , t <sub>on over</sub> = 60 s, T <sub>j</sub> ≤ 125 °C	I <sub>AC over1</sub>			550	A <sub>RMS</sub>
Rated continuous current for 150% overload capability	$I_{AC\ 150\%}$ = 950 A <sub>RMS</sub> , $t_{on\ over}$ = 3 s, $T_{j} \le$ 125 °C	IAC over2			630	A <sub>RMS</sub>
Over current shutdown	within 15 μs	I <sub>AC OC</sub>		1790		A <sub>peak</sub>
Power losses	$I_{AC}$ = 400 A, $V_{DC}$ = 650 V, $cos(\phi)$ = 0.85, $f_{AC  sine}$ = 50 Hz, $f_{sw}$ = 3000 Hz, $T_{inlet}$ = 40 °C, $T_{j}$ ≤ 120 °C	P <sub>loss</sub>		5900		W

### Notes

Maximum junction temperature limited to 125°C under all operating conditions

### nverter Section (specific condition)

inverter Section (specin	c condition)		mın.	typ.	max.	
	$V_{DC} = 800 \text{ V}, V_{AC} = 440 \text{ V}_{RMS}, f_{AC \text{ sine}} = 50 \text{ Hz}, f_{sw} = 2667 \text{ Hz}, T_{inlet} = 40 ^{\circ}\text{C}, T_{j} \le 125 ^{\circ}\text{C}$	I <sub>ACsp</sub>		800		A <sub>RMS</sub>

### Notes

With optimized cooling condition higher load current is possible. Details see customized application note.

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### **Preliminary data**

### **Controller interface**

Driver and interface board	ref. to separate Application Note			DR240		
			min.	typ.	max.	
Auxiliary voltage		V <sub>aux</sub>	18	24	30	V
Auxiliary power requirement	V <sub>aux</sub> = 24 V	Paux			40	W
Digital input level	resistor to GND 10 k $\Omega$ , capacitor to GND 1 nF	V <sub>in low</sub>	0		4	V
		V <sub>in high</sub>	11		15	V
Digital output level	open collector, logic low = no fault, max. 15 mA	V <sub>out low</sub>	0		1.5	V
		V <sub>out high</sub>		15		V
Analog current sensor output inverter section	load max 5 mA, @ 800 A <sub>RMS</sub>	VIU ana2 VIV ana2 V <sub>IW</sub> ana2	4.3	4.4	4.5	V
Over temperature shutdown inverter section	load max 5 mA, @T <sub>NTC</sub> = 94 °C	V <sub>Error OT2</sub>		12.5		V

System data min. typ. max. according to IEC 61800-3 at named power  $V_{\text{Burst}}$ kV 2 **EMC** robustness interfaces control  $V_{\text{Burst}}$ 1 kV  $V_{\text{surge}}$ aux (24V) 1 kV  $^{\circ}\text{C}$ Storage temperature  $T_{\text{stor}}$ -40 80 Operational ambient PCB, DC link capacitor, bus bar, excluding cooling °C -25 60  $T_{\text{op amb}}$ temperature Cooling air velocity PCB, DC link capacitor, bus bar, standard atmosphere  $V_{\text{air}} \\$ 2 m/s Humidity no condensation Rel. F 0 85 % Vibration according to IEC 60721 m/s<sup>2</sup> according to IEC 60721 Shock m/s² Protection degree IP00 2 Pollution degree Dimensions 664 438 299 width x depth x height  $\mathsf{mm}$ Weight 53 kg

### Notes

System data valid for continuous operation

Heatsink air cooled			min.	typ.	max.	
Air flow	$T_{air}$ = 20 °C, $P_{air}$ = 1013 hPa, dry and dust free, measured at the side of the heat sink according to DIN 41882	ΔV/Δt	1500			m³/h
Air pressure drop	at min. air flow	Δр		200		Pa
Air inlet temperature		T <sub>inlet</sub>	-30		60	°C

### Notes

Conditions are standard Infineon characterization for heatsinks.

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## **Preliminary data**

Overview of optional components	Unit 1	Inverter Section	Unit 3
Parallel interface board			
Optical interface board			
Voltage sensor			
Current sensor		×	
Temperature sensor		×	
Temperature simulation			
DC link capacitors		×	
Data cable for control signals		×	
Fan			
Collector-emitter Active Clamping			
Snubber capcitors		×	

Notes

Datacable not specified for the STACK permitted temperature range. The included cables are standard computer cable.

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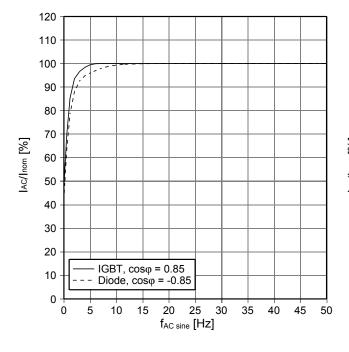
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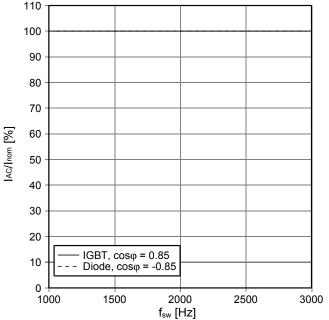


### **Preliminary data**

 $f_{\text{AC sine}}$  - derating curve IGBT (motor), Diode (generator)  $V_{\text{DC}}$  = 650 V,  $V_{\text{AC}}$  = 400  $V_{\text{RMS}},\,f_{\text{sw}}$  = 3 kHz,  $cos\phi$  =  $\pm0.85,\,$   $T_{\text{inlet}}$  = 40 °C and nom. cooling conditions

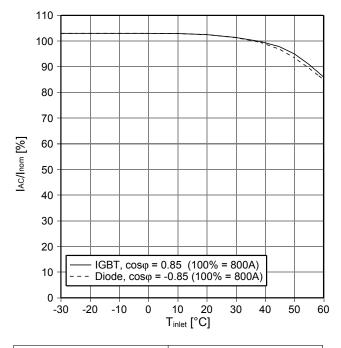
$$\begin{split} f_{\text{sw}} &- \text{derating curve IGBT (motor), Diode (generator)} \\ V_{\text{DC}} &= 650 \text{ V}, \text{ V}_{\text{AC}} = 400 \text{ V}_{\text{RMs}}, f_{\text{AC sine}} = 50 \text{ Hz}, \cos \phi = \pm 0.85, \\ T_{\text{inlet}} &= 40 \text{ °C and nom. cooling conditions} \end{split}$$

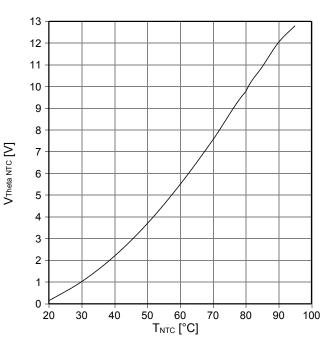




$$\begin{split} &T_{\text{inlet}} \text{- derating curve IGBT (motor), Diode (generator)} \\ &V_{\text{DC}} = 650 \text{ V}, \text{ V}_{\text{AC}} = 400 \text{ V}_{\text{RMS}}, f_{\text{AC sine}} = 50 \text{ Hz, } \cos\phi = \pm 0.85, \\ &T_{\text{inlet}} = 40 \text{ °C and nom. cooling conditions} \end{split}$$

Analog temperature sensor output  $V_{\text{Theta NTC}}$ Sensing NTC of heatsink





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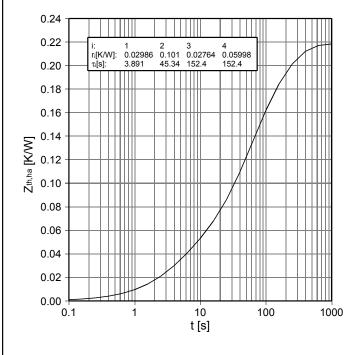
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## **Preliminary data**

 $Z_{\text{th,ha}} \text{ - thermal impedance heatsink to ambient per switch} \\ \text{nom. cooling conditions}$ 



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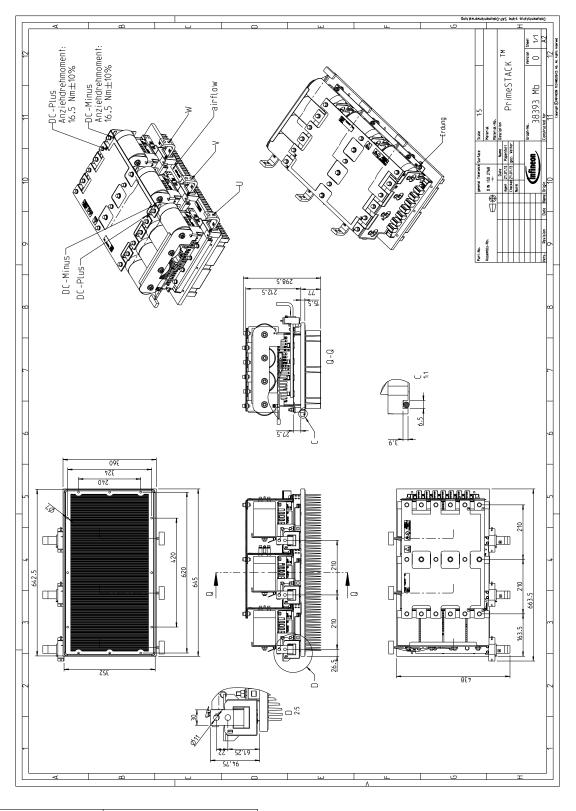
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# Preliminary data

# **Mechanical drawing**



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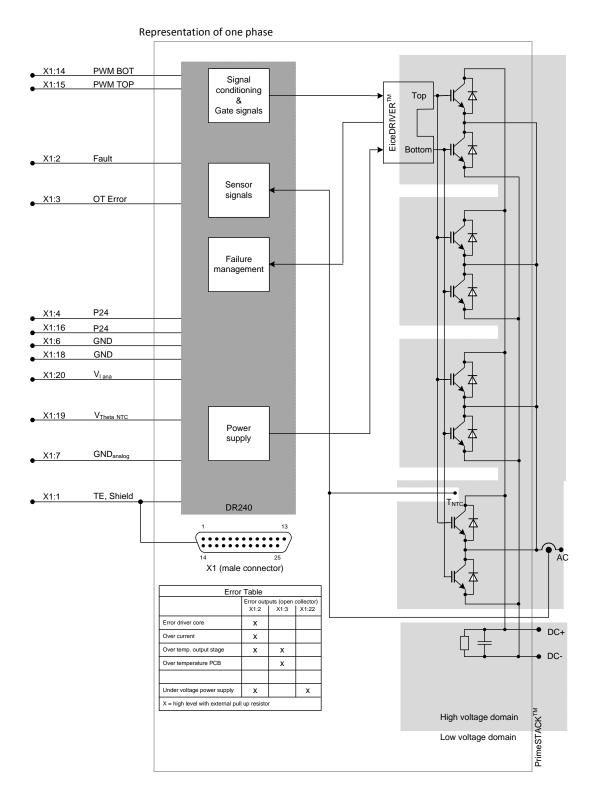
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## **Preliminary data**

# Circuit diagram



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### Preliminary data

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- the conclusion of Quality Agreements;
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Prior to installation and operation, all safety notices and warnings and all warning signs attached to the equipment have to be carefully read. Make sure that all warning signs remain in a legible condition and that missing or damaged signs are replaced. To installation and operation, all safety notices and warnings and all warning signs attached to the equipment have to be carefully read. Make sure that all warning signs remain in a legible condition and that missing or damaged signs are replaced.

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