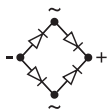


CBRLDSH2-40

**SURFACE MOUNT
HIGH DENSITY
2 AMP SILICON
SCHOTTKY BRIDGE RECTIFIER**



LPDIP CASE



www.centrasemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CBRLDSH2-40 is a full wave Schottky bridge rectifier mounted in a low profile epoxy surface mount molded case, utilizing glass passivated chips.

MARKING CODE: CL40

APPLICATIONS:

- Input rectification
- Polarity protection
- Steering diode array

FEATURES:

- Low leakage current (5.5 μ A TYP @ V_{RRM})
- High 2.0A current rating
- Low V_F Schottky diodes (480mV TYP @ $I_F=2.0A$)
- Low profile package (1.6mm MAX)

MAXIMUM RATINGS: ($T_A=25^{\circ}C$ unless otherwise noted)

Peak Repetitive Reverse Voltage

DC Blocking Voltage

RMS Reverse Voltage

Average Forward Current ($T_L=50^{\circ}C$)

Peak Forward Surge Current

Operating Junction Temperature

Storage Temperature

Thermal Resistance

SYMBOL

V_{RRM}

V_R

$V_{R(RMS)}$

I_O

I_{FSM}

T_J

T_{stg}

Θ_{JA}

40

40

28

2.0

50

-55 to +125

-55 to +150

95

UNITS

V

V

V

A

A

$^{\circ}C$

$^{\circ}C$

$^{\circ}C/W$

ELECTRICAL CHARACTERISTICS PER DIODE: ($T_A=25^{\circ}C$ unless otherwise noted)

SYMBOL **TEST CONDITIONS**

I_R $V_R=40V$

I_R $V_R=40V, T_A=100^{\circ}C$

V_F $I_F=2.0A$

C_J $V_R=4.0V, f=1.0MHz$

TYP

5.5

2.0

480

155

MAX

100

20

500

250

UNITS

μA

mA

mV

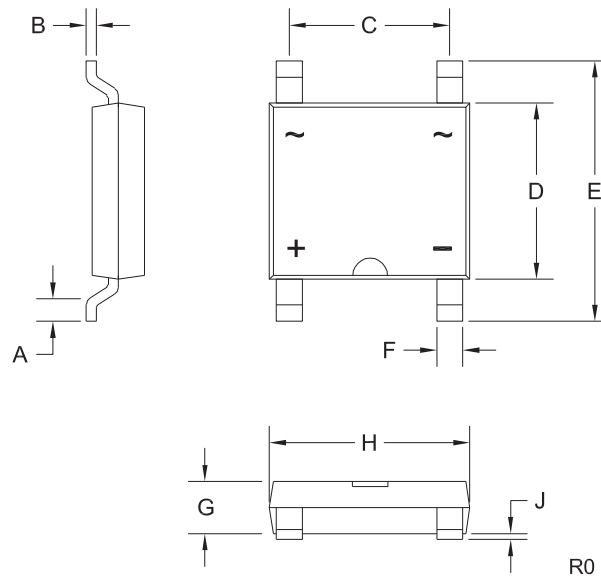
pF

R1 (26-November 2012)

CBRLDSH2-40
SURFACE MOUNT
HIGH DENSITY
2 AMP SILICON
SCHOTTKY BRIDGE RECTIFIER



LPDIP CASE - MECHANICAL OUTLINE



DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.019	0.028	0.50	0.70
B	0.007	0.012	0.20	0.30
C	0.153	0.162	3.90	4.10
D	0.167	0.179	4.25	4.55
E	0.248	0.264	6.30	6.70
F	0.023	0.028	0.60	0.70
G	0.045	0.058	1.15	1.45
H	0.190	0.203	4.85	5.15
J	0.002	0.006	0.05	0.15

LPDIP (REV: R0)

MARKING CODE: CL40

R1 (26-November 2012)

CBRLDSH2-40

SURFACE MOUNT

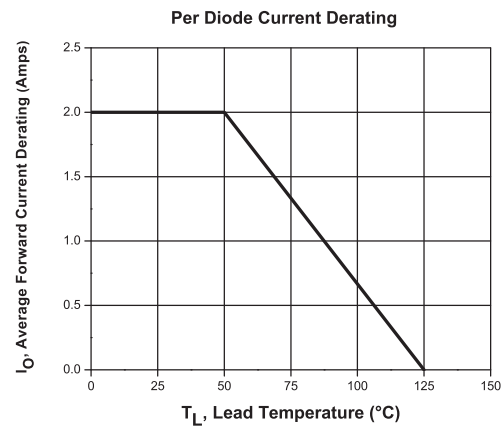
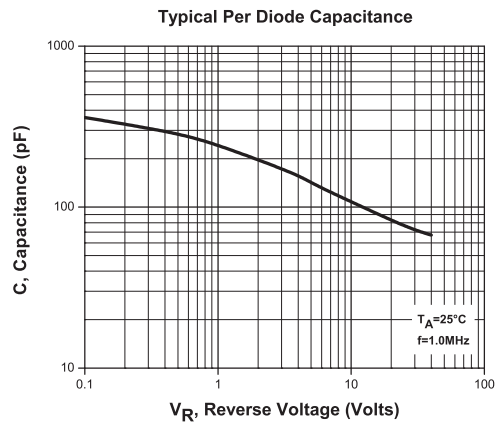
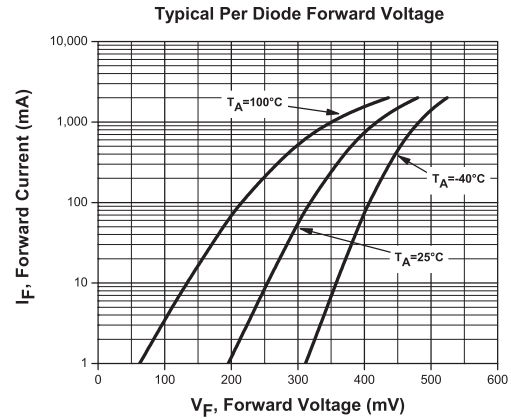
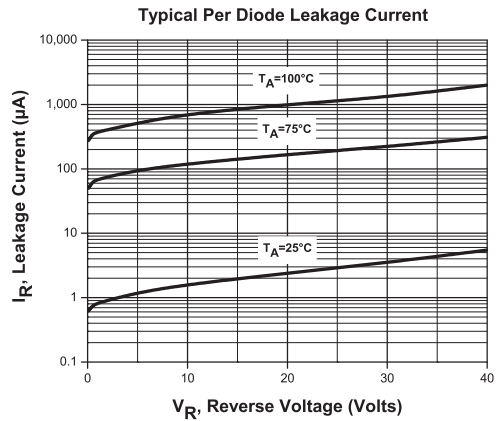
HIGH DENSITY

2 AMP SILICON

SCHOTTKY BRIDGE RECTIFIER



TYPICAL ELECTRICAL CHARACTERISTICS



R1 (26-November 2012)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix " TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp.
145 Adams Avenue
Hauppauge, NY 11788 USA
Main Tel: (631) 435-1110
Main Fax: (631) 435-1824
Support Team Fax: (631) 435-3388
www.centra-semi.com

Worldwide Field Representatives:
www.centra-semi.com/wwreps

Worldwide Distributors:
www.centra-semi.com/wwdistributors

For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: www.centra-semi.com/terms