



Product Change Notification / ALAN-15FPNL260

Date:

22-Dec-2021

Product Category:

General Purpose FPGAs, Radiation Tolerant FPGAs, System On Chip FPGAs

PCN Type:

Document Change

Notification Subject:

eSign# E000077600 Final Notice: Implement change of memory density from 4GB to 2GB on selected Microsemi M2GLxx, RT4Gxx and 5962-16xx FPGA device families available in various packages.

Affected CPNs:

[ALAN-15FPNL260_Affected_CPN_12222021.pdf](#)
[ALAN-15FPNL260_Affected_CPN_12222021.csv](#)

Notification Text:

PCN Status:Final Notification

PCN Type:Document Change

Microchip Parts Affected:Please open one of the files found in the Affected CPNs section.
Note: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change:Implement change of memory density from 4GB to 2GB on selected Microsemi M2GLxx, RT4Gxx and 5962-16xx FPGA device families available in various packages.

Pre and Post Change Summary:

	Pre Change	Post Change
Memory Density	4GB is originally documented in the RTG4, IGLOO2 and SmartFusion2	2GB will be documented in the RTG4, IGLOO2 and SmartFusion2 Product

Product briefs and the DDR Memory Controller User Guides as well as supported by Libero SoC software

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CN: RTG4, SmartFusion2, and IGLOO2 DDR Controller Memory Density Support can be view on the link below: <https://www.microchip.com/product-change-notifications/Data/ALAN-15FPNL260/RTG4,%20SmartFusion2,%20and%20IGLOO2%20DDR%20Controller%20Memory%20Density%20Support.pdf>

Impacts to Data Sheet:None

Change Impact:None

Reason for Change:To improve productivity by implementing change of memory density from 4GB to 2GB.

Change Implementation Status:In Progress

Estimated Implementation Date:

December 16, 2021 (date code: 2151)

Time Table Summary:

	December 2021				
Workweek	4 9	5 0	5 1	5 2	5 3
Final PCN Issue Date			X		
Estimated Implementation Date			X		

Method to Identify Change:Traceability code

Qualification Report:Not applicable

Revision History:

December 16, 2021: Issued final notification.

December 22, 2021: Re-issued final notification. Added "FPGA" on the notification subject.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachments:

[RTG4, SmartFusion2, and IGLOO2 DDR Controller Memory Density Support.pdf](#)

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Affected Catalog Part Numbers (CPN)

M2GL050-1FGG896
M2GL050-FGG896I
M2GL050-FGG896X377
M2GL050-FGG896X399
M2GL050-FGG896
M2GL050T-1FGG896IX417
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