

CBRLDSH2-100
SURFACE MOUNT
LOW PROFILE
2 AMP SILICON
SCHOTTKY BRIDGE RECTIFIER



www.centralemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CBRLDSH2-100 is a full wave Schottky bridge rectifier mounted in a low profile epoxy surface mount molded case, utilizing glass passivated chips.

MARKING CODE: CLP2

APPLICATIONS:

- Solid state lighting (SSL)
- DC-DC converters
- Polarity protection

FEATURES:

- Low leakage current (0.25 μ A TYP @ 100V)
- Low profile case (1.45mm MAX)
- Low V_F Schottky diodes (770mV TYP @ $I_F=2.0A$)

MAXIMUM RATINGS: ($T_A=25^\circ C$ unless otherwise noted)

Peak Repetitive Reverse Voltage

DC Blocking Voltage

RMS Reverse Voltage

Average Forward Current ($T_L=50^\circ C$)

Peak Forward Surge Current ($t_p=8.3ms$)

Operating Junction Temperature

Storage Temperature

Thermal Resistance (Note 1)

SYMBOL

V_{RRM}

V_R

$V_{R(RMS)}$

I_O

I_{FSM}

T_J

T_{stg}

θ_{JA}

100

100

70

2.0

50

-55 to +125

-55 to +150

95

UNITS

V

V

V

A

A

$^\circ C$

$^\circ C$

$^\circ C/W$

ELECTRICAL CHARACTERISTICS PER DIODE: ($T_A=25^\circ C$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_R	$V_R=100V$		0.25	4.0	μA
BV_R	$I_R=150\mu A$	100			V
V_F	$I_F=2.0A$		770	840	mV
C_J	$V_R=4.0V, f=1.0MHz$		110		pF

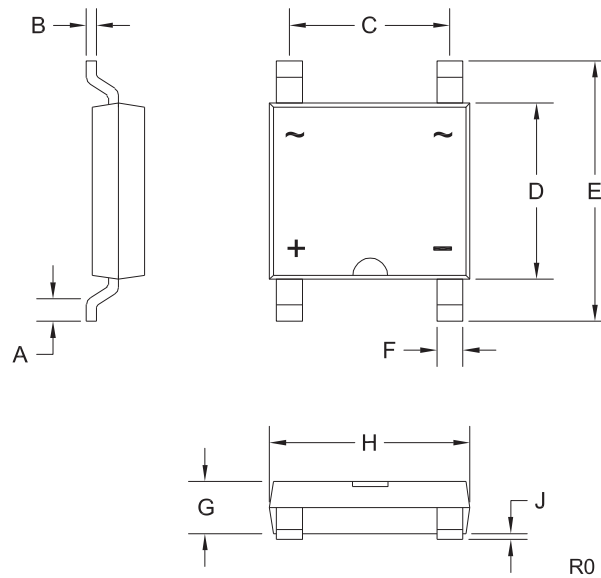
Notes: (1) FR-4 epoxy PCB with minimum copper pad area.

R3 (17-October 2012)

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LPDIP CASE - MECHANICAL OUTLINE



DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.019	0.028	0.50	0.70
B	0.007	0.012	0.20	0.30
C	0.153	0.162	3.90	4.10
D	0.167	0.179	4.25	4.55
E	0.248	0.264	6.30	6.70
F	0.023	0.028	0.60	0.70
G	0.045	0.058	1.15	1.45
H	0.190	0.203	4.85	5.15
J	0.002	0.006	0.05	0.15

LPDIP (REV: R0)

MARKING CODE: CLP2

R3 (17-October 2012)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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