# **DBE/DVE Series**

DC-DC Bus Converter

Up to 300W Digital Fully Regulated Intermediate

TNV standard for input Voltage with a 36–75Vin

specified Vin range and was designed to provide

payload power in distributed power architectures.

The DBE offers a "droop" load sharing option for

paralleling modules in the most demanding, power

hungry applications or to provide redundancy in high

reliability applications. The converter also offers high

for Power over Ethernet (PoE) applications. The DBE

series is suitable for applications covering MicroTCA,

servers and storage applications, networking equip-

ment, Power over Ethernet (PoE), fan trays, wireless

networks, wireless pre-amplifiers, industrial and test

equipment along with other applications requiring a

input to output isolation of 2250 VDC as required





Typical unit

#### **FEATURES**

- Advanced Bus Converter industry standard eighthbrick with digital PMBus interface
- Optional standard five pin Eighth-brick
- High efficiency
- Fast dynamic response
- ± 1% Vout Setting accuracy
- 2250 Vdc input to output isolation voltage
- Optional baseplate (B option)
- Voltage droop load sharing for parallel operation
- Certified to UL/IEC 62368-1, CAN/CSA-C22.2 No. 62368-1, safety approvals and EN55022/CISPR22 standards

Output (V)	Current (A)	Input (Vdc)
3.3	40	
5	40	V2 = 36-75Vdc
12	25	

#### **PRODUCT OVERVIEW**

Murata Power Solutions is introducing the first in a series of digitally controlled DC-DC converters that are based on a 32-bit ARM processor. The DBE series provides a fully regulated, digitally controlled DC output in an eighth-brick format that will support the evolving, Advanced Bus Converter (ABC) industry standard footprint for isolated board mounted power modules. The DBE series supports advances in power conversion technology including a digital interface supporting the PMBus protocol for communications to power modules.

The DBE series products are eighth-brick DC-DC converter with a digital interface supporting the PMBus standard for communicating with power devices. The DBE series also supports the

#### Power Management (PMBus Options)

- Configurable soft-start/stop
- Configurable output voltage (Vout) and voltage margins (Margin low and Margin high)
- Configurable protection limits for OVP, input over voltage, input under voltage, over current, on/off, and temperature
- Module Status monitor Vout, lout, Vin, Temp, Power good, and On/Off
- System status monitor (Vout, lout, Vin and Temp over time)

#### **Applications**

regulated 12V and 5V.

- Distributed power architectures
- Intermediate bus voltage applications
- Servers and storage applications
- Network equipment



www.murata-ps.com/support

### **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

PERFORMANC	E SPECIF	ICATIONS	SUMMAR	Y AND O	RDERINO	GUIDE ①									
		Output							Inj	put					
	Vout	Іоит	Total Power	Ripple ( mV	& Noise p-p)		Regulation (max.) $V_{III}$		Range	lın, min. load	lın, full load	Effic	iency		e Package ns (max.)
Root Model	(V)	(A, max)	(W)	Тур.	Max.	Line (%)	Load (%)	(V)	(V)	(mA)	(A)	Min.	Тур.	(Inches, max.)	(mm)
DBE0340V2	3.3	40	132	60	100	±0.3	±0.25	48	36-75	90	2.96	91%	92.5%	2.3x0.9x0.45	58.4x22.9x11.4
DBE0240V2	5	40	200	50	100	±0.25	±0.25	48	36-75	90	4.46	93%	94%	2.3x0.9x0.45	58.4x22.9x11.4
DBE0125V2	12	25	300	80	150	±0.25	±0.25	48	36-75	110	6.61	93%	94.6%	2.3x0.9x0.46	58.4x22.9x11.7
DVE0340V2 @	3.3	40	132	60	100	±0.3	±0.25	48	36-75	90	2.96	91%	92.5%	2.3x0.9x0.45	58.4x22.9x11.4
DVE0240V2 @	5	40	200	50	100	±0.25	±0.25	48	36-75	90	4.46	93%	94%	2.3x0.9x0.45	58.4x22.9x11.4
DVE0125V2 @	12	25	300	80	150	±0.25	±0.25	48	36-75	110	6.61	93%	94.6%	2.3x0.9x0.46	58.4x22.9x11.7

 $\odot$  Typical at TA = +25°C under nominal line voltage and full-load conditions. All models are specified with an external 1µF multi-layer ceramic and 10µF capacitors across their output pins.

DVE models do not have the PMBus feature.

#### DIGITAL CONTROL BRICK PART NUMBER FORMAT

Description	Par	rt N	uml	ber	Strı	ictu	re											Definition and Options
Product Family	D	X																DB = Full Featured Digital Bus converter, DV = NO PMBus, NO Sense & Trim Pins
Form Factor			E															E = Eighth Brick
Vout ①				0	1													01 = 12Vout, 02 = 5Vout, 03=3.3Vout (Without PMBus Vout cannot be changed)
Output Current						2	5											Max lout in Amps
Vin Range								۷	2									V2 = 36-75V
Logic ①										N								N = Negative, P = Positive
Pin Length ②											x							1 = 0.110" (cut), $2 = 0.145$ " (cut). Omit for standard shown in the mechanical drawings
Mechanical Configuration												В						B = Baseplate, Omit for Open Frame (Standard Configuration)
Load Sharing													S					S = Load Sharing, Omit for Standard (Standard Configuration) ④
Paste In Hole (PIH) Processable ③														R				R = MSL-3 compliant Packaging, Blank for Standard Configuration / Packaging
Specific Customer Configuration															x	x		Customer Code; Omit for Standard
RoHS																	C	RoHS 6/6 Compliant

#### **O PMBus Configurable**

② Minimum order quantity is required. Samples available with standard pin length only.

③ PIH parts are special order only.

Load Sharing is not available on the 3.3Vout and 5Vout models.

Note: Some model number combinations may not be available. See website or contact your local Murata sales representative.

#### **Part Number Examples**

DBE0125V2NBSC = Full Featured Digital 1/8th Brick, 12Vout, 25A, 36-75Vin, Negative logic, Baseplate, Load Sharing, RoHS 6/6 compliant DVE0240V2NBC = No PMBus, No Sense & Trim 1/8th Brick, 5Vout, 40A, 36-75Vin, Negative logic, Baseplate, RoHS 6/6 compliant

<b>EVALUATIO</b>	N BOARD AVAILABLE FROM MURATA POV	VER SOLUTIONS	
Part Number	Application Note	USB Adaptor Part Number	GUI Software Application Note
MPS-TD001	www.murata-ps.com/data/apnotes/dcan-63.pdf	MPS-AD001	www.murata-ps.com/data/apnotes/dcan-63.pdf

Contact your local Murata sales representative for ordering details.

## **DBE/DVE Series**

ABSOLUTE MAXIMUM RATINGS	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
nput Voltage, Continuous		0	TypicarNomman	80	Vdc
nput Voltage, Transient	100 mS max. duration	0		100	Vdc
solation Voltage	Input to output, continuous			2250	Vdc
nput Reverse Polarity	None, install external fuse		None	2200	Vuo
n/Off Remote Control	Power on, referred to -Vin	0	Nono	13.50	Vdc
Dutput Power		0		132	W
Dutput Current	Current-limited, no damage, short-circuit protected	0		40	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
• • •	of devices to greater than any of these conditions may adv		m reliability. Proper operation (		-
he Performance/Functional Specifications Table is		for soly anoor long ton	in reliability. I roper operation c		
NPUT					
Dperating voltage range (V2)		36	48	75	Vdc
	(Default Capfivia DMRLIC)	32	34	36	
Start-up threshold	(Default, Conf via PMBUS)				Vdc
Indervoltage shutdown	(Default, Conf via PMBUS)	30	32	34	Vdc
nternal Filter Type			Pi		_
external Input Fuse (Recommended)			10		A
nput current					
Full Load Conditions	Vin = nominal		2.96	3.02	A
Low Line input current	Vin = minimum		3.92	4.03	A
Inrush Transient	Vin = 48V.			0.05	A <sup>2</sup> -Sec.
Short Circuit input current			0.05	0.10	A
No Load input current	lout = minimum, unit=ON		90	120	mA
Shut-Down input current(Off, UV, OT)			20	40	
1 ( ) , )			20	40	mA
ENERAL and SAFETY					
fficiency	Vin=48V, full load	91	92.5		%
	Vin=min.	91	92		%
	Input to output, continuous		2250		Vdc
solation Voltage	Input to Baseplate, continuous		1500		Vdc
	Output to Baseplage, continuous		1500		Vdc
nsulation Safety Rating			Functional		
solation Resistance			10		MΩ
solation Capacitance			1000		pF
· · · · · · · · · · · · · · · · · · ·	Certified to UL/IEC 62368-1, CSA-C22.2				pr.
Safety	No.62368-1, IEC 60950-1, 2nd edition		Yes		
Calculated MTBF	Per Telcordia SR-332, issue 1, class 3, ground		2200		Hours x 10
	fixed, Tcase=+25°C		2200		TIOUIS X TO
OYNAMIC CHARACTERISTICS					
ixed Switching Frequency			180		KHz
furn on Time	(Default, Conf via PMBUS)			50	mS
	Vin On to Vout regulated				
Ramp-up time	Remote On to Vout regulated			30	mS
/out Rise Time	Conf via PMBUS, from 0% to 100%	9	15	20	mS
/out Fall Time	Conf via PMBUS, from 100% to 0%	7	10	13	mS
Dynamic Load Response	25-75-25% load step to 1% error band, 1A/uS,		100	200	µSec
	with 470uF output external Cap				
Dynamic Load Peak Deviation	same as above		500	700	mVdc
EATURES and OPTIONS					
	ng with an open collector logic, Voltages referenced	to -Vin)			
"P" suffix:			,		
Positive Logic, ON state	ON = pin open or external voltage	3.5		13.50	V
Positive Logic, OFF state	OFF = ground pin or external voltage	0		0.80	V
Control Current	open collector/drain		1	2	mA
					-1
"N" suffix:					
"N" suffix: Negative Logic ON state		-0.1		0.80	V
"N" suffix: Negative Logic, ON state Negative Logic, OFF state	ON = ground pin or external voltage OFF = pin open or external voltage	-0.1 3.5		0.80	V

# **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### FUNCTIONAL SPECIFICATIONS, DBE0340 (3.3V, 40A, CONT.)

OUTPUT	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Total Output Power		0	132	133.32	W
Voltage					
Setting Accuracy	At 100% load, no trim	3.27	3.3	3.33	Vdc
Overvoltage Protection	Conf via PMBUS, Magnetic Feedback	4.0	4.5	5.0	Vdc
Voltage Droop (Conf via PMBUS)			0		ΜΩ
Current					
Output Current Range		0	40	40	A
Minimum Load			No minimum load		
Current Limit Inception 2	90% of Vnom., after warmup, Conf via PMBUS	44	49	53	A
Short Circuit					
Short Circuit Current	Hiccup technique, autorecovery within 1% of Vout		2.5	3.0	A
Short Circuit Duration	Output shorted to ground, no damage		Continuous		
(remove short for recovery)					_
Short circuit protection method	Hiccup current limiting		Non-latching		
Regulation 3					
Line Regulation	Vin=min. to max., Vout=nom., full load			±0.3	%
Load Regulation	lout=min. to max., Vin=nom.			±0.25	%
Ripple and Noise	5 Hz- 20 MHz BW, Cout=1µF MLCC			100	mV pk-pk
Temperature Coefficient	paralleled with 10μF At all outputs		0.02		% of Vnom./°C
Output Capacitance	Low ESR	220		7500	μF
Power Good—Negative logic (Conf via PMBUS		-			
Power good high stage voltage		2.4		3.6	Vdc
Power good low stage voltage		-0.1		0.40	Vdc
Out voltage for power good off triggering	Conf via PMBUS	2	2.3	2.5	Vdc
Out Voltage for power good on triggering	Conf via PMBUS	2.6	2.8	3	Vdc
PMBus					
PMBUS GENERAL					
PMBUS REV. 1.2. SMBALERT# is supported. F	PEC is supported. Linear data format used.				
Bus speed				400	Khz
Logic high input		2		3.3	Vdc
Logic low input		-0.1		0.8	Vdc
Logic high output		2.4		3.6	Vdc
Logic low output		-0.1		0.4	Vdc
PMBUS ADDRESSING		-			
If the calculated PMBus address is 0d, 11d or resistor connected to GND.	or 12d, SAO or SA1 lefts open, default PMBus addre	ess 119d is assigned	instead. PMBUS address	= 8*SA0 + SA1. SA0/	SA1 value VS
0			10		kΩ
1			22		kΩ
2			33		kΩ
3			47		kΩ
4			68		kΩ
5			100		kΩ
6			150		kΩ
0			100		1122

# **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### FUNCTIONAL SPECIFICATIONS, DBE0340 (3.3V, 40A, CONT.)

PMBus	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
PMBus MONITORING ACCURACY		Miningin	rypical/Nominia	Maximum	Units
VIN READ		-2		2	%
VOUT_READ		-2		2	%
IOUT_READ (> = 10A)		-5		5	%
IOUT_READ (<10A)		-1		1	70 A
_ , ,					_
			M)	5	°C
	IS (PMBUS MONITORING & FUNCTION	AL DESCRIPTIO	N)		
ault Protection Specifications	Factory, default		4.5		N
Output Voltage, Over Voltage protection, OVP	Factory default		4.5		V
/OUT_OV_FAULT_LIMIT, Configurable via PMBus		4.0		5.0	V
	Fault response time		200		μS
nput Voltage, Input Over Voltage Protection	Factory default		80		V
	Setpoint accuracy	-2		2	%
/IN_OV_FAULT_LIMIT (Configurable via PMBus) ④	>VIN_OV_WARM_LIMIT		34	110	V
	Delay		300		μS
nput Voltage, Input Under Voltage Protection, UVLV	Factory default		32		V
	Setpoint accuracy	-2		2	%
/IN_FAULT_LIMIT_LIMIT(Configurable via PMBus) ④	<vin_uv_warm_limit <vin_ov_fault_limit<br=""><vin_off< td=""><td></td><td>32</td><td>75</td><td>V</td></vin_off<></vin_uv_warm_limit>		32	75	V
	Delay		300		μS
Ver Current Protection, OCP	Setpoint accuracy (lo)	-3		3	%
	(factory default)		48		A
OUT_OC_FAULT_LIMIT (Configurable via PMBus) ④	>IOUT_OC_WARN_LIMT	0		50	A
	Fault response time		200		μS
Over Temperature Protection, OTP	OT_FAULT_LIMIT (factory default)		125		0°
T_FAULT_LIMIT (Configurable via PMBus) ④	>OT_WARM_LIMIT	0		150	0°
	OTP accuracy (factory default)	5		5	°C
	Fault response time		300		μS
MECHANICAL (Through Hole Models)					
Dutline Dimensions—open frame			2.3x0.9x0.45		Inches
•	WxLxH		58.4x22.9x11.4		mm
Outline Dimensions—with baseplate			2.3x0.9x0.52		Inches
	WxLxH		58.4x22.9x13.2		mm
Weight	Open frame		34		Grams
	With baseplate		45		Grams
Chrough Hole Pin Diameter			0.04 & 0.062		Inches
			1.016 & 1.575		mm
Fhrough Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate		50		µ-inches
	Gold overplate		5		µ-inches
ENVIRONMENTAL					
Dperating Ambient Temperature Range	with derating	-40		85	°C
Derating Baseplate Temperature		-40		110	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown	Measured at hotspot		125		°C
Electromagnetic Interference	External filter required		В		
Conducted, EN55022/CISPR22					Class
RoHS rating			RoHS-6		

#### Notes

 $\odot$  Typical at TA = +25°C under nominal line voltage and full-load conditions. All models are specified with an external 1µF multi-layer ceramic and 10µF capacitors across their output pins.

③ Regulation specifications describe the output voltage changes as the line voltage or load current is varied from its nominal or midpoint value to either extreme.

 $\oslash \quad \mbox{Over-current protection is non-latching with auto recovery (Hiccup)}$ 

## **DBE/DVE Series**

ABSOLUTE MAXIMUM RATINGS	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
nput Voltage, Continuous		0	Typical/Nominal	80	Vdc
nput Voltage, Transient	100 mS max. duration	0		100	Vdc
solation Voltage	Input to output, continuous			2250	Vdc
nput Reverse Polarity	None, install external fuse		None	2230	Vuc
n/Off Remote Control	Power on, referred to -Vin	0	None	13.50	Vdc
Dutput Power		0		200	W
Dutput Current	Current-limited, no damage, short-circuit protected	0		40	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
	of devices to greater than any of these conditions may adv	versely affect long-terr	n reliability. Proper operation u	under conditions other th	han those listed
perating voltage range (V2)		36	48	75	Vdc
tart-up threshold	(Default, Conf via PMBUS)	32	34	36	Vdc
Indervoltage shutdown	(Default, Conf via PMBUS)	30	32	34	Vdc
nternal Filter Type			Pi		
External Input Fuse (Recommended)			10		A
nput current			10		11
Full Load Conditions	Vin = nominal		4.43	4.53	A
			5.94		
Low Line input current	Vin = minimum		0.94	6.03	A A2 Soo
Inrush Transient	Vin = 48V.		0.05	0.05	A <sup>2</sup> -Sec
Short Circuit input current			0.05	0.10	A
No Load input current	lout = minimum, unit=ON		90	120	mA
Shut-Down input currrent(Off, UV, OT)			20	40	mA
ENERAL and SAFETY					
fficiency	Vin=48V, full load	93	94		%
inclency	Vin=min.	93	93.5		%
	Input to output, continuous		2250		Vdc
solation Voltage	Input to Baseplate, continuous		1500		Vdc
-	Output to Baseplage, continuous		1500		Vdc
nsulation Safety Rating			Functional		
solation Resistance			10		ΜΩ
solation Capacitance			1000		pF
•	Certified to UL/IEC-62368-1, CSA-C22.2				pi
Safety	No.62368-1, IEC 60950-1, 2nd edition		Yes		
Calculated MTBF	Per Telcordia SR-332, issue 1, class 3, ground fixed, Tcase=+25°C		2200		Hours x 1
OYNAMIC CHARACTERISTICS	1000,1000-120 0				
ixed Switching Frequency			180		KHz
furn on Time	(Default, Conf via PMBUS)			EO	_
	Vin On to Vout regulated			50	mS
Ramp-up time	Remote On to Vout regulated			30	mS
/out Rise Time	Conf via PMBUS, from 0% to 100%	9	15	20	mS
out Fall Time	Conf via PMBUS, from 100% to 0%	7	10	13	mS
)ynamic Load Response	25-75-25% load step to 1% error band, 1A/uS,		100	200	µSec
Vynamic Load Peak Deviation	with 470uF output external Cap same as above		500	700	mVdc
EATURES and OPTIONS				100	mvdc
lemote On/Off Control		1- N=->			
	ng with an open collector logic, Voltages referenced	10 -VIN)			
"P" suffix:					
Positive Logic, ON state	ON = pin open or external voltage	3.5	_	13.50	V
Positive Logic, OFF state	OFF = ground pin or external voltage	0		0.80	V
0	open collector/drain		1	2	mA
Control Current					
"N" suffix:					
"N" suffix:	ON = ground pin or external voltage	-0.1		0.80	V
	ON = ground pin or external voltage OFF = pin open or external voltage	-0.1 3.5		0.80	V

# **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### FUNCTIONAL SPECIFICATIONS, DBE0240 (5V, 40A, CONT.)

OUTPUT	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Total Output Power		0	200	202	W
Voltage					
Setting Accuracy	At 100% load, no trim	4.95	5	5.05	Vdc
Overvoltage Protection	Conf via PMBUS, Magnetic Feedback	5.6	6	7	Vdc
Voltage Droop (Conf via PMBUS)			0		ΜΩ
Current					
Output Current Range		0	40	40	A
Minimum Load			No minimum load		
Current Limit Inception ②	90% of Vnom., after warmup, Conf via PMBUS	44	49	53	A
Short Circuit	·····, ······, ·······················				
Short Circuit Current	Hiccup technique, autorecovery within 1% of Vout		2.5	3.0	А
Short Circuit Duration					
(remove short for recovery)	Output shorted to ground, no damage		Continuous		
Short circuit protection method	Hiccup current limiting		Non-latching		
Regulation 3	1				
Line Regulation	Vin=min. to max., Vout=nom., full load			±0.3	%
Load Regulation	lout=min. to max., Vin=nom.			±0.25	%
Ripple and Noise	5 Hz- 20 MHz BW, Cout=1µF MLCC			100	mV pk-pk
Temperature Coefficient	<u>paralleled with 10μF</u> At all outputs		0.02		% of Vnom./°C
Output Capacitance	Low ESR	220	0.02	7500	μF
Power Good—Negative logic (Conf via PMBU		220			p.
Power good high stage voltage		2.4		3.6	Vdc
Power good low stage voltage		-0.1		0.40	Vdc
Out voltage for power good off triggering	Conf via PMBUS	3	3.5	4	Vdc
Out Voltage for power good on triggering	Conf via PMBUS	3.75	4.25	4.75	Vdc
PMBus		0.70	4.20	4.70	Vuc
PMBUS GENERAL					
PMBUS REV. 1.2. SMBALERT# is supported.	DEC is supported Linear data format used				
Bus speed	Lo is supported. Linear data format dised.			400	Khz
Logic high input		2		3.3	Vdc
Logic low input		-0.1		0.8	Vdc
Logic high output		2.4		3.6	Vdc
Logic low output PMBUS ADDRESSING		-0.1		0.4	Vdc
	10d CAO or CA1 lefts amon default DMDue addr	as 110d is assimula	Lineteed DMDUC eddress	0*040 . 041 040/	
resistor connected to GND.	or 12d, SAO or SA1 lefts open, default PMBus addre	ess i rou is assigned	i ilisteau. Pividuo auuress	= 0"3AU + 3A1. 3AU/	SAT Value VS
0			10		kΩ
1			22		kΩ
2	+		33		kΩ
3			47		kΩ
4			68		k0
			100		kΩ
6			150		kΩ
7			220		kΩ
1			220		N12

## **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### FUNCTIONAL SPECIFICATIONS, DBE0240 (5V, 40A, CONT.)

PMBus	Conditions $\mathbb O$	Minimum	Typical/Nominal	Maximum	Units
PMBus MONITORING ACCURACY			- Jorean Hommun		onneo
VIN READ		-2		2	%
VOUT_READ		-2		2	%
IOUT READ (> = 10A)		-5		5	%
IOUT_READ (<10A)		-1		1	A
_ , ,		-1		5	۸ °C
	S (PMBUS MONITORING & FUNCTION			0	-0
Fault Protection Specifications		AL DESUNIPTIO	N)		
Dutput Voltage, Over Voltage protection, OVP	Factory default		6		V
		F 0	0	7	
/OUT_OV_FAULT_LIMIT, Configurable via PMBus		5.6	000	7	V
	Fault response time		200		μS
nput Voltage, Input Over Voltage Protection	Factory default		80		V
	Setpoint accuracy	-2		2	%
/IN_OV_FAULT_LIMIT (Configurable via PMBus) ④	>VIN_OV_WARM_LIMIT		34	110	V
	Delay		300		μS
nput Voltage, Input Under Voltage Protection, UVLV	Factory default		32		V
	Setpoint accuracy	-2		2	%
/IN_FAULT_LIMIT_LIMIT(Configurable via PMBus) ④	<vin_uv_warm_limit <vin_ov_fault_limit<br=""><vin_off< td=""><td></td><td>32</td><td>75</td><td>V</td></vin_off<></vin_uv_warm_limit>		32	75	V
	Delay		300		μS
over Current Protection, OCP	Setpoint accuracy (lo)	-3		3	%
	(factory default)		48		A
OUT_OC_FAULT_LIMIT (Configurable via PMBus) ④	>IOUT_OC_WARN_LIMT	0		50	A
r mbus) 😌	Fault response time		200		μS
Over Temperature Protection, OTP	OT_FAULT_LIMIT (factory default)		125		°C
T_FAULT_LIMIT (Configurable via PMBus) ④	>OT_WARM_LIMIT	0	120	150	°C
	OTP accuracy (factory default)	5		5	0°C
	Fault response time	5	300	5	μS
MECHANICAL (Through Hole Models)	r aut response time		300		μο
Dutline Dimensions—open frame			2.3x0.9x0.45		Inches
	WxLxH		58.4x22.9x11.4		mm
Outline Dimensions—with baseplate	WALAIT		2.3x0.9x0.52		Inches
	WxLxH		58.4x22.9x13.2		mm
Veight	Open frame		34		Grams
weight					
	With baseplate		45		Grams
Through Hole Pin Diameter			0.04 & 0.062		Inches
			1.016 & 1.575		mm
Fhrough Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate Gold overplate		50		μ-inches μ-inches
ENVIRONMENTAL			0		μποποδ
Operating Ambient Temperature Range	with derating	-40		85	°C
Deperating Baseplate Temperature		-40		110	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown	Measured at hotspot		125	120	0 °C
Electromagnetic Interference					
Conducted, EN55022/CISPR22	External filter required		В		Class
RoHS rating			RoHS-6		

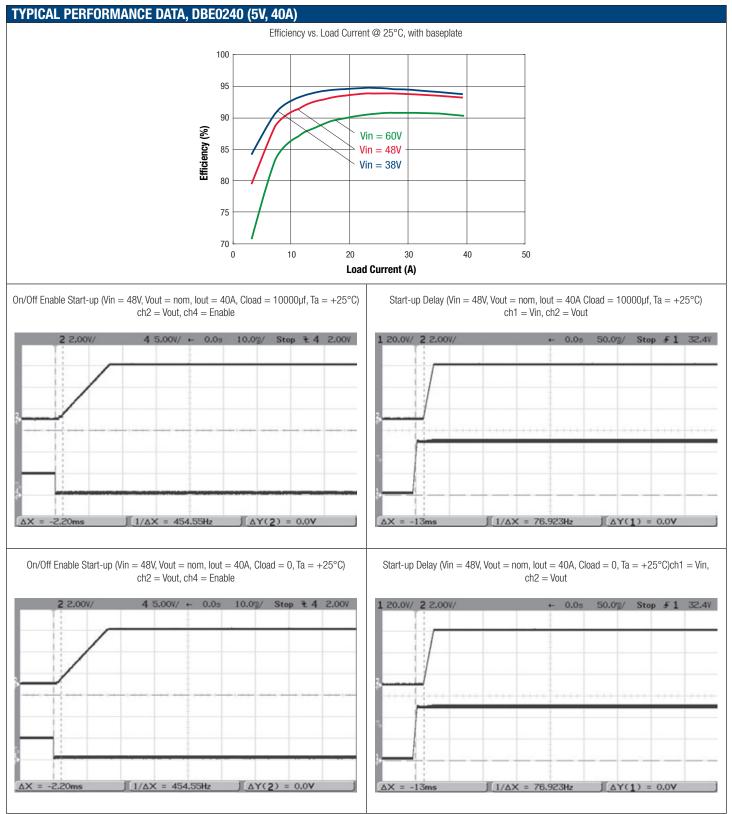
#### Notes

 $\odot$  Typical at TA = +25°C under nominal line voltage and full-load conditions. All models are specified with an external 1µF multi-layer ceramic and 10µF capacitors across their output pins.

③ Regulation specifications describe the output voltage changes as the line voltage or load current is varied from its nominal or midpoint value to either extreme.

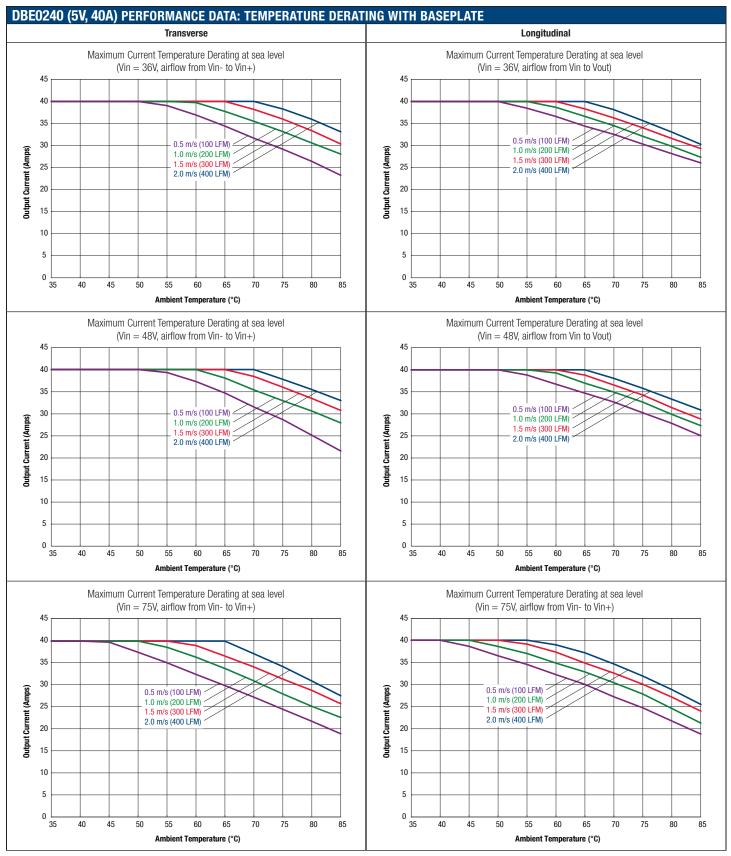
 $\oslash \quad \mbox{Over-current protection is non-latching with auto recovery (Hiccup)}$ 

# **DBE/DVE Series**



# **DBE/DVE Series**

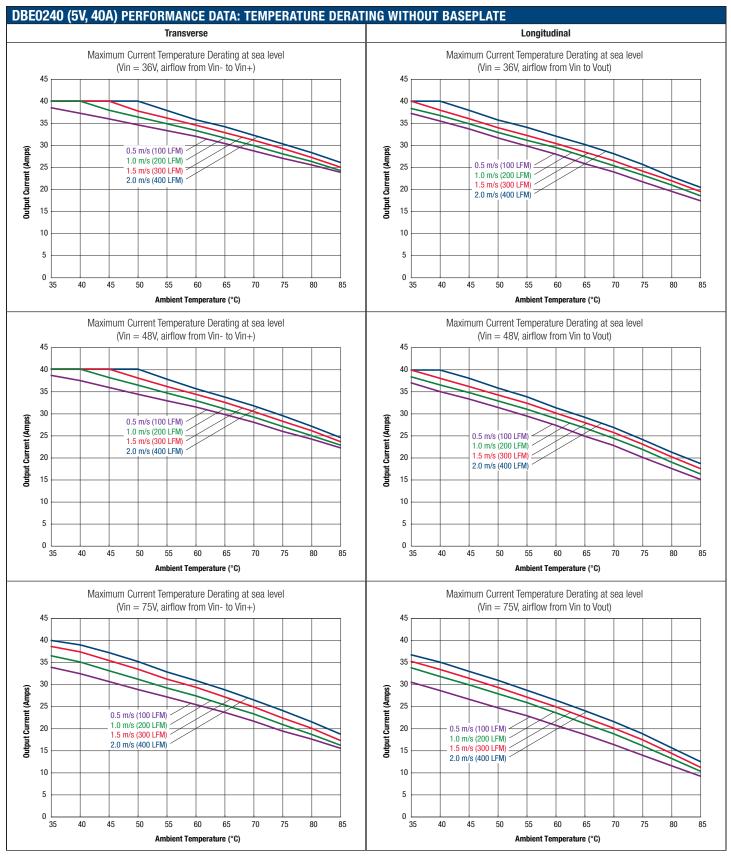
Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter



www.murata-ps.com/support

# **DBE/DVE Series**

Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter



www.murata-ps.com/support

## **DBE/DVE Series**

FUNCTIONAL SPECIFICATIONS, DBE			- · · · · · · · · · · · · · · · · · · ·	· · · · · · · · · · · · · · · · · · ·	
ABSOLUTE MAXIMUM RATINGS	Conditions $\mathbb O$	Minimum	Typical/Nominal	Maximum	Units
nput Voltage, Continuous		0		80	Vdc
Input Voltage, Transient	100 mS max. duration			100	Vdc
Isolation Voltage	Input to output, continuous			2250	Vdc
nput Reverse Polarity	None, install external fuse		None		
Dn/Off Remote Control	Power on, referred to -Vin	0		13.50	Vdc
Output Power		0		306	W
Dutput Current	Current-limited, no damage, short-circuit protected	0		25	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
Absolute maximums are stress ratings. Exposure o	f devices to greater than any of these conditions may a	adversely affect long-terr	m reliability. Proper operation	under conditions other th	an those listed
the Performance/Functional Specifications Table is	not implied nor recommended.				
INPUT					
Operating voltage range		36	48	75	Vdc
Start-up threshold	(Default, Conf via PMBUS)	32	34	36	Vdc
Jndervoltage shutdown	(Default, Conf via PMBUS)	30	32	34	Vdc
nternal Filter Type	(,,, note (1000)		Pi		
External Input fuse			20		A
nput Current	1	<u> </u>		1	
Full Load Conditions	Vin = nominal		6.61	6.85	A
Low Line input current	Vin = minimum		8.77	9.14	A
Inrush Transient	Vin = 48V.		0.11	0.05	A <sup>2</sup> -Sec.
Short Circuit input current	viii = 40v.		0.03	0.10	ASec.
No Load input current	lout = minimum, unit=ON		110	150	mA
Shut-Down input current (Off, UV, OT)			20	40	
Shut-Down input currrent (OII, OV, OI)	Measured at the input of module with a simulated		20	40	mA
	source impedance of 4.7µH, 220µF, 100V,				
Back Ripple Current	across source, 33µF, 100V external capacitors		2000	3000	mAp-p
	across input pins				
Back Ripple Current (no filtering)			620	1000	mAp-p
GENERAL and SAFETY					
	Vin=48V, full load	93	94.6		%
fficiency	Vin=min.	93	95		%
	Input to output, continuous		2250		Vdc
solation Voltage	Input to Baseplate, continuous		1500		Vdc
	Output to Baseplage, continuous		1500		Vdc
nsulation Safety Rating			Functional		Vuc
solation Resistance			10		ΜΩ
solation Capacitance			1500		pF
•	Certified to UL/IEC-62368-1, CSA-C22.2				рг
Safety			Yes		
	No.62368-1, IEC 60950-1, 2nd edition Per Telcordia SR-332, issue 1, class 3, ground		1000		
Calculated MTBF	fixed, Tcase=+25°C		1800		Hours x 10
DYNAMIC CHARACTERISTICS					
SWITCHING FREQUENCY (Configurable via			180		kHz
PMBUS)			100		NI IZ
URN ON TIME (Default, Configurable via PME	3US)				
URN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout)	3US)			60	ms
URN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout)	3US)			30	ms
URN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout) Vin Start up delay time	3US)		25	30 40	
URN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout) Vin Start up delay time Enable Start up delay time			25	30	ms
URN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout) Vin Start up delay time Enable Start up delay time			25	30 40	ms ms
URN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout) Vin Start up delay time Enable Start up delay time		16	25	30 40	ms ms
URN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout) Vin Start up delay time Enable Start up delay time /OUT RISE TIME (Default, Configurable via PM		16 12.8		30 40 10	ms ms ms
TURN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout) Vin Start up delay time Enable Start up delay time /OUT RISE TIME (Default, Configurable via PM From 0%~100% From 10%~90%	/BUS)		20	30 40 10 23	ms ms ms ms
TURN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout) Vin Start up delay time Enable Start up delay time /OUT RISE TIME (Default, Configurable via PM From 0%~100% From 10%~90%	/BUS)		20	30 40 10 23	ms ms ms ms
FURN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout) Vin Start up delay time Enable Start up delay time /OUT RISE TIME (Default, Configurable via PN From 0%~100% From 10%~90% /OUT FALL TIME OF REGULATED OFF (Default, From 100%~0%	/BUS)	12.8	20 16 20	30 40 10 23 18.4 23	ms ms ms ms ms
TURN ON TIME (Default, Configurable via PME Vin On to Vout Regulated (100% Vout) Remote On to Vout Regulated (100% Vout) Vin Start up delay time Enable Start up delay time VOUT RISE TIME (Default, Configurable via PN From 0%~100% From 10%~90% VOUT FALL TIME OF REGULATED OFF (Default,	/BUS)	12.8	20 16	30 40 10 23 18.4	ms ms ms ms ms

Conditions ①

Minimum

# **DBE/DVE Series**

Units

V

V

mΑ

V

V

mΑ

%

W

Vdc

Vdc MΩ

MΩ

А

А

А

mV

mV

mV pk-pk

% of Vnom./°C

μF

Vdc

Vdc

Vdc

Vdc

Khz

Vdc

Vdc

Vdc

Vdc

Maximum

#### Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

Typical/Nominal

Renue do n'Off Control"Pinary On/Off Control (designed to be drivina unita no pen construct avoltage3.613.6Positive Logic, Off stateOff = ground pin or external voltage00.8Control Currentopen construct avoltage0.12"P' sufficOff = ground pin or external voltage0.10.8Regative Logic, Off stateOff = ground pin or external voltage0.11Negative Logic, Off stateOff = ground pin or external voltage0.11Regative Logic, Off stateOff = ground pin or external voltage3.50.8Regative Logic, Off stateOff = ground pin or external voltage3.5110Control CurrentGene collector/drim112Remote Sense ComplianceSense pine connectif externally to respective0300300Off Uff UffControl Marking0112Statig AccurseA1 100% load, no trim11.761212.24Overvoltage ProtectionControl Markings, Magnetic Feedchick13.814.415.6Voltage Drong (Cont Va PMBUS)Without "S's affite0253Output artent Range0No minitum Mad23Current Logic Control CurrentBelog technique, autorcozeny swith 1% of Vot011Short Circuit Drotection methodHicup technique, autorcozeny swith 1% of Vot233Short Circuit DurationLogic technique, autorcozeny swith 1% of Vot014 <th>FEATURES and OPTIONS</th> <th>Conditions (1)</th> <th>Minimum</th> <th>Typical/Nominal</th> <th>Maximum</th>	FEATURES and OPTIONS	Conditions (1)	Minimum	Typical/Nominal	Maximum
mpP suffic       Positive Logic, OF state     ON = pin open or external voltage     3.5     13.5       Positive Logic, OF state     OFF = ground pin or external voltage     0     1     2       Way suffic     Image and the state     OFF = ground pin or external voltage     0.1     2       Negative Logic, OF state     OFF = ground pin or external voltage     0.1     1     2       Control Current     OFF = ground pin or external voltage     0.1     1     2       Control Current     OFF = ground pin or external voltage     0.1     1     2       Control Current     OFF = ground pin or external voltage     0.1     1     2       Control Current     Ome or external voltage     0.1     1     2       Control Current     Ome or external voltage     0     300     306       Ottrage     Dever of a pMRIDS. Mogenic Factback     13.8     14.4     15.6       Voltage Droop (Conf via PMBUS)     with %'s suffix     0     25       Minimum Load     Ome of via PMRIDS     27     29     31       Stort Circuit Current     Hocap technique, autorexony within % of volt     0.4     1       Stort Circuit Current     Output storter wetting     Non-latching     275       Stort Circuit Current     Hocap technique, autorexony within % of	Remote On/Off Control				
Pesitive Logic, ON state         ON = pin open or external voltage         3.5         13.5           Positive Logic, OFF state         OFF = ground pin or external voltage         0         0.8           Control Current         open collector/frain         1         2           "N" suffix:          0         0.8         0.8           Negative Logic, OFF state         OFF = pin open or external voltage         3.5         13.5           Control Current         open collector/drain         1         2           Remoto Sanse Compliance         Sanse prins connected external voltage         3.5         10           Ottrout         Voltage         0         300         306           Outrout         Voltage         0         300         306           Outrout         Voltage         0         300         306           Voltage Protection         Control Kennelly to respective         0         300           Voltage Droop (Cont via PMBUS)         without 'S' suffix         0         0           Output Current Range         0         No minimum tode         0           Current Ummu Load         0         Voltage Protection         25           Maitimu Load         0         Voltage Internative Stand to the t	Primary On/Off control (designed to be driving	g with an open collector logic, Voltages referenced	to -Vin)		
Positive Logic, OFF stateOFF = ground pin or external voltage010.8Control Currentopen callector/drain-0.112We gative Logic, OF stateON = ground pin or external voltage3.513.5Control Currentopen or external voltage3.513.5Control Currentopen or external voltage3.513.5Control Currentopen or external voltage3.510Currentopen or external voltage3.510Other StateOFF = pin open or external voltage3.513.5Control Currentopen callector/drain12Other StateO300306Other State0300306Other State	"P" suffix:				
Control Currentopen collector/drain12"N" suffix:ON = ground pin or external voltage-0.10.8Negative Logic, OF stateOF = pin open or external voltage3.513.5Control Currentopen collector/drain3.510Remote Sense ComplianceSense pins connected externally for respective Volt pins03.00OUTPUTVolt pins03.003006Outrage ProtectionCort via PMBLIS, Magnetic Feedback13.814.415.6Voltage ProtectionCort via PMBLIS, Magnetic Feedback13.814.415.6Voltage ProtectionCort via PMBLIS, Magnetic Feedback13.814.415.6Voltage Droop (Cort via PMBUS)wthoot "5" suffix00CurrentInternet Anage02523Stort Grout Userent Range00.411Current CurrentHocup technique, autorecovery within 1% of Vont0.41Stort Grout UserationOudput shoted to ground, no damageContinuous2729Stort Grout UserationOudput shoted to ground, no damageContinuous150Stort Grout Potection methodHicup technique, autorecovery within 1% of Vont0.41Stort Grout UserationOudput shoted to ground, no damageContinuous475Lad RegulationOudput shoted to ground, no damageContinuous475Lad RegulationOudput shoted to ground, no damageContinuous475Lad RegulationDud-mint, to	Positive Logic, ON state	ON = pin open or external voltage	3.5		13.5
"N" suffix:       ON = ground pin or external voltage       -0.1       0.8         Negative Logic, OF state       ON = ground pin or external voltage       -0.1       0.8         Negative Logic, OF state       OFF = pin pon or external voltage       3.5       11.2         Remote Sense Compliance       Sense pins connected demensity to respective       10         OUTPUT       Volt pins       0       300       306         Setting Accuracy       At 100% load, no trim       11.76       12       12.24         Overvoltage Protection       Conf via PMBUS, Magnetic Freeback       13.8       14.4       15.6         Voltage Droop (Conf via PMBUS)       without "S" suffix       0       0       0         Output Current Range       0       No minimum load       25       5         Short Circuit Durrent       9% of Vnom, after warmup, Conf via PMBUS       27       29       31         Short Circuit Durrent       Hiccup technique, autorecovery within 1% of Vout       0.4       1       0         Short Circuit Duration       Voltagin current limiting       Non katching       445         Reguitation @       SHice Prove P	Positive Logic, OFF state	OFF = ground pin or external voltage	0		0.8
Negative Logic, ON state         ON = ground pin or external voltage         -0.1         0.8           Negative Logic, OFF state         OFF = pin open or external voltage         3.5         13.5           Control Current         open collector/frain         1         2           Remote Sense Compliance         Volt pins         10         0           OUTPUT         Volt pins         0         300         306           Oversoltage Accuracy         At 10% load, no trim         11.76         12         12.24           Overvoltage Protection         Conf via PMBUS, Megnetic Feedback         13.8         14.4         15.6           Voltage Droop (Conf via PMBUS)         without "S" suffix         0         0         25           Minimu Load         0         0         25         Minimu Load         0         25           Current Limit Inception ©         99% of Vnom., after warmup, Conf via PMBUS         27         29         31           Short Circuit Buration         Hiccap technique, autorocovery within 1% of Vout         0.4         1         275           Short Circuit Buration         Hiccap technique, autorocovery within 1% of Vout         0.4         1         275           Short Circuit Dreation         Hiccap technique, autorocovery within 1% of Vout	Control Current	open collector/drain		1	2
Negative Logic, OFF state         OFF = pin open or extend voltage         3.5         13.5           Control Current         open collector/train         1         2           Remote Sense Compliance         Sense pins connected externally to respective Voltage         0         300         306           Voltage         0         300         306         300         306           Voltage         0         300         306         300         306           Voltage         Corr Via PMBUS, Magnetic Feedback         13.8         14.4         15.6           Overvoltage Protection         Corf Via PMBUS, Magnetic Feedback         13.8         14.4         15.6           Voltage Droop (Corf Via PMBUS)         within 'S' suffix         0         0         25           Minimum Load         0         QS         31         31           Short Circuit Current         Magnetic Area warmup, Cord via PMBUS         27         29         31           Short Circuit Duration         Quiput shorted to ground, no damage         Continuous         Continuous           Short Circuit Duration         Uniterpin to max, Vin=-non.         10.4         1           Carrent         Hickap textringue, autorecovery within 1% of Vout         0.4         1	"N" suffix:				
Control Current         open collector/drain         1         2           Remote Sense Compliance         Sense piris connected externally to respective Volt age         10         10           OUTPUT         Volt age         0         300         306           Voltage         0         300         306           Voltage         0         300         306           Voltage Protection         Corf via PMBUS, Magnetic Feedback         13.8         14.4         15.6           Voltage Droop (Corf via PMBUS)         with "S" suffix         0         0         25           Minimum Load         0         Moniminum load         0         25           Current         0         98% of Vnom., after warmup, Corf via PMBUS         27         29         31           Short Circuit Current         Hiccup technique, autorecovery within 1% of Vout         0.4         1         1           Short Circuit Current         Hiccup technique, autorecovery within 1% of Vout         0.4         1         1           Short Circuit protection method         Hiccup current limiting         Non-labching         ±75         Load Regulation 0         1         10           Line Regulation 0         Vin=min. to max, Vin=norn.         24         80         150	Negative Logic, ON state	ON = ground pin or external voltage	-0.1		0.8
Remote Sense Compliance         Sense pins connected externally to respective Voit pins         Image: Control of Contro of Control of	Negative Logic, OFF state	OFF = pin open or external voltage	3.5		13.5
Netherite series complianceVout pinsVout pinsIOUTPUTOUTPUTSetting Accuracy0300306VoltageSetting AccuracyAt 100% load, no trim11.761212.24Overvoltage ProtectionCord via PMBUS, Magnetic Feetback13.814.415.6Voltage Droop (Conf via PMBUS)without "S" suffix00Current025Output Current Range0025Minimu Load025Current Limit Inception 2098% of Vnom., after warmup, Conf via PMBUS272931Short Circuit DurationOutput shorted to ground, no damageContinuous11Short Circuit DurationOutput shorted to ground, no damageContinuous-27Regulation ©Vin=min. to max, Vout=nom.23636Temperature CoefficientMinimus0.010.02445Ripple and Noise5 H2- 20 MH2 BW, Coat=1 µF NLCC paralified with 10µF80150Power good Jost cip (Conf via PMBUS0.010.0210.000Power good low stage voltageConf via PMBUS8.649.610.56Power good on triggeringConf via PMBUS8.649.610.56Power good on triggeringConf via PMBUS8.649.610.56PMBUS REV. 1.2. SMALETH# is supported. Linear data format used.4000.83.3Logic high input000.83.33.3Logic hi	Control Current			1	2
OUTPUT         Image: Control of the superior	Remote Sense Compliance				10
VoltageSetting AccuracyAt 100% load, no trim11.761212.24Overvoltage ProtectionCont via PMBUS, Magnetic Feedback13.814.415.6Voltage Droop (Conf via PMBUS)with "S" suffix00Current0100Current Limit Inception ①99% of Vnom., after warmup, Conf via PMBUS272931Short Circuit Current Limit Inception ①99% of Vnom., after warmup, Conf via PMBUS272931Short Circuit CurrentHiccup technique, autorecovery within 1% of Vout0.411Short Circuit CurrentHiccup technique, autorecovery within 1% of Vout0.411Short Circuit CurrentHiccup technique, autorecovery within 1% of Vout0.411Short Circuit Duration (remove short for recovery)Output shorted to ground, no damageContinuous*********************************	OUTPUT				
Setting AccuracyAt 100% load, no trim11.761212.24Overvoltage ProtectionCont via PMBUS, Magnetic Feedback13.814.415.6Voltage Droop (Conf via PMBUS)without "S" suffix00Current0025Minimum Load025Current Limit Inception @98% of Vnom., after warmup, Conf via PMBUS272931Short Circuit00.411Short Circuit CurrentHiccup technique, autorecovery within 1% of Vout0.41Short Circuit DurationOutput shorted to ground, no damageContinuousShort Circuit protection methodHiccup current limitingNon-latchingShort Circuit protection methodVine-min. to max, Vuit=nom., full load±75Load Regulation @Vine-min. to max, Vuit=nom.±45Ripple and Noise5 Hz- 20 MHz BW, Cout=1µF MLCC paralled with 10µF80150Temperature CoefficientAt all outputs0.010.02Maximum Output CapacitanceLow ESR1000.010.02Power good high stage voltageConf via PMBUS9.7210.811.88PMBUS REV.PMBUS9.7210.811.88PMBUS BEV.PMBUS9.7210.811.88PMBUS REV.Low ESR9.7210.811.88PMBUS Grower Good ntrigeringConf via PMBUS9.7210.811.88PMBUS REV.Low ESR9.7210.811.88PMBUS Grower Good ntrigering <td>Total Output Power</td> <td></td> <td>0</td> <td>300</td> <td>306</td>	Total Output Power		0	300	306
Overvoltage Protection         Conf via PMBUS, Magnetic Feedback         13.8         14.4         15.6           Voltage Droop (Conf via PMBUS)         without "S" suffix         0         0         0           Current         00         0         25         0	Voltage				
Woltage Droop (Conf via PMBUS)         without "S" suffix         0           Outry Current Range         0         10           Outry Current Range         0         25           Minimum Load         0         27         29         31           Short Circuit         99% of Vnom., after warmup, Conf via PMBUS         27         29         31           Short Circuit Current         Hiccup technique, autorecovery within 1% of Vout         0.4         1           Short Circuit Duration         Output shorted to ground, no damage         Continuous         Continuous           Short Circuit Duration         Output shorted to ground, no damage         Continuous         ±75           Short Circuit Duration         Output shorted to ground, no damage         Continuous         ±75           Short Circuit Duration         Output shorted to ground, no damage         Continuous         ±75           Short Circuit Duration         Iniemin. to max, Vout=nom., full load         ±75         Load Regulation         ±45           Short Circuit Duration         Iout=min. to max, Vin=nom.         10.0         ±45           Ipple and Noise         5 H2- 20 MHz BW, Cout=1µF MLCC         80         150           paralleled with 10µF         0.01         0.02         Maximum Output Capacitance	Setting Accuracy	At 100% load, no trim	11.76	12	12.24
Voltage Droop (Conf via PMBUS)Init "S" suffix10Current025Output Current Range00Minimum Load0No minimum loadCurrent Limit Inception @98% of Vnom., after warmup, Conf via PMBUS272931Short Circuit CurrentHiccup technique, autorecovery within 1% of Vout0.41Short Circuit Duration (remove short for recovery)Output shorted to ground, no damageContinuousShort Circuit Duration (remove short for recovery)Output shorted to ground, no damageNon-latchingRegulation ③Hiccup current limitingNon-latching±75Line Regulation ③Uin=min. to max, Vout=nom., full load±75±45Load Regulation ③Istar 20 MHz BW, Cout=TuFF MLCC paralleled with 10µF80150Temperature CoefficientAt all outputs0.010.02Maximum Output CapacitanceLow ESR10010,000Power good high stage voltage-0.10.40.4Out Voltage for power good on triggering Out voltage for power good on triggeringConf via PMBUS9.7210.811.88PMBUS SENERALPMBUS SENERAL23.33.33.3PMBUS GLERAL000.83.33.6Ling Ingin Ling Ling Ling Ling Ling Ling Ling	Overvoltage Protection	Conf via PMBUS, Magnetic Feedback	13.8	14.4	15.6
CurrentInterfereInterfereCurrent Range0025Minimum Load0No minimum loadCurrent Limit Inception @98% of Vnom., after warmup, Conf via PMBUS272931Short Circuit98% of Vnom., after warmup, Conf via PMBUS272931Short Circuit CurrentHiccup technique, autorecovery within 1% of Vout0.41Short Circuit DurationOutput shorted to ground, no damageContinuousShort circuit protection methodHiccup current limitingNon-latchingRegulation @Vin=min. to max, Vout=nom., full load445Ripple and Noise5 Hz- 20 MHz BW, Cout=1µF MLCC paralleded with 10µF80150Temperature CoefficientA tal dictputs0.010.02Maximum Output CapacitanceLow ESR1000.010.02Power good oft stage voltageCont via PMBUS3.63.6Power good oft stage voltageCont via PMBUS9.7210.811.88PMBUS EVENENTVia PMBUS9.7210.811.88PMBUS REV. 1.2. SMBALERT# is supported. EUC is supported. Linear data format used.9.7210.811.83PMBUS REV. 1.2. SMBALERT# is supported. EUC is supported. Linear data format used.23.33.3Logic loigh inputQQ3.33.33.3Loigb Lingh Lin	Valtara Draan (Canfuia DMDUC)	without "S" suffix		0	
Output Current Range025Minimum LoadNo minimum load25Gurrent Limit Inception ③98% of Vnom., after warmup, Conf via PMBUS272931Short Circuit CurrentHiccup technique, autorecovery within 1% of Vout0.41Short Circuit Duration (remove short for recovery)Output shorted to ground, no damageContinuousShort Circuit protection methodHiccup current limitingNon-latchingRegulation ③Vin=min. to max, Vout=nom., full load±75Line RegulationVin=min. to max, Vout=nom., full load±45Ripple and Noise5 H2-20 MH2 BW, Cout=1µF MLCC paralleled with 10µF80150Power Good-Negative Logic (Conf via PMBUS)0.010.020.02Maximum Output CapacitanceLow ESR1000.010.02Power good low stage voltageConf via PMBUS8.649.610.56Out Voltage for power good on triggering Conf via PMBUS9.7210.811.88PMBUS GENERALPMBUS GENERAL23.33.3PMBUS GENERALConf via PMBUS9.7210.811.88PMBUS GENERAL000.83.63.3Logic kingh input000.83.63.6Logic kingh input000.83.63.6Logic kingh input000.83.63.6Logic kingh input000.83.63.6Logic kingh input00.83.63.6 <td< td=""><td>Voltage Droop (Cont via PMBUS)</td><td>with "S" suffix</td><td></td><td>10</td><td></td></td<>	Voltage Droop (Cont via PMBUS)	with "S" suffix		10	
Minimum Load       No minimum load         Current Limit Inception ②       98% of Vnom., after warmup, Conf via PMBUS       27       29       31         Short Circuit Current       Hiccup technique, autorecovery within 1% of Vout       0.4       1         Short Circuit Duration (remove short for recovery)       Output shorted to ground, no damage       Continuous	Current				
Current Limit Inception ②98% of Vnom., after warmup, Conf via PMBUS272931Short CircuitShort Circuit CurrentHiccup technique, autorecovery within 1% of Vout0.41Short Circuit Duration (remove short for recovery)Output shorted to ground, no damageContinuousShort Circuit protection methodHiccup current limitingNon-latchingRegulation ③Vin=min. to max., Vout=nom., full load±75Load Regulationlout=min. to max., Vout=nom., full load±75Load Regulationlout=min. to max., Vin=nom.80150Ripple and Noise5 Hz- 20 MHz BW, Cout=1µF MLCC paralleled with 10µF80150Temperature CoefficientAt all outputs0.010.02Maximum Output CapacitanceLow ESR10010,000Power good high stage voltageConf via PMBUS8.649.610.56Out voltage for power good on triggeringConf via PMBUS8.649.610.56Out voltage for power good on triggeringConf via PMBUS9.7210.811.88PMBUSEVENEXEVENEXEVENEX11.88PMBUS REV. 1.2. SMBALERT# is supported. Linear data format used.23.33.3Logic high nuptQQ3.33.33.3Logic bigh inputConf via PMBUS23.33.3Logic high nuptQQ00.83.6	Output Current Range		0		25
Short Circuit       Hiccup technique, autorecovery within 1% of Vout       0.4       1         Short Circuit Duration (remove short for recovery)       Output shorted to ground, no damage       Continuous         Short Circuit precovery       Output shorted to ground, no damage       Continuous         Regulation ③       Non-latching         Line Regulation ③       Vin=min. to max., Vin=nom.       ±75         Load Regulation       Imax., Vin=nom.       ±80       150         Ripple and Noise       5 HZ- 20 MHz BW, Cout=1µF MLCC paralleled with 10µF       80       150         Reperature Coefficient       At all outputs       0.01       0.02         Maximum Output Capacitance       Low KSR       100       0.01       0.00         Power good high stage voltage       Conf via PMBUS       8.64       9.6       10.56         Out voltage for power good on triggering       Conf via PMBUS       8.64       9.6       10.56         PMBUS GENERAL       PMBUS GENERAL       PMBUS GENERAL       4.00       4.00       3.3         Isogering       Conf via PMBUS       2.4       3.3       3.3         Logic high input       Que       Que       3.3       3.3         Logic high input       Que       Que       3.3       3.3 <td>Minimum Load</td> <td></td> <td></td> <td>No minimum load</td> <td></td>	Minimum Load			No minimum load	
Short Circuit CurrentHiccup technique, autorecovery within 1% of Vout0.41Short Circuit Duration (remove short for recovery)Output shorted to ground, no damageContinuousShort Circuit protection methodHiccup current limitingNon-latchingRegulation ③Vin=min. to max, Vout=nom, full loadNon-latchingLine Regulation ③Vin=min. to max, Vout=nom, full load1Line Regulation ③Vin=min. to max, Vout=nom, full load2.75Load Regulation ③Vin=min. to max, Vout=nom, full load80150Ripple and Noise5 HZ- 20 MHz BW, Cout=1µF MLCC paralled with 10µF80150Temperature CoefficientAt all outputs0.010.02Maximum Output CapacitanceLow ESR1000.010.02Power good high stage voltageCont via PMBUS8.649.610.56Out Voltage for power good oft triggeringCont via PMBUS8.649.610.56Out Voltage for power good on triggeringCont via PMBUS8.649.610.56PMBUS GENERALPMEUSVin=MILEVin=MILEVin=MILEPMBUS GENERALSupported. Linear data format used.4.003.33.3Dis speedQQQ3.33.3Logic high inputQQQ3.33.6	Current Limit Inception ②	98% of Vnom., after warmup, Conf via PMBUS	27	29	31
Short Circuit Duration (remove short for recovery)Output shorted to ground, no damageContinuousShort circuit protection methodHiccup current limitingNon-latchingRegulation ③Line RegulationImage of the state	Short Circuit				
(remove short for recovery)Output shored to ground, no damageContinuousShort circuit protection methodHiccup current limitingNon-latchingRegulation ③±75Line RegulationIout=min. to max., Vout=nom., full load±45Load RegulationIout=min. to max., Vin=nom.±80±75Load RegulationIout=min. to max., Vin=nom.80150Ripple and Noise5 H2- 20 MH2 BW, Cout=1µF MLCC paralleled with 10µF80100Power CoefficientAt all outputs0.010.02Maximum Output CapacitanceLow ESR10010,000Power Good-Megative logic (Conf via PMBUS2.43.6Power good ligh stage voltageConf via PMBUS8.649.6Out voltage for power good off triggeringConf via PMBUS9.7210.811.88PMBUSEEEEEPMBUS BENERALPMBUS BENERALPMBUS BENERALAdo Ion Ion Ion Ion Ion Ion Ion Ion Ion Io		Hiccup technique, autorecovery within 1% of Vout		0.4	1
Circle in or becoveryHiccup current limitingNon-latchingRegulation ③Uin=min. to max., Vout=nom., full load±75Load Regulationlout=min. to max., Vin=nom.±45Ripple and Noise5 HZ- 20 MHz BW, Cout=1µF MLCC paralleled with 10µF80150Temperature CoefficientAt all outputs0.010.02Maximum Output CapacitanceLow ESR10010.000Power Good—Negative logic (Conf via PMBUS)2.43.6Power good high stage voltageConf via PMBUS8.649.6Out voltage for power good off triggeringConf via PMBUS9.7210.8PMBUSEVENUEEVENUEEVENUEEVENUEPMBUS GENERALPMBUS2.43.3PMBUS GENERALJana data format used.Bus speed400Logic high input00.83.3Logic low input00.83.6Logic low input00.83.6		Output shorted to ground, no damage		Continuous	
Regulation ③       Vin=min. to max., Vout=nom., full load       ±75         Load Regulation       lout=min. to max., Vin=nom.       ±45         Ripple and Noise       5 Hz- 20 MHz BW, Cout=1µF MLCC paralleled with 10µF       80       150         Temperature Coefficient       At all outputs       0.01       0.02         Maximum Output Capacitance       Low ESR       100       10,000         Power Good—Negative logic (Conf via PMBUS)       2.4       3.6         Power good low stage voltage       -0.1       0.4         Out voltage for power good off triggering       Conf via PMBUS       8.64       9.6       10.56         Out Voltage for power good on triggering       Conf via PMBUS       9.72       10.8       11.88         PMBUS GENERAL       PMBUS GENERAL       PMBUS GENERAL       400         Logic high input       2       3.3       3.3         Logic low input       0       0.8       3.6				Non-latching	
Line RegulationVin=min. to max., Vout=nom., full load±75Load Regulationlout=min. to max., Vin=nom.±45Ripple and Noise5 Hz- 20 MHz BW, Cout=1µF MLCC paralleled with 10µF80150Temperature CoefficientAt all outputs0.010.02Maximum Output CapacitanceLow ESR10010,000Power Good—Negative logic (Conf via PMBUS)2.43.6Power good high stage voltage2.43.6Out voltage for power good off triggeringConf via PMBUS8.64Out voltage for power good off triggeringConf via PMBUS9.7210.8PMBUS GENERALPMBUS GENERALPMBUS GENERAL400Logic high input23.33.3Logic low input00.80.8Logic high output00.83.6	•	Fliceup current inniting		Non-latening	
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Maximum Output CapacitanceLow ESR10010,000Power Good—Negative logic (Conf via PMBUS)2.43.6Power good high stage voltage2.43.6Power good low stage voltage0.10.4Out voltage for power good off triggeringConf via PMBUS8.649.610.56Out Voltage for power good on triggeringConf via PMBUS9.7210.811.88PMBUSPMBUS GENERALPMBUS REV. 1.2. SMBALERT# is supported. Linear data format used.Bus speed12400Logic high input23.3400Logic low input00.83.6Logic high output03.63.6	Ripple and Noise			80	150
Power Good—Negative logic (Conf via PMBUS)Power good high stage voltage2.43.6Power good low stage voltage-0.10.4Out voltage for power good off triggeringConf via PMBUS8.649.610.56Out Voltage for power good on triggeringConf via PMBUS9.7210.811.88PMBUSPMBUS GENERALPMBUS REV. 1.2. SMBALERT# is supported. PEC is supported. Linear data format used.Bus speed2400Logic high input23.3Logic low input00.8Logic high output0.43.6	Temperature Coefficient	At all outputs		0.01	0.02
Power good high stage voltage2.43.6Power good low stage voltage-0.10.4Out voltage for power good off triggeringConf via PMBUS8.649.610.56Out Voltage for power good on triggeringConf via PMBUS9.7210.811.88PMBUSPMBUSPMBUS GENERALPMBUS REV. 1.2. SMBALERT# is supported. Linear data format used.Bus speed2400Logic high input23.3Logic low input00.8Logic high output2.43.6	Maximum Output Capacitance	Low ESR	100		10,000
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Out voltage for power good off triggeringConf via PMBUS8.649.610.56Out Voltage for power good on triggeringConf via PMBUS9.7210.811.88PMBUSPMBUS GENERALPMBUS REV. 1.2. SMBALERT# is supported. Linear data format used.Bus speedConf via PMBUSLogic high input2400Logic low input00.8Logic high output2.43.6	Power good high stage voltage		2.4		3.6
Out Voltage for power good on triggeringConf via PMBUS9.7210.811.88PMBUSPMBUS GENERALPMBUS REV. 1.2. SMBALERT# is supported. PEC is supported. Linear data format used.Bus speedConf via PMBUS2400Logic high input23.33.3Logic low input00.80.8Logic high output2.43.6	Power good low stage voltage		-0.1		0.4
PMBUS       PMBUS GENERAL         PMBUS REV. 1.2. SMBALERT# is supported. PEC is supported. Linear data format used.       400         Bus speed       2       3.3         Logic high input       0       0.8         Logic high output       2.4       3.6	Out voltage for power good off triggering	Conf via PMBUS	8.64	9.6	10.56
PMBUS GENERAL         PMBUS REV. 1.2. SMBALERT# is supported. Linear data format used.         Bus speed        400         Logic high input       2       3.3         Logic low input       0       0.8         Logic high output       2.4       3.6	Out Voltage for power good on triggering	Conf via PMBUS	9.72	10.8	11.88
PMBUS REV. 1.2. SMBALERT# is supported. Linear data format used.         Bus speed       Image: Colspan="3">400         Logic high input       2       3.3         Logic low input       0       0.8         Logic high output       2.4       3.6	PMBUS				
Bus speed         400           Logic high input         2         3.3           Logic low input         0         0.8           Logic high output         2.4         3.6	PMBUS GENERAL				
Logic high input         2         3.3           Logic low input         0         0.8           Logic high output         2.4         3.6	PMBUS REV. 1.2. SMBALERT# is supported. F	EC is supported. Linear data format used.			
Logic low input         0         0.8           Logic high output         2.4         3.6	Bus speed				400
Logic high output         2.4         3.6	Logic high input		2		3.3
	Logic low input		0		0.8
Logic low output -0.1 0.4	Logic high output		2.4		3.6
	Logic low output		-0.1		0.4

#### FUNCTIONAL SPECIFICATIONS, DBE0125 (12V, 25A, CONT.)

FEATURES and OPTIONS

## **DBE/DVE Series**

### Up to 300W Digital Fully Regulated Intermediate

DC-DC Bus Converter

### FUNCTIONAL SPECIFICATIONS, DBE0125 (12V, 25A, CONT.)

PMBus	Conditions ${\mathbb O}$	Minimum	Typical/Nominal	Maximum	Units
PMBUS ADDRESSING					
	12d, SAO or SA1 lefts open, default PMBus addre	ess 119d is assigned i	nstead. PMBUS address =	8*SA0 + SA1. SA0/SA	1 value VS resis
tor connected to GND.			10		1.0
0			10		kΩ
1			22		kΩ
2			33		kΩ
3			47		kΩ
4			68		kΩ
5			100		kΩ
6			150		kΩ
7			220		kΩ
PMBus MONITORING ACCURACY					
VIN_READ		-3		3	%
VOUT_READ		-2		2	%
IOUT_READ		-1.5		1.5	A
TEMP_READ		-5		5	C°
DIGITAL INTERFACE SPECIFICATIONS	S (PMBUS MONITORING & FUNCTION	AL DESCRIPTION	)		
Fault Protection Specifications					
Output Voltage, Over Voltage protection, OVP	Factory default		14.4		V
/OUT_OV_FAULT_LIMIT, Configurable via PMBus	>VOUT_OV_WARM_LIMIT	8.1		15.6	V
	Fault response time		200		μS
nput Voltage, Input Over Voltage Protection	Factory default		80		V
	Setpoint accuracy	-2		2	%
/IN_OV_FAULT_LIMIT (Configurable via PMBus) ④	>VIN_OV_WARM_LIMIT		34	110	V
	Delay		300		μS
nput Voltage, Input Under Voltage Protection, UVLV	Factory default		32		V
	Setpoint accuracy	-2		2	%
/IN_FAULT_LIMIT_LIMIT(Configurable via	<vin_uv_warm_limit <vin_ov_fault_limit<="" td=""><td></td><td>32</td><td>75</td><td>V</td></vin_uv_warm_limit>		32	75	V
PMBus) ④	<vin_off Delay</vin_off 		300		μS
Over Current Protection, OCP	Setpoint accuracy (lo)	-3	300	3	μ3 %
		-3	20	3	
OUT_OC_FAULT_LIMIT (Configurable via	(factory default)		29		A
PMBus) @	>IOUT_OC_WARN_LIMT	0		34	A
	Fault response time		200		μS
Over Temperature Protection, OTP	OT_FAULT_LIMIT (factory default)		120/125		0°
DT_FAULT_LIMIT (Configurable via PMBus) ④	>OT_WARM_LIMIT	30		130	°C
	OTP accuracy (factory default)	5		5	°C
	Fault response time		300		μS
/IECHANICAL (Through Hole Models)					
Outline Dimensions—open frame			2.3x0.9x0.46		Inches
	WxLxH		58.4x22.7x11.7		mm
Outline Dimensions—with baseplate			2.3x0.9x0.52		Inches
	WxLxH		58.4x22.7x13.2		mm
Neight—open frame			0.98		Ounces
			28		Grams
Neight—with baseplate			1.37		Ounces
			39		Grams
Fhrough Hole Pin Diameter			0.04 & 0.062		Inches
			1.016 & 1.575		mm
Through Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate		50		µ-inches
-	Gold overplate		5		µ-inches

## **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### FUNCTIONAL SPECIFICATIONS, DBE0125 (12V, 25A, CONT.)

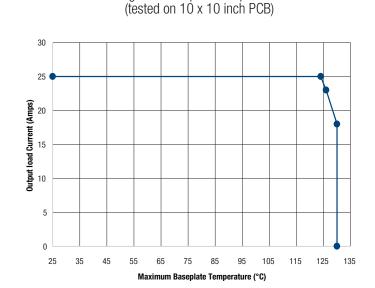
ENVIRONMENTAL	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Operating Ambient Temperature Range	With derating	-40		85	°C
Operating Baseplate Temperature	No derating according to IPC 9592	-40		110	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown—open frame	Measured at hotspot		140		°C
Thermal Protection/Shutdown with baseplate ④			130		°C
Electromagnetic Interference Conducted, EN55022/CISPR22	External filter required		В		Class
Radiated, EN55022/CISPR22			В		Class
RoHS rating			RoHS-6		

#### Notes

- $\odot$  Typical at TA = +25°C under nominal line voltage and full-load conditions. All models are specified with an external 1µF multi-layer ceramic and 10µF capacitors across their output pins.
- ② Over-current protection is non-latching with auto recovery (Hiccup)
- ③ Regulation specifications describe the output voltage changes as the line voltage or load current is varied from its nominal or midpoint value to either extreme.
- ④ When baseplate temperature is above the maximum operating baseplate temperature of 110°C and below thermal protection temperature of 130°C, the module will continue working but the thermal stress will be above the IPC9592 guide limit for long-term operation. It is not recommended to run in such a situation long-term.

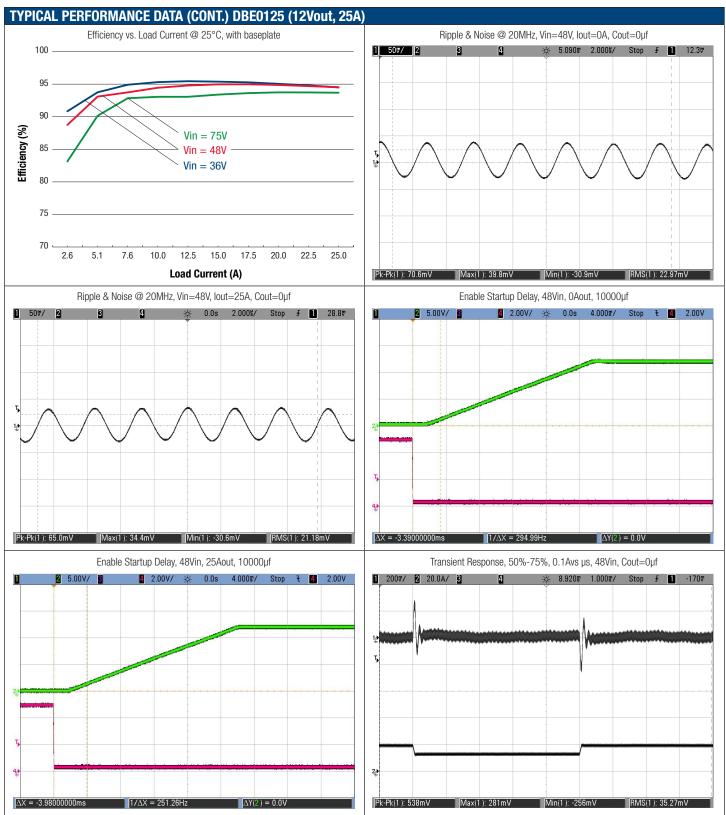
Vin=48V



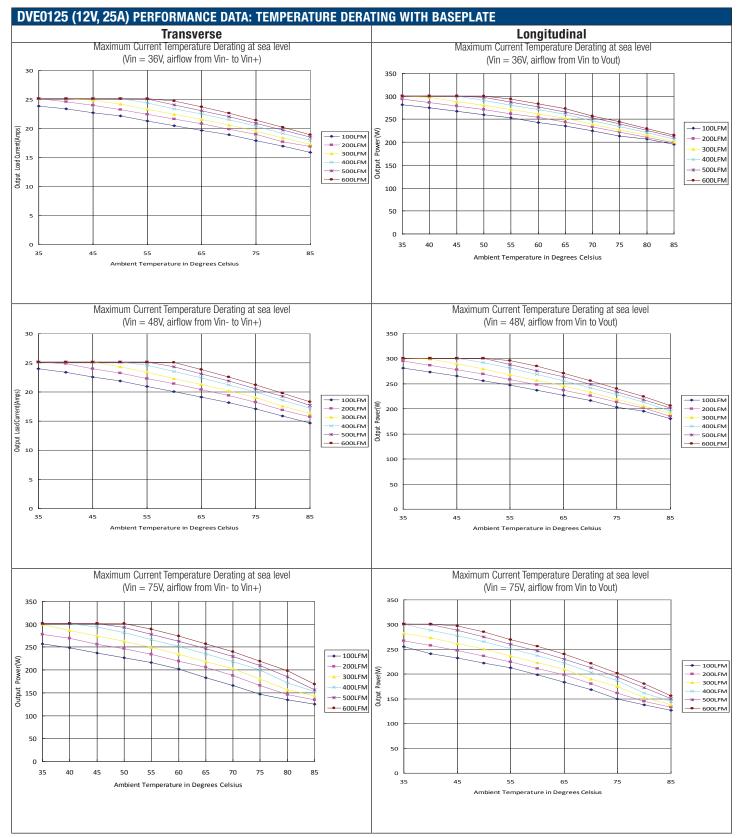


Current Derating Max Baseplate Temperature

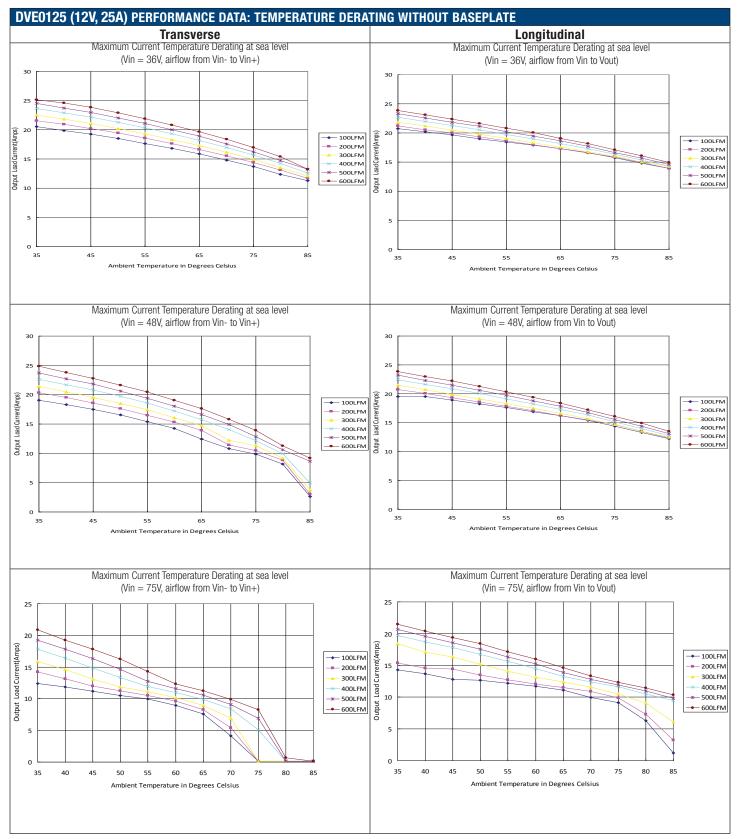
# **DBE/DVE Series**



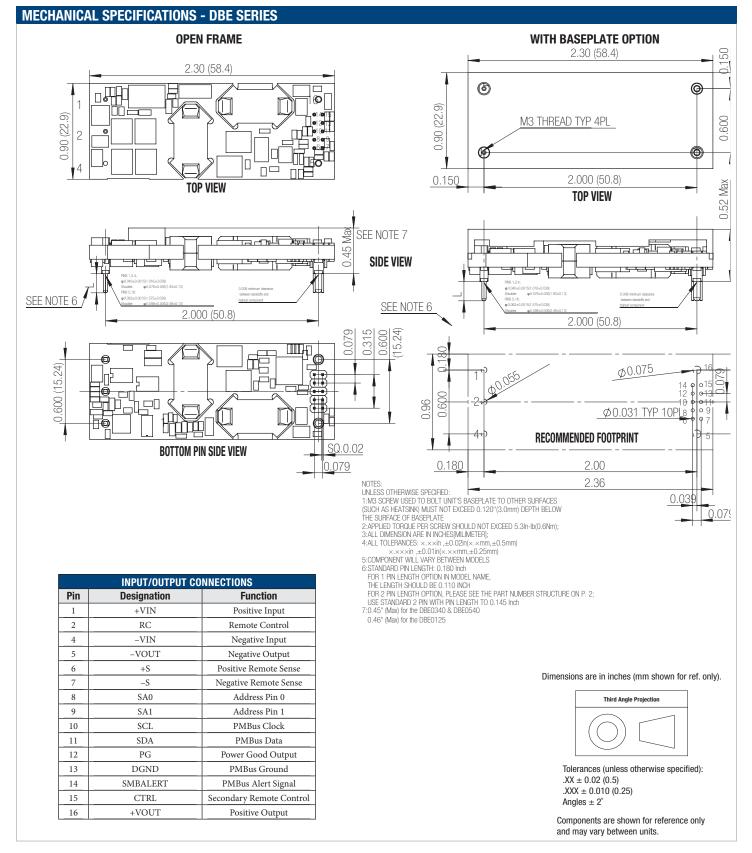
# **DBE/DVE Series**



# **DBE/DVE Series**

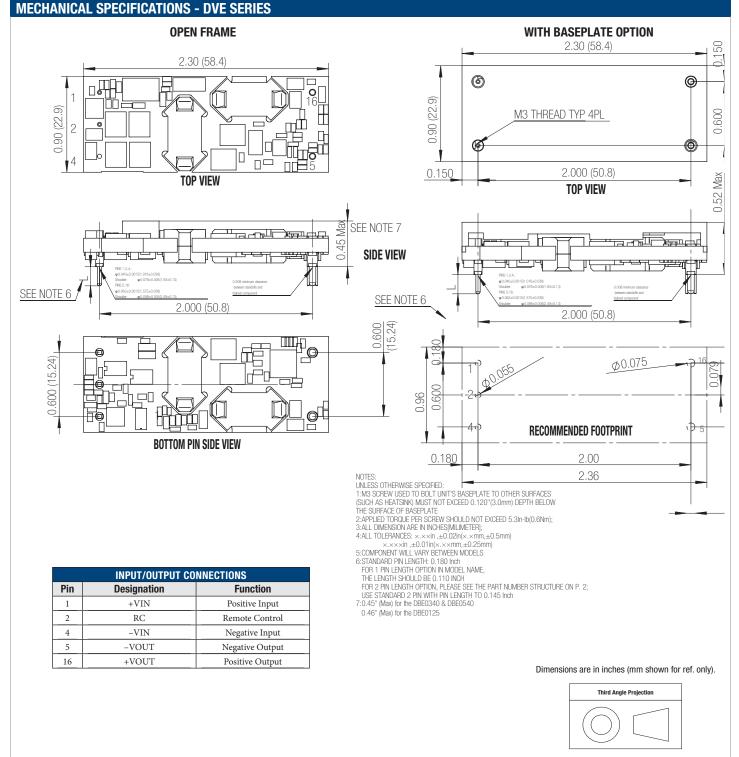


# **DBE/DVE Series**



# **DBE/DVE Series**

Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

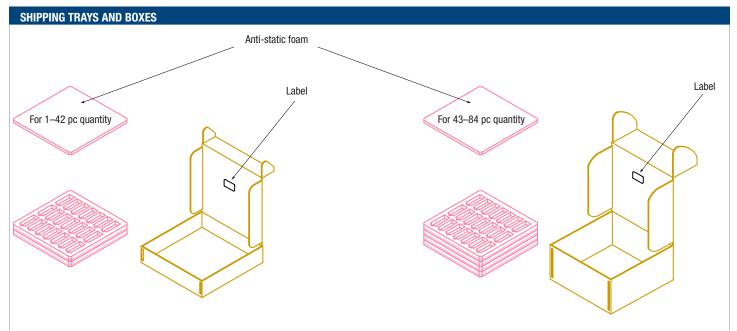


Tolerances (unless otherwise specified): .XX  $\pm$  0.02 (0.5) .XXX  $\pm$  0.010 (0.25) Angles  $\pm$  2°

Components are shown for reference only and may vary between units.

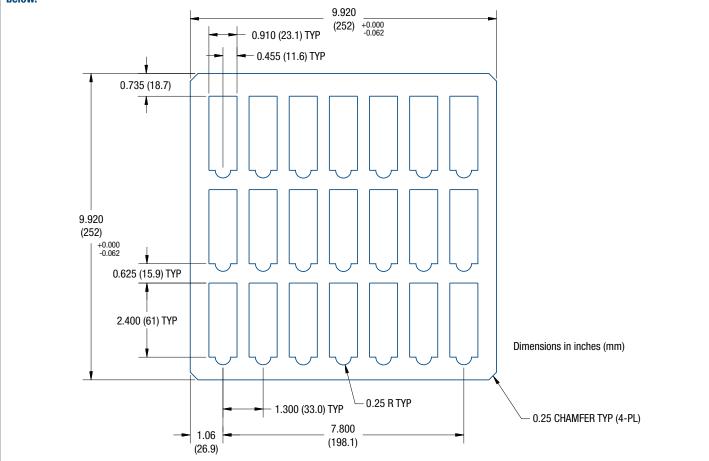
## **DBE/DVE Series**

Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter



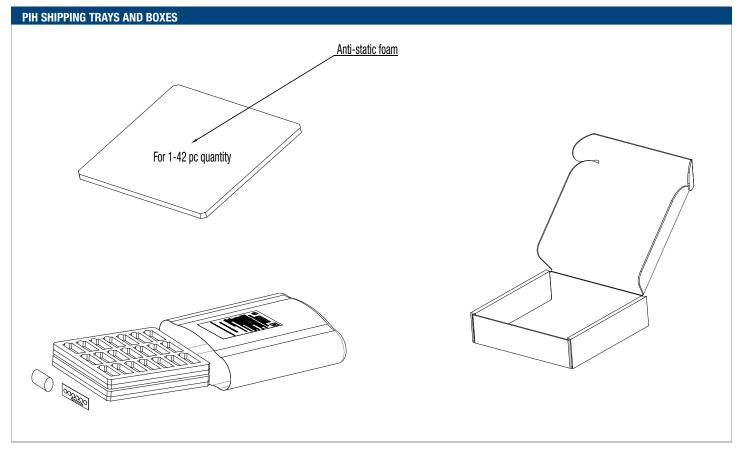
#### SHIPPING TRAY

DBE/DVE modules are supplied in a 21-piece (3-by-7) shipping tray. The tray is an anti-static closed-cell polyethylene foam. Dimensions are shown below.





# **DBE/DVE Series**





### **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### TECHNICAL NOTES

#### Power Management Overview

The module includes a wide range of readable and configurable power management features that are easy to implement with a minimum of external components. Furthermore, the module includes protection features that continuously protect the load from damage due to unexpected system faults. The SMBALERT pin alerts the host if there is a fault in the module. The following product parameters can continuously be monitored by a host: Vout, lout, Vin, Temperature, and Power Good. The module is distributed with a default configuration suitable for a wide range operation in terms of Vin, Vout, and load. All power management functions can be reconfigured using the PMBus interface. The product provides a PMBus digital interface that enables the user to configure many aspects of the device operation as well as monitor the input and output parameters. Please contact Murata-PS for design support of special configurations.

#### Soft-start Power Up

The default rise time of the ramp up is 20 ms. When starting by applying input voltage the control circuit boot-up time adds an additional 10 ms delay. The soft-start power up of the module can be reconfigured using the PMBus interface.

#### **Over Voltage Protection (OVP)**

The module includes over voltage limiting circuitry for protection of the load. The default OVP limit is 20% above the nominal output voltage. If the output voltage surpasses the OVP limit, the module can respond in different ways. The default response from an over voltage fault is to immediately shut down. The device will continuously check for the presence of the fault condition, and when the fault condition no longer exists the device will be re-enabled. The OVP fault level and fault response can be reconfigured using the PMBus interface.

#### **Over Current Protection (OCP, Current limit)**

The module includes current limiting circuitry for protection at continuous over load. The default setting for the product is hicup mode. The current limit could be configured by simply setting the IOUT\_OC\_FAULT\_LIMIT to be greater than the IOUT\_OC\_WARN\_LIMIT. The maximum value that the current limit could be set is 50A.

#### **Power Good**

The module provides Power Good (PG) flag in the Status Word register that indicates the output voltage is within a specified tolerance of its target level and no fault condition exists. The Power Good pin default logic is negative and it can be configured by MFR\_PGOOD\_POLARITY.

Switching Frequency Adjust Using PMBus

By default the switching frequency is set to 175-250 KHz variable frequency control. The product is optimized at this frequency but it can be changed to 175 KHz fixed frequency control. The thermal performance can be affected if the switching frequency is changed when Vin is above 50 Vdc. The following graph shows Frequency vs. Vin.

#### **PMBus Interface**

This module offers a PMBus digital interface that enables the user to configure many characteristics of the device operation as well as to monitor the input and output voltages, output current and device temperature. The module can be used with any standard two-wire I2C or SMBus host device. In addition, the module is compatible with PMBus version 1.2 and includes an SMBALERT line to help alleviate bandwidth limitations related to continuous fault monitoring. The module supports 100 kHz and 400 kHz bus clock frequency only.

#### **Monitoring via PMBus**

A system controller (host device) can monitor a wide variety of parameters through the PMBus interface. The controller can monitor fault conditions by monitoring the SMBALERT pin, which will be asserted when any number of pre-configured fault or warning conditions occur. The system controller can also continuously monitor any number of power conversion parameters including but not limited to the following:

- Input voltage
- Output voltage
- Output current
- Module temperature

#### Software Tools for Design and Production

For these modules Murata-PS provides software for configuring and monitoring via the PMBus interface. For more information please contact your local Murata-PS representative.

# **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### PMBus<sup>™</sup> General Notes

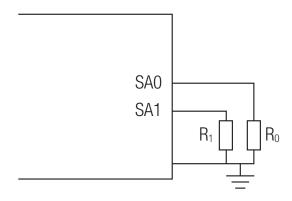
- Unit restores the entire contents of the non-volatile User Store memory when power up
- PEC is supported
- Max bus speed: 400kHZ
- SMBALERT# is supported
- Linear data format used

#### **PMBus Addressing**

Figure 1 and the accompanying table display the recommended resistor values for hard-wiring PMBus addresses (1% tolerance resistors recommended): The address is set in the form of two octal (0 to 7) digits, with each pin setting one digit. The SA1 pin sets the high order digit and SA0 sets the low order digit. The resistor values for each digit is shown below.

- The SAO and SA1 pins can be configured with a resistor to GND according to the following equation.
- PMBus Address = 8 x (SA0value) + (SA1 value)

If the calculated PMBus address is 0d, 11d or 12d, SA0 or SA1 lefts open, default PMBus address 119d is assigned instead.. From a system point of view, the user shall also be aware of further limitations of the addresses as stated in the PMBus Specification. It is not recommended to keep the SA0 and SA1 pins left open. The products are designed to be PMBus compliant. The following tables list the implemented PMBus read commands. For more detailed information see "PMBus Power System Proto-col Specification, Part I – General Requirements, Transport and Electrical Interface" and "PMBus Power System Management Protocol, Part II – Command Language."



Digit (SA0, SA1 index)	Resistor Value [k $\Omega$ ]
0	10
1	22
2	33
3	47
4	68
5	100
6	150
7	220

Figure 1. Schematic of Connection of Address Resistors

#### **COMMAND LIST:**

CMD	Command Name	SMBus Transaction Type: Writing Data	SMBus Transaction Type: Reading Data	Num- ber Of Data Bytes	Default Value	Lower limit	Upper limit	Unit
01h	OPERATION <sup>2</sup>	Write Byte	Read Byte	1	0x80			
02h	ON_OFF_CONFIG <sup>3</sup>	Write Byte	Read Byte	1	0x19			
03h	CLEAR_FAULTS	Send byte	N/A	0	N/A			
10h	WRITE_PROTECT	Write Byte	Read Byte	1	0x00			
11h	STORE_DEFAULT_AL <sup>L</sup> 4	Send byte	N/A	0	N/A			
12h	RESTORE_DEFAULT_ALL <sup>4</sup>	Send byte	N/A	0	N/A			
15h	STORE_USER_ALL <sup>4</sup>	Send byte	N/A	0	N/A			
16h	RESTORE_USER_ALL <sup>4</sup>	Send byte	N/A	0	N/A			
19h	CAPABILITY	N/A	Read Byte	1	0xB0			
20h	VOUT_MODE	N/A	Read Byte	1	0x17			
21h	VOUT_COMMAND	Write Word	Read Word	2		8.100	13.200	V
22h	VOUT_TRIM	Write Word	Read Word	2	0			V
25h	VOUT_MARGIN_HIGH	Write Word	Read Word	2		8.100	13.199	V

### **DBE/DVE Series**

CMD	Command Name	SMBus Transaction Type: Writing Data	SMBus Transaction Type: Reading Data	Num- ber Of Data Bytes	Default Value	Lower limit	Upper limit	Unit
26h	VOUT_MARGIN_LOW	Write Word	Read Word	2	8.100	8.100	13.200	V
28h	VOUT_DROOP	Write Word <sup>10</sup>	Read Word	2	0/1012	0	100	mΩ
40h	VOUT_OV_FAULT_LIMIT	Write Word	Read Word	2		8.100	15.600	V
41h	VOUT_OV_FAULT_RESPONSE⁵	Write Byte	Read Byte	1	0xB8			
42h	VOUT_OV_WARN_LIMIT	Write Word	Read Word	2		8.100	15.600	V
46h	IOUT_OC_FAULT_LIMIT	Write Word	Read Word	2	29.00	0.00	29.00	А
47h	IOUT_OC_FAULT_RESPONSE6	Write Byte	Read Byte	1	0xF8			
4Ah	IOUT_OC_WARN_LIMIT	Write Word	Read Word	2	27.00	0.00	29.00	А
4Fh	OT_FAULT_LIMIT	Write Word	Read Word	2		30	130	°C
50h	OT_FAULT_RESPONSE <sup>5</sup>	Write Byte	Read Byte	1	0xB8			
51h	OT_WARN_LIMIT	Write Word	Read Word	2	110	30	130	°C
55h	VIN_OV_FAULT_LIMIT	Write Word	Read Word	2		32.00	110.00	V
56h	VIN_OV_FAULT_RESPONSE7	Write Byte	Read Byte	1	0xF8			
57h	VIN_OV_WARN_LIMIT	Write Word	Read Word	2		32.00	110.00	V
58h	VIN_UV_WARN_LIMIT	Write Word	Read Word	2	33.00	32.00	75.00	V
59h	VIN_UV_FAULT_LIMIT	Write Word	Read Word	2	32.00	32.00	75.00	V
5Ah	VIN_UV_FAULT_RESPONSE7	Write Byte	Read Byte	1	0xF8			
5Eh	POWER_GOOD_ON	Write Word	Read Word	2		1.000	13.199	V
5Fh	POWER_GOOD_OFF	Write Word	Read Word	2	9.600	1.000	13.199	V
60h	TON_DELAY	Write Word <sup>10,11</sup>	Read Word	2	1	1	500	ms
61h	TON_RISE	Write Word <sup>10,11</sup>	Read Word	2	20	20	100	ms
64h	TOFF_DELAY	Write Word <sup>10,11</sup>	Read Word	2	0	0	500	ms
65h	TOFF_FALL	Write Word <sup>10,11</sup>	Read Word	2	20	20	100	ms
78h	STATUS_BYTE	N/A	Read Byte	1	N/A			
79h	STATUS_WORD	N/A	Read Word	2	N/A			
7Ah	STATUS_VOUT	Write Byte	Read Byte	1	N/A			
7Bh	STATUS_IOUT	Write Byte	Read Byte	1	N/A			
7Ch	STATUS_INPUT	Write Byte	Read Byte	1	N/A			
7Dh	STATUS_TEMPERATURE	Write Byte	Read Byte	1	N/A			
7Eh	STATUS_CML	Write Byte	Read Byte	1	N/A			
88h	READ_VIN	N/A	Read Word	2	N/A			V
8Bh	READ_VOUT	N/A	Read Word	2	N/A			V
8Ch	READ_IOUT	N/A	Read Word	2	N/A			А
8Dh	READ_TEMPERATURE_1	N/A	Read Word	2	N/A			°C
94h	READ_DUTY_CYCLE	N/A	Read Word	2	N/A			%
95h	READ_FREQUENCY	N/A	Read Word	2	N/A			kHZ
96h	READ_POUT	N/A	Read Word	2	N/A			W
98h	PMBUS_REVISION	N/A	Read Byte	1	0x42			
99h	MFR_ID	N/A	Block Read	22				
9Ah	MFR_MODEL <sup>8</sup>	Block Write*	Block Read		N/A			
9Bh	MFR_REVISION <sup>8</sup>	Block Write*	Block Read		N/A			

### **DBE/DVE Series**

#### Up to 300W Digital Fully Regulated Intermediate **DC-DC Bus Converter**

#### **COMMAND LIST**

CMD	Command Name	SMBus Transaction Type: Writing Data	SMBus Transaction Type: Reading Data	Num- ber Of Data Bytes	Default Value	Lower limit	Upper limit	Unit	
9Dh	MFR_DATE <sup>8</sup>	Block Write*	Block Read		N/A				
9Eh	MFR_SERIAL <sup>8</sup>	Block Write*	Block Read		N/A				
A0h	MFR_VIN_MIN	N/A	Read Word	2	36.00			V	
A1h	MFR_VIN_MAX	N/A	Read Word	2	75.00			V	
A2h	MFR_IIN_MAX	N/A	Read Word	2	15			А	
A3h	MFR_PIN_MAX	N/A	Read Word	2	324			W	
A4h	MFR_VOUT_MIN	N/A	Read Word	2	8.100			V	
A5h	MFR_VOUT_MAX	N/A	Read Word	2				V	
A6h	MFR_IOUT_MAX	N/A	Read Word	2	25.00			А	
A7h	MFR_POUT_MAX	N/A	Read Word	2	300			W	
A8h	MFR_TAMBIENT_MAX	N/A	Read Word	2	85			°C	
A9h	MFR_TAMBIENT_MIN	N/A	Read Word	2	-40			°C	
B0h	USER_DATA_00	Block Write	Block Read		""				
B1h	USER_DATA_01	Block Write	Block Read		""				
C0h	MFR_MAX_TEMP_1	N/A	Read Word	2	130			°C	
DBh	MFR_CURRENT_SHARE_CONFIG	Write Byte*	Read Byte	1					
DDh	MFR_PRIMARY_ON_OFF_CONFIG	Write Byte	Read Byte	1					
DEh	MFR_PGOOD_POLARITY	Write Byte	Read Byte	1	0x00				
E8h	MFR_VIN_OV_FAULT_HYS	Write Word	Read Word	2	2.00	1.00	20.00	V	
E9h	MFR_VIN_UV_FAULT_HYS	Write Word	Read Word	2	2.00	1.00	20.00	V	
EAh	MFR_OT_FAULT_HYS	Write Word	Read Word	2	20	5	50	°C	
F6h	MFR_CALIBRATION_STATUS	N/A	Read Byte*	1	0xC7				
F9h	MFR_VIN_SENSE_CALIBRATION	Write byte*	N/A	1	N/A				
FAh	MFR_IOUT_SENSE_CALIBRATION	Write Word*	N/A	2	N/A				
FBh	MFR_VOUT_SET_POINT_CALI- BRATION	Write Word*	N/A	2	N/A				
FCh	MFR_SUPERVISOR_PASSWORD	Block Write	N/A	N/A	N/A				

<sup>2</sup>Not supported items:

100101XXb Margin Low(Ignore Fault),

101001XXb On Margin High(Ignore Fault) <sup>3</sup>Restart delay of turned off by OPEATION or CONTROL or primary on/off is 200ms

<sup>4</sup>Unit will shutdown 1s for protection , then recover automaticly

- 5 Restart delay: 500ms
- Turn off delay unit: 500ms, lower limit: 0ms

6 Restart delay: 500ms

Bits 7:6: 00b,01b,10b are not supported 7 Only Support bits 7:6

8 Unit's actual inforamtion

<sup>9</sup> Default value of DROOP CURRENT SHARE ENABLED mode: 0x01

Default value of DROOP CURRENT SHARE DISABLED mode: 0x00

<sup>10</sup> Available in supervisor mode when droop current share on, available in both mode when droop current share off

<sup>11</sup> Only support the data when n = 0 by Linear Data Format <sup>12</sup> Locked to 10m $\Omega$  in DROOP CURRENT SHARE mode; VOUT\_DROOP is not used in CURRENT SHARE DISABLED mode

<sup>13</sup> Default value of negative logic: 0x04

Default value of positive logic: 0x06 <sup>14</sup> VOUT\_TRIM + VOUT COMMAND is limited to 8.1~13.2V, if calculated Vout exceeds limit, then show invalid data. <sup>15</sup> Default value of without "B" suffix: 120C

Default value of with "B" suffix: 125C

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## **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### MURATA-PS DEFINED COMMANDS (01-CFH REFER TO PMBUS 1.2 SPEC)

	h: MFR_VARIABLE_	·							
Bits	Purpose	Value	Meaning						
7:1		0000000	Reserved						
0	Variable frequency	0	Turn on variable frequency control						
0	control	1	Turn off variable frequency control						
DB	h: MFR_CURRENT_	SHARE_C	ONFIG						
Bits	B Purpose	Value	Meaning	On/Off 2 pin	VOUT_DROOP	TON_DELAY	TOFF_DELAY	TON_RISE	TOFF_FALL
7:1		0000000	Reserved						
0	Variable frequency	0	Current share disabled	On/Off 2	configurable	configurable	configurable	configurable	configurable
0	control	1	Droop current share mode enabled	On/Off 2	locked to 0x000A	locked to 0x0001	locked to 0x0000	locked to 0x0000	locked to 0x000
DD	h: MFR_PRIMARY_	ON_OFF_	CONFIG						
Bits	s Purpose	Value	Meaning						
7:3		00000	Reserved						
	Controls how the	0	Unit ignores the primary ON/OFF pin						
2	unit responds to the CONTROL pin	1	Unit requires the primary ON/OFF pin to be asserted to start the unit.						
4	Polarity of primary	0	Active low (Pull pin low to start the unit)						
I	ON/OFF logic	1	Active high (Pull high or open to start the unit)						
0		0	Reserved						
DE	h: MFR_ PGOOD_PO	DLARITY							
Bits	s Purpose	Value	Meaning						
7:1		0000000	Reserved						
0	Power good polarity	0	Negative logic, output low if Vout rises to specific value						
0	of pin 12	1	Positive logic, output high if Vout rises to specific value						

## **DBE/DVE Series**

Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### STATUS WORD AND BYTE

#### GREEN = supported

STATUS_VOUT
7 VOUT_OV_FAULT
6 VOUT_OV_WARNING
5 VOUT_UV_WARNING
4 VOUT_UV_FAULT
3 VOUT_MAX Warning
2 TON_MAX_FAULT
1 TOFF_MAX_WARNING
0 VOUT Tracking Error

STATUS_IOUT
7 IOUT_OC_FAULT
6 IOUT_OC_LV_FAULT
5 IOUT_OC_WARNING
4 IOUT_UC_FAULT
3 Current Share Fault
2 In Power Limiting Mode
1 POUT_OP_FAULT
0 POUT_OP_WARNING

STATUS_TEMPERATURE
7 OT_FAULT
6 OT_WARNING
5 UT_WARNING
4 UT_FAULT
3 Reserved
2 Reserved
1 Reserved
0 Reserved

STATUS_CML
7 Invalid/Unsupported Command
6 Invalid/Unsupported Data
5 Packet Error Check Failed
4 Memory Fault Detected
3 Processor Fault Detected
2 Reserved
1 Other Communication Fault
0 Other Memory Or Logic Fault

STATUS_WORD
7 VOUT
6 IOUT/POUT
5 INPUT
4 MFR_SPECIFIC
3 POWER_GOOD#
2 FANS
1 OTHER
0 UNKNOWN
7 BUSY
6 OFF
5 VOUT_OV_FAULT
4 IOUT_OC_FAULT
3 VIN_UV_FAULT
2 TEMPERATURE
1 CML
0 NONE OF THE ABOVE

STATUS_C	THER
7 Reserve	Ł
6 Reserve	Ł
5 Input A l	use/Breaker Fault
4 Input B I	use/Breaker Fault
3 Input A (	OR-ing Device Fault
2 Input B (	OR-ing Device Fault
1 Output (	OR-ing Device Fault
0 Reserve	ł

STATUS_INPUT
7 VIN_OV_FAULT
6 VIN_OV_WARNING
5 VIN_UV_WARNING
4 VIN_UV_FAULT
3 Unit Off For Low Input Voltage
2 IIN_OC_FAULT
1 IIN_OC_WARNING
0 PIN_OP_WARNING

STATUS_MFR_SPECIFIC
Manufacturer Defined

STATUS_FANS_1_2
7 Fan 1 Fault
6 Fan 2 Fault
5 Fan 1 Warning
4 Fan 2 Warning
3 Fan 1 Speed Override
2 Fan 2 Speed Override
1 Air Flow Fault
0 Air Flow Warning

STATUS_FANS_3_4
7 Fan 3 Fault
6 Fan 4 Fault
5 Fan 3 Warning
4 Fan 4 Warning
3 Fan 3 Speed Override
2 Fan 4 Speed Override
1 Reserved
0 Reserved



### **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### **TECHNICAL NOTES (CONT.)**

#### Parallel Load Sharing (S Option, Droop Load Sharing)

Two or more converters may be connected in parallel at both the input and output terminals to support higher output current (total power, see figure 2) or to improve reliability due to the reduced stress that results when the modules are operating below their rated limits. For applications requiring current share, followed the guidelines below. The products have a pre-configured voltage droop. The stated output voltage set point is at no load. The output voltage will decrease when the load current is increased. The voltage will drop droop 0.35V while load reaches max load. Our goal is to have each converter contribute nearly identical current into the output load under all input, environmental and load conditions.

#### Using Parallel Connections – Load Sharing (Power Boost)

Direct Connection Parallel Guidelines

- Use a common input power source. The input voltage must be between 36V and 75V.
- +Vout and -Vout of all parallel units should be connected with a balance output impedance; +Sense and -Sense should be connected together (see figure 2).
- Turn all units off before configuring the output voltage via PMBus commands; all units must have the same output voltage configuration.
- It is recommended to turn on one unit first and then turn other unit (s) on after the output for the first one has settled. Turn on the next unit (s) after the previous unit reaches its regulated output voltage for at least 10mS. Users can use a different control signal to turn each unit on.
- Users can use a common primary Remote On/Off logic control signal to turn on modules at the same time after the input voltage rises above 36V.
- Do not use PMBus to control unit On/Off when parallel operation is used.
- First power up the parallel system (all converters) with a load not exceeding the rated load of 60%\*35A\*UNITS\_QUANTITY and allow converters to settle (typically 10-50mS) before applying full load (90% load is recommended). If the loads are downstream POL converters, power these up shortly after the converter has reached steady state output. Also be aware of the delay caused by charging up external bypass capacitors.
- When converters are connected in parallel, allow for a safety factor of at least 10%. Up to 90% of max output current can be used from each module.
- It is critical that the PCB layout incorporates identical connections from each module to the load; use the same trace rating and airflow/thermal environments. If you add input filter components, use identical components and layout.
- For Power-down, do not soft-off (GUI) while in parallel operation. Power down units by primary On/Off signal. Turn units off at the same time or one by one to avoid the OCP being triggered.

**CAUTION:** This converter is not internally fused. To avoid danger to persons or equipment and to retain safety certification, the user must connect an external fast-blow input fuse as listed in the specifications. Be sure that the PC board pad area and etch size are adequate to provide enough current so that the fuse will blow with an overload.

#### Using Parallel Connections – Redundancy (N+1)

The redundancy connections require external user supplied "OR"ing diodes or "OR"ing MOSFETs for reliability purposes. The diodes allow for an uninterruptable

power system operation in case of a catastrophic failure (shorted output) by one of the converters.

The diodes should be identical part numbers to enhance balance between the converters. The default factory nominal voltage should be sufficiently matched between converters. The OR'ing diode system is the responsibility of the user. Be aware of the power levels applied to the diodes and possible heat sink requirements.

Schottky power diodes with approximately 0.3V drops or "OR"ing MOSFETs may be suitable in the loop whereas 0.7 V silicon power diodes may not be advisable. In the event of an internal device fault or failure of the mains power modules on the primary side, the other devices automatically take over the entire supply of the loads. In the basic N+1 power system, the "N" equals the number of modules required to fully power the system and "+1" equals one back-up module that will take over for a failed module. If the system consists of two power modules, each providing 50% of the total load power under normal operation and one module fails, another one delivers full power to the load. This means you can use smaller and less expensive power converters as the redundant elements, while achieving the goal of increased availability.

#### **Thermal Shutdown**

Extended operation at excessive temperature will initiate overtemperature shutdown triggered by a temperature sensor outside the PWM controller. This operates similarly to overcurrent and short circuit mode. The inception point of the overtemperature condition depends on the average power delivered, the ambient temperature and the extent of forced cooling airflow. Thermal shutdown uses only the hiccup mode (autorestart) and PMBus configurable hysteresis.

#### **Start Up Considerations**

When power is first applied to the DC-DC converter, there is some risk of start up difficulties if you do not have both low AC and DC impedance and adequate regulation of the input source. Make sure that your source supply does not allow the instantaneous input voltage to go below the minimum voltage at all times.

Use a moderate size capacitor very close to the input terminals. You may need two or more parallel capacitors. A larger electrolytic or ceramic cap supplies the surge current and a smaller parallel low-ESR ceramic cap gives low AC impedance.

Remember that the input current is carried both by the wiring and the ground plane return. Make sure the ground plane uses adequate thickness copper. Run additional bus wire if necessary.

#### Input Fusing

Certain applications and/or safety agencies may require fuses at the inputs of power conversion components. Fuses should also be used when there is the possibility of sustained input voltage reversal which is not current-limited. For greatest safety, we recommend a fast blow fuse installed in the ungrounded input supply line.

## **DBE/DVE Series**

Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

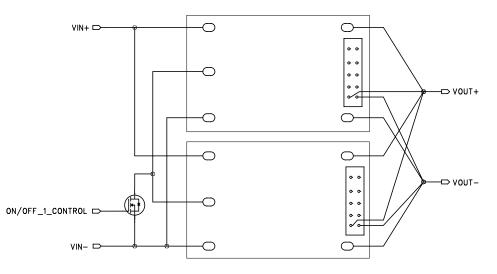


Figure 2. Load Sharing Block Diagram

#### Input Under-Voltage Shutdown and Start-Up Threshold

Under normal start-up conditions, converters will not begin to regulate properly until the rising input voltage exceeds and remains at the Start-Up Threshold Voltage (see Specifications). Once operating, converters will not turn off until the input voltage drops below the Under-Voltage Shutdown Limit. Subsequent restart will not occur until the input voltage rises again above the Start-Up Threshold. This built-in hysteresis prevents any unstable on/off operation at a single input voltage. The over/under-voltage fault level and fault response and hysterisis can be configured via the PMBus interface.

#### Start-Up Time

Assuming that the output current is set at the rated maximum, the Vin to Vout Start-Up Time (see Specifications) is the time interval between the point when the rising input voltage crosses the Start-Up Threshold and the fully loaded output voltage enters and remains within its specified accuracy band. Actual measured times will vary with input source impedance, external input capacitance, input voltage slew rate and final value of the input voltage as it appears at the converter.

These converters include a soft start circuit to moderate the duty cycle of its PWM controller at power up, thereby limiting the input inrush current.

The On/Off Remote Control interval from On command to Vout (final  $\pm$ 5%) assumes that the converter already has its input voltage stabilized above the Start-Up Threshold before the On command. The interval is measured from the On command until the output enters and remains within its specified accuracy band. The specification assumes that the output is fully loaded at maximum rated current. Similar conditions apply to the On to Vout regulated specification such as external load capacitance and soft start circuitry.

#### **Recommended Input Filtering**

The user must assure that the input source has low AC impedance to provide dynamic stability and that the input supply has little or no inductive content, including long distributed wiring to a remote power supply. The converter will operate with no additional external capacitance if these conditions are met. For best performance, we recommend installing a low-ESR capacitor immediately adjacent to the converter's input terminals. The capacitor should be a ceramic type such as the Murata GRM32 series or a polymer type. Make sure that the input terminals do not go below the undervoltage shutdown voltage at all times. More input bulk capacitance may be added in parallel (either electrolytic or tantalum) if needed.

#### **Recommended Output Filtering**

The converter will achieve its rated output ripple and noise with no additional external capacitor. However, the user may install more external output capacitance to reduce the ripple even further or for improved dynamic response. Again, use low-ESR ceramic (Murata GRM32 series) or polymer capacitors. Mount these close to the converter. Measure the output ripple under your load conditions.

Use only as much capacitance as required to achieve your ripple and noise objectives. Excessive capacitance can make step load recovery sluggish or possibly introduce instability. Do not exceed the maximum rated output capacitance listed in the specifications.

#### Input Ripple Current and Output Noise

All models in this converter series are tested and specified for input reflected ripple current and output noise using designated external input/output components, circuits and layout as shown in the figures below. The Cbus and Lbus components simulate a typical DC voltage bus.

#### **Minimum Output Loading Requirements**

All models regulate within specification and are stable under no load to full load conditions.

#### Thermal Shutdown (OTP, UTP)

To prevent many over temperature problems and damage, these converters include thermal shutdown circuitry. If environmental conditions cause the temperature of

#### TO OSCILLOSCOPE VIN $\downarrow$ + $\downarrow$ + $\downarrow$ - $\downarrow$ CBUS CIN + $\downarrow$ + $\downarrow$ CBUS CIN + $\downarrow$ - $\downarrow$ CBUS CIN + $\downarrow$ - $\downarrow$ - $\downarrow$ CBUS CIN - $\downarrow$ - $\downarrow$

the DC-DCs to rise above the Operating Temperature Range up to the shutdown temperature, an on-board electronic temperature sensor will power down the unit. When the temperature decreases below the turn-on threshold set in the command recover temp is (OT\_FAULT\_LIMIT-MFR\_OT\_FAULT\_HYS), the hysteresis is defined in general electrical specification section. The OTP and hysteresis of the module can be reconfigured using the PMBus. The OTP and UTP fault limit and fault response can be configured via the PMBus.

**<u>CAUTION</u>**: If you operate too close to the thermal limits, the converter may shut down suddenly without warning. Be sure to thoroughly test your application to avoid unplanned thermal shutdown.

#### **Temperature Derating Curves**

The graphs in this data sheet illustrate typical operation under a variety of conditions. The Derating curves show the maximum continuous ambient air temperature and decreasing maximum output current which is acceptable under increasing forced airflow measured in Linear Feet per Minute ("LFM"). Note that these are AVERAGE measurements. The converter will accept brief increases in current or reduced airflow as long as the average is not exceeded.

Note that the temperatures are of the ambient airflow, not the converter itself which is obviously running at higher temperature than the outside air. Also note that "natural convection" is defined as very flow rates which are not using fanforced airflow. Depending on the application, "natural convection" is usually about 30-65 LFM but is not equal to still air (0 LFM).

Murata Power Solutions makes Characterization measurements in a closed cycle wind tunnel with calibrated airflow. We use both thermocouples and an infrared camera system to observe thermal performance. As a practical matter, it is quite difficult to insert an anemometer to precisely measure airflow in most applications. Sometimes it is possible to estimate the effective airflow if you thoroughly understand the enclosure geometry, entry/exit orifice areas and the fan flowrate specifications.

**<u>CAUTION</u>**: If you exceed these Derating guidelines, the converter may have an unplanned Over Temperature shut down. Also, these graphs are all collected near Sea Level altitude. Be sure to reduce the derating for higher altitude.

#### **Output Short Circuit Condition**

The short circuit condition is an extension of the "Current Limiting" condition. When the monitored peak current signal reaches a certain range, the PWM controller's outputs are shut off thereby turning the converter "off." This is followed by an

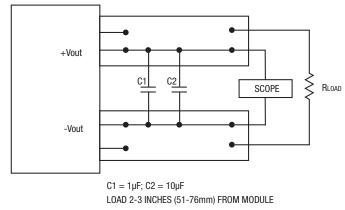


Figure 4. Measuring Output Ripple and Noise (PARD)

extended time out period. This period can vary depending on other conditions such as the input voltage level. Following this time out period, the PWM controller will attempt to re-start the converter by initiating a "normal start cycle" which includes softstart. If the "fault condition" persists, another "hiccup" cycle is initiated. This "cycle" can and will continue indefinitely until such time as the "fault condition" is removed, at which time the converter will resume "normal operation." Operating in the "hiccup" mode during a fault condition is advantageous in that average input and output power levels are held low preventing excessive internal increases in temperature.

#### Remote On/Off Control

The DBE series modules are equipped with both primary (On/Off 1, enabled, pull up internal) and secondary control pins. So far, the secondary control pin is always set as ON. The On/Off pins are TTL open-collector and/or CMOS open-drain compatible. (See general specifications for threshold voltage levels. See also MFR\_PRIMARY\_ON\_OFF\_CONFIG section.)

<u>Negative-logic</u> models are on (enabled) when the On/Off is grounded or brought to within a low voltage (see specifications) with respect to –Vin. The device is off (disabled) when the On/Off is left open or is pulled high to +13.5Vdc with respect to –Vin. The On/Off function allows the module to be turned on/off by an external device switch.

<u>Positive-logic</u> models are enabled when the On/Off pin is left open or is pulled high to +13.5V with respect to –Vin. Positive-logic devices are disabled when the On/Off is grounded or brought to within a low voltage (see specifications) with respect to –Vin.

The restart delay for this module to turn On/Off by the On/Off control pin is 100ms.

On/Off 1 can be configured by PMBus command MFR\_PRIMARY\_ON\_OFF\_ CONFIG (DDh); default configuration is not ignored; required On/Off 1 control pin to be asserted to start the unit.

DBE's On/Off status is dependent on On/Off 1 control, and OPERATION (PMBus command) status; both must be ON to turn DBE on; if one of them is OFF, unit will be turned off.

#### **Output Capacitive Load**

These converters do not require external capacitance added to achieve rated specifications. Users should only consider adding capacitance to reduce switching noise and/or to handle spike current load steps. Install only enough capacitance to achieve noise objectives. Excess external capacitance may cause degraded transient response and possible oscillation or instability.

# **DBE/DVE Series**

### **DBE/DVE Series**

# Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### **Remote Sense Input**

Use the Sense inputs with caution. Sense is normally connected **at the load**. Sense inputs compensate for output voltage inaccuracy delivered at the load. This is done by correcting IR voltage drops along the output wiring and the current carrying capacity of PC board etch. This output drop (the difference between Sense and Vout when measured at the converter) should not exceed 0.5V. Consider using heavier wire if this drop is excessive. Sense inputs also improve the stability of the converter and load system by optimizing the control loop phase margin.

Note: The Sense input and power Vout lines are internally connected through low value resistors to their respective polarities so that the converter can operate without external connection to the Sense. Nevertheless, if the Sense function is not used for remote regulation, the user should connect +Sense to +Vout and –Sense to –Vout at the converter pins.

The remote Sense lines carry very little current. They are also capacitively coupled to the output lines and therefore are in the feedback control loop to regulate and stabilize the output. As such, they are not low impedance inputs and must be treated with care in PC board layouts. Sense lines on the PCB should run adjacent

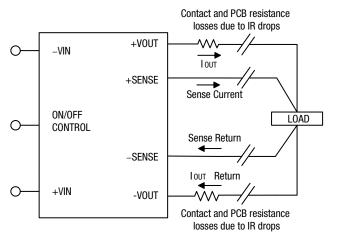


Figure 5. Remote Sense Circuit Configuration

#### **Soldering Guidelines**

Murata Power Solutions recommends the specifications below when installing these converters. These specifications vary depending on the solder type. Exceeding these specifications may cause damage to the product. Be cautious when there is high atmospheric humidity. We strongly recommend a mild pre-bake (100° C. for 30 minutes). Your production environment may differ; therefore please thoroughly review these guidelines with your process engineers.

Wave Solder Operations for through	h-hole mounted products (THMT)
For Sn/Ag/Cu based solders:	
Maximum Preheat Temperature	115° C.
Maximum Pot Temperature	270° C.
Maximum Solder Dwell Time	7 seconds
For Sn/Pb based solders:	
Maximum Preheat Temperature	105° C.
Maximum Pot Temperature	250° C.
Maximum Solder Dwell Time	6 seconds

Any long, distributed wiring and/or significant inductance introduced into the Sense control loop can adversely affect overall system stability. If in doubt, test your applications by observing the converter's output transient response during step loads. There should not be any appreciable ringing or oscillation. You may also adjust the output trim slightly to compensate for voltage loss in any external filter elements. Do not exceed maximum power ratings.

Please observe Sense inputs tolerance to avoid improper operation:

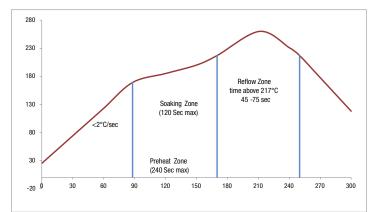
#### $[Vout(+) - Vout(-)] - [Sense(+) - Sense(-)] \le 10\%$ of Vout

Output overvoltage protection is monitored at the output voltage pin, not the Sense pin. Therefore excessive voltage differences between Vout and Sense together with trim adjustment of the output can cause the overvoltage protection circuit to activate and shut down the output.

Power derating of the converter is based on the combination of maximum output current and the highest output voltage. Therefore the designer must ensure:

(Vout at pins) x (lout)  $\leq$  (Max. rated output power)

#### **PIH Soldering Profile**



Optimal Peak 245°C, Max Peak 260°C. Lead-Free Solder type: SAC305 Duration Time above Liquidus: 45 to 75 seconds Ramp less than 1°C per second across liquidus Cooling rate less than 3°C per second

## **DBE/DVE Series**

Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

#### **Emissions Performance**

Murata Power Solutions measures its products for conducted emissions against the EN 55022 and CISPR 22 standards. Passive resistance loads are employed and the output is set to the maximum voltage. If you set up your own emissions testing, make sure the output load is rated at continuous power while doing the tests.

The recommended external input and output capacitors (if required) are included. Please refer to the fundamental switching frequency. All of this information is listed in the Product Specifications. An external discrete filter is installed and the circuit diagram is shown below.

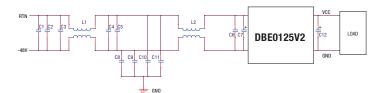


Figure 6. Conducted Emissions Test Circuit

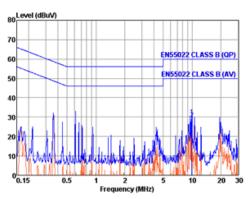
#### [1] Conducted Emissions Parts List

Reference	Part Number	Description	Vendor
C1, C2, C3, C4, C5	GRM32ER72A105KA01L	SMD CERAMIC-100V- 1000nF-X7R-1210	Murata
C6	GRM319R72A104KA01D	SMD CERAMIC 100V-100nF- ±10%-X7R-1206	Murata
L1, L2	PG0060T	COMMON MODE-473uH- ±25%-A	Pulse
C8, C9, C10, C11	GRM55DR72J224KW01L	SMD CERAMIC 630V-0.22uF- ±10%-X7R-2220	Murata
C7	UHE2A221MHD	Aluminum100V-220Uf- ±10%-long lead	Nichicon
C12	NA		

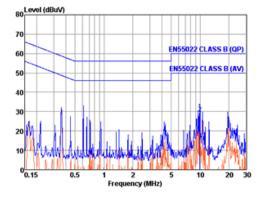
#### [2] Conducted Emissions Test Equipment Used

Hewlett Packard HP8594L Spectrum Analyzer – S/N 3827A00153 2Line V-networks LS1-15V 50Ω/50Uh Line Impedance Stabilization Network

#### [3] Conducted Emissions Test Results



Graph 1. Conducted emissions performance, Positive Line, CISPR 22, Class B, full load



Graph 2. Conducted emissions performance, Negative Line, CISPR 22, Class B, full load

#### [4] Layout Recommendations

Most applications can use the filtering which is already installed inside the converter or with the addition of the recommended external capacitors. For greater emissions suppression, consider additional filter components and/or shielding. Emissions performance will depend on the user's PC board layout, the chassis shielding environment and choice of external components. Please refer to Application Note GEAN-02 for further discussion.

Since many factors affect both the amplitude and spectra of emissions, we recommend using an engineer who is experienced at emissions suppression.

# **DBE/DVE** Series

Up to 300W Digital Fully Regulated Intermediate DC-DC Bus Converter

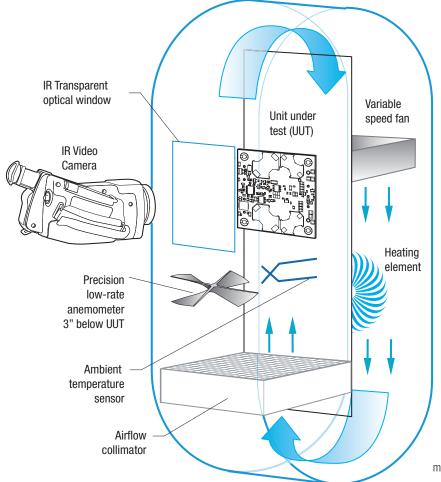


Figure 7. Vertical Wind Tunnel

#### Vertical Wind Tunnel

Murata Power Solutions employs a computer controlled custom-designed closed loop vertical wind tunnel, infrared video camera system, and test instrumentation for accurate airflow and heat dissipation analysis of power products. The system includes a precision low flow-rate anemometer, variable speed fan, power supply input and load controls, temperature gauges, and adjustable heating element.

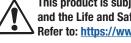
The IR camera monitors the thermal performance of the Unit Under Test (UUT) under static steady-state conditions. A special optical port is used which is transparent to infrared wavelengths.

Both through-hole and surface mount converters are soldered down to a 10" x 10" host carrier board for realistic heat absorption and spreading. Both longitudinal and transverse airflow studies are possible by rotation of this carrier board since there are often significant differences in the heat dissipation in the two airflow directions. The combination of adjustable airflow, adjustable ambient heat, and adjustable Input/Output currents and voltages mean that a very wide range of measurement conditions can be studied.

The collimator reduces the amount of turbulence adjacent to the UUT by minimizing airflow turbulence. Such turbulence influences the effective heat transfer characteristics and gives false readings. Excess turbulence removes more heat from some surfaces and less heat from others, possibly causing uneven overheating.

Both sides of the UUT are studied since there are different thermal gradients on each side. The adjustable heating element and fan, built-in temperature gauges, and no-contact IR camera mean that power supplies are tested in real-world conditions.

Murata Power Solutions, Inc. 129 Flanders Rd, Westborough, MA 01581 USA. ISO 9001 REGISTERED



This product is subject to the following operating requirements and the Life and Safety Critical Application Sales Policy: Refer to: https://www.murata-ps.com/requirements/

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