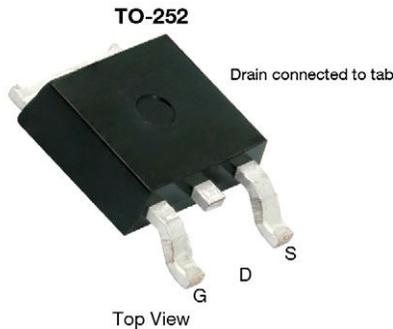


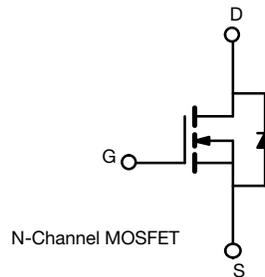
Automotive N-Channel 40 V (D-S) 175 °C MOSFET



FEATURES

- TrenchFET® power MOSFET
- Package with low thermal resistance
- 100 % R_G and UIS tested
- AEC-Q101 qualified
- Material categorization:
for definitions of compliance please see www.vishay.com/doc?99912

 AUTOMOTIVE
GRADE

RoHS
COMPLIANT
HALOGEN
FREE


PRODUCT SUMMARY	
V _{DS} (V)	40
R _{DS(on)} (Ω) at V _{GS} = 10 V	0.0060
R _{DS(on)} (Ω) at V _{GS} = 4.5 V	0.0078
I _D (A)	30
Configuration	Single
Package	TO-252

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V _{DS}	40	V
Gate-source voltage		V _{GS}	± 20	
Continuous drain current ^a	T _C = 25 °C	I _D	30	A
	T _C = 125 °C		30	
Continuous source current (diode conduction) ^a		I _S	30	
Pulsed drain current ^b		I _{DM}	120	
Single pulse avalanche current	L = 0.1 mH	I _{AS}	24	
Single pulse avalanche energy		E _{AS}	28.8	
Maximum power dissipation	T _C = 25 °C	P _D	62	W
	T _C = 125 °C		20	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB mount ^c	R _{thJA}	50	°C/W
Junction-to-case (drain)		R _{thJC}	2.4	

Notes

- Package limited
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- When mounted on 1" square PCB (FR4 material)



SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	40	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.2	1.7	2.2	
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 40 V	-	-	1	μA
		V _{GS} = 0 V, V _{DS} = 40 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V, V _{DS} = 40 V, T _J = 175 °C	-	-	250	μA
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V, V _{DS} ≥ 5 V	20	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 15 A	-	0.0046	0.0060	Ω
		V _{GS} = 4.5 V, I _D = 10 A	-	0.0060	0.0078	
		V _{GS} = 10 V, I _D = 15 A, T _J = 125 °C	-	-	0.0091	
		V _{GS} = 10 V, I _D = 15 A, T _J = 175 °C	-	-	0.0107	
Forward transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 15 A	-	62	-	S
Dynamic ^b						
Input capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz	-	1844	2600	pF
Output capacitance	C _{oss}		-	527	750	
Reverse transfer capacitance	C _{rss}		-	56	80	
Total gate charge ^c	Q _g	V _{GS} = 10 V, V _{DS} = 20 V, I _D = 15 A	-	34.5	52	nC
Gate-source charge ^c	Q _{gs}		-	6.1	-	
Gate-drain charge ^c	Q _{gd}		-	6.6	-	
Gate resistance	R _g	f = 1 MHz	1.45	2.91	4.37	Ω
Turn-on delay time ^c	t _{d(on)}	V _{DD} = 20 V, R _L = 1.33 Ω I _D ≅ 15 A, V _{GEN} = 10 V, R _g = 1 Ω	-	12	25	ns
Rise time ^c	t _r		-	7	15	
Turn-off delay time ^c	t _{d(off)}		-	28	50	
Fall time ^c	t _f		-	10	20	
Source-Drain Diode Ratings and Characteristics ^b						
Pulsed current ^a	I _{SM}		-	-	120	A
Forward voltage	V _{SD}	I _F = 15 A, V _{GS} = 0 V	-	0.83	1.5	V
Body diode reverse recovery time	t _{rr}	I _F = 10 A, di/dt = 100 A/μs	-	26	52	ns
Body diode reverse recovery charge	Q _{rr}		-	13	26	nC
Reverse recovery fall time	t _a		-	11	-	ns
Reverse recovery rise time	t _b		-	15	-	
Body diode peak reverse recovery current	I _{RM(REC)}			-	-0.86	-

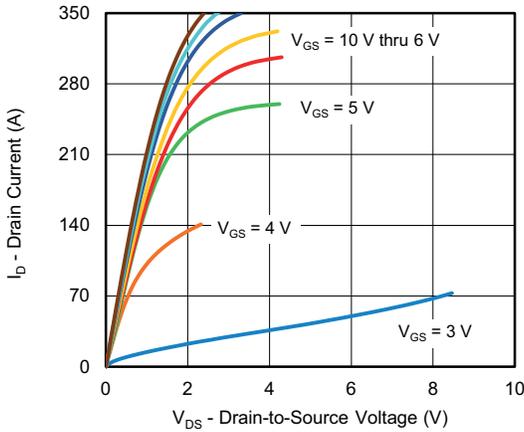
Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

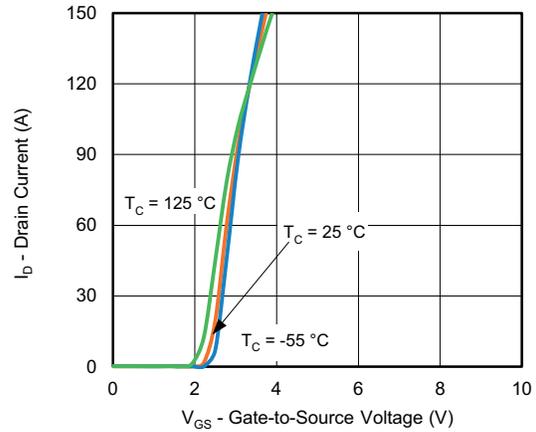
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



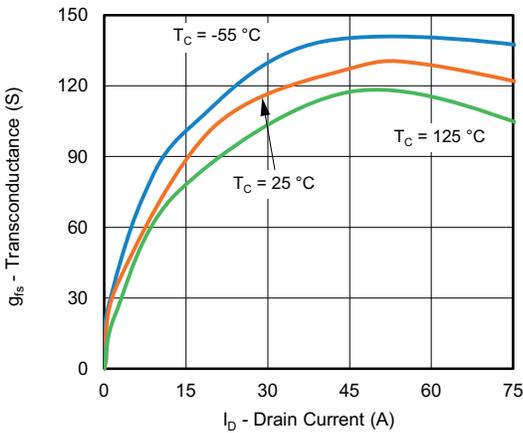
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



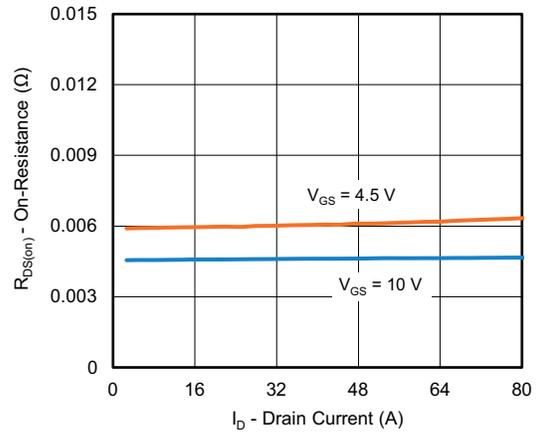
Output Characteristics



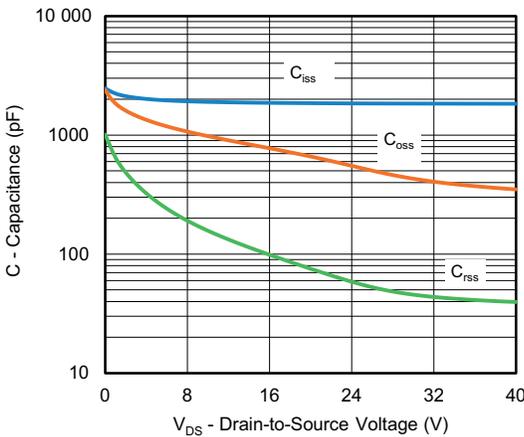
Transfer Characteristics



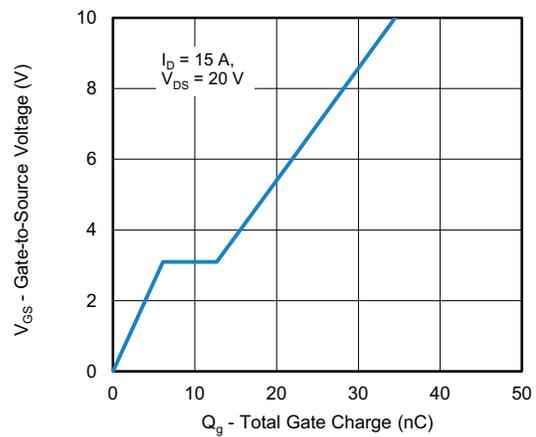
Transconductance



On-Resistance vs. Drain Current



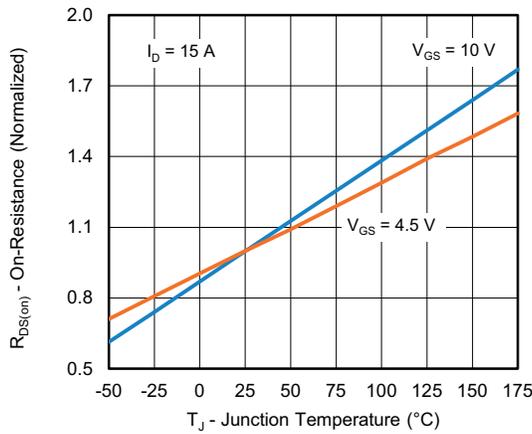
Capacitance



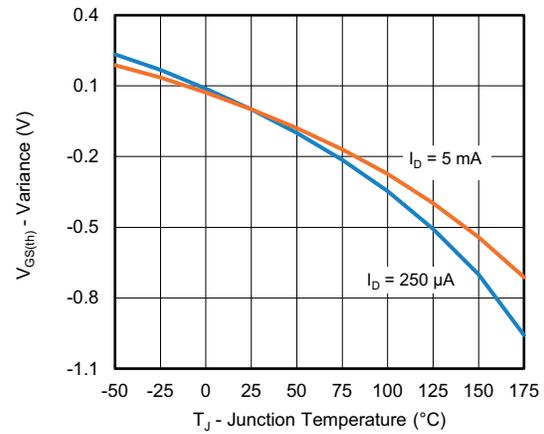
Gate Charge



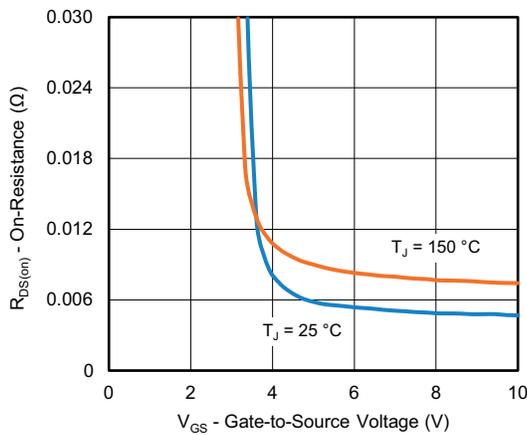
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



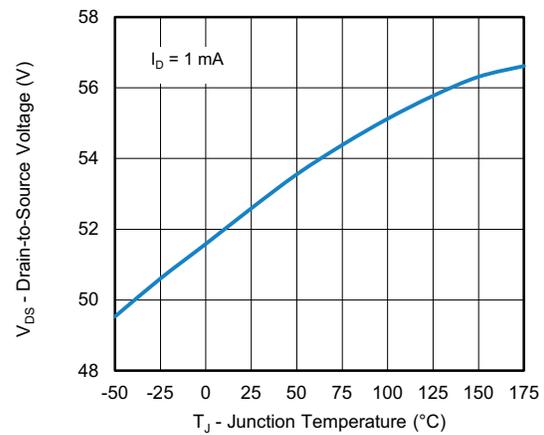
On-Resistance vs. Junction Temperature



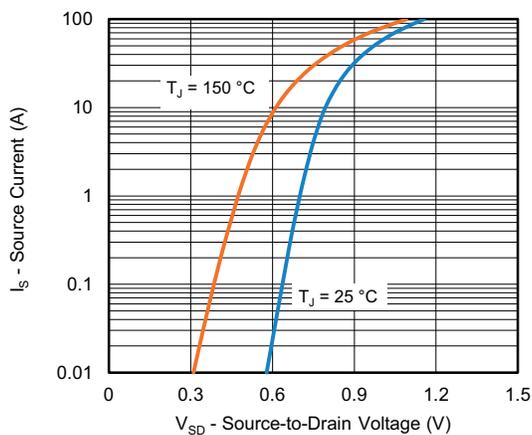
Threshold Voltage



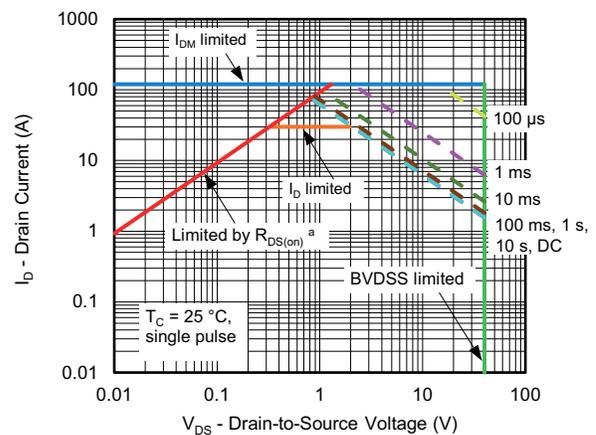
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



Source Drain Diode Forward Voltage



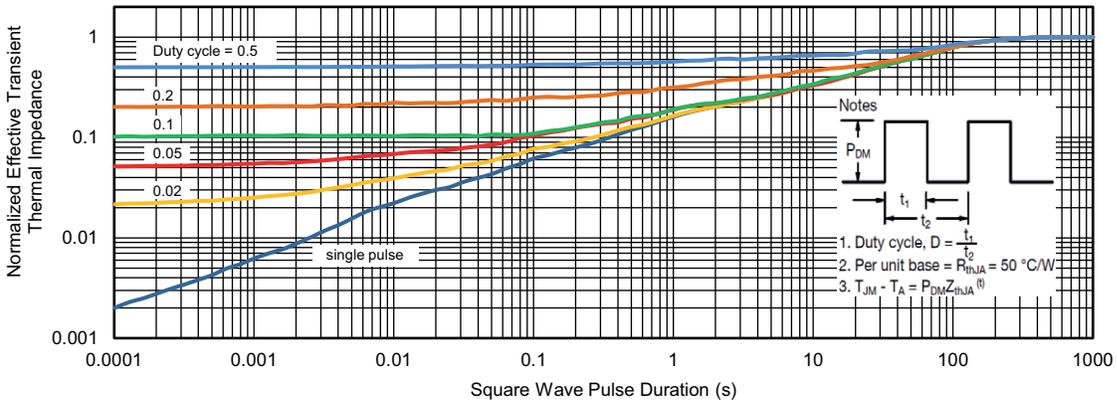
Safe Operating Area

Note

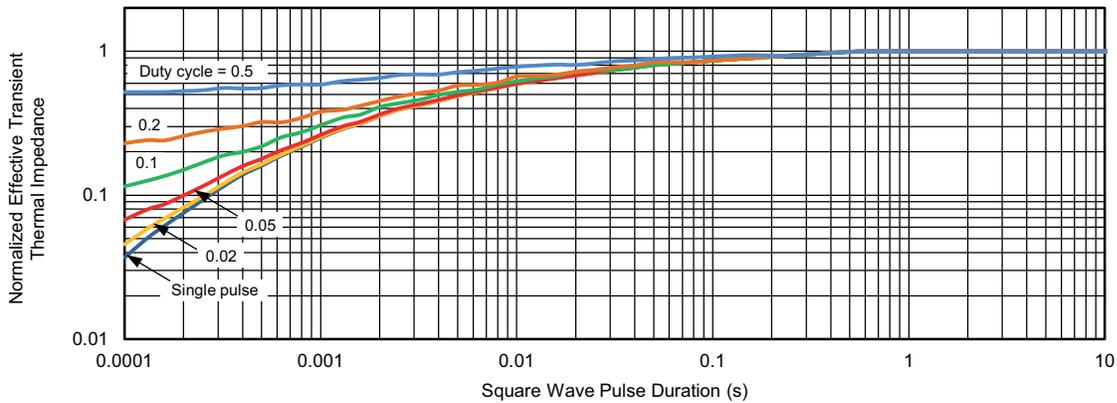
- a. V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified



THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

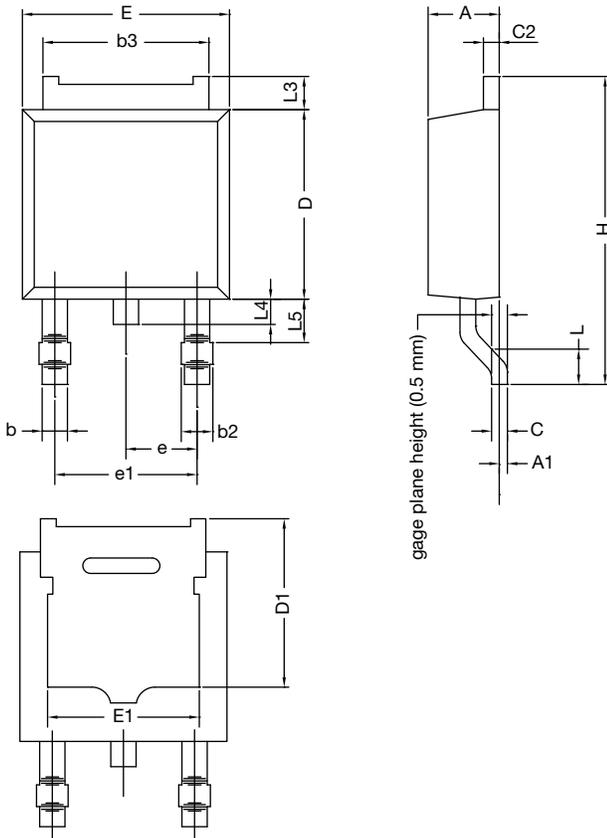
Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^\circ\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Case ($25\text{ }^\circ\text{C}$)
 are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?77607.



TO-252AA Case Outline

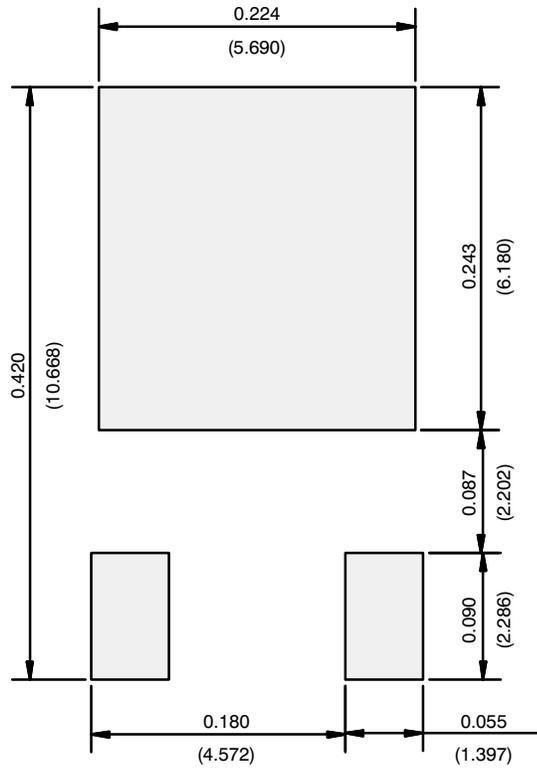


DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.38	0.086	0.094
A1	-	0.127	-	0.005
b	0.64	0.88	0.025	0.035
b2	0.76	1.14	0.030	0.045
b3	4.95	5.46	0.195	0.215
C	0.46	0.61	0.018	0.024
C2	0.46	0.89	0.018	0.035
D	5.97	6.22	0.235	0.245
D1	4.10	-	0.161	-
E	6.35	6.73	0.250	0.265
E1	4.32	-	0.170	-
H	9.40	10.41	0.370	0.410
e	2.28 BSC		0.090 BSC	
e1	4.56 BSC		0.180 BSC	
L	1.40	1.78	0.055	0.070
L3	0.89	1.27	0.035	0.050
L4	-	1.02	-	0.040
L5	1.01	1.52	0.040	0.060
ECN: T13-0592-Rev. A, 02-Sep-13 DWG: 6019				

Note

- Dimension L3 is for reference only.

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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