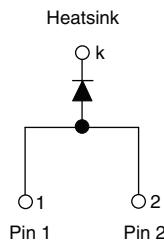


Hyperfast Rectifier, 8 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



| PRIMARY CHARACTERISTICS | |
|-------------------------|---------------------|
| $I_{F(AV)}$ | 8 A |
| V_R | 600 V |
| V_F at I_F | 1.3 V |
| t_{rr} (typ.) | 16 ns |
| T_J max. | 175 °C |
| Package | SlimDPAK (TO-252AE) |
| Circuit configuration | Single |

FEATURES

- Hyperfast recovery time, reduced Q_{rr} and soft recovery
- For PFC CRM / CCM operation
- Low forward voltage drop, low power losses
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc/99912



RoHS
COMPLIANT
HALOGEN
FREE

TYPICAL APPLICATIONS

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters, or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating

Base PN-/M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

| ABSOLUTE MAXIMUM RATINGS | | | | |
|---|-------------------|--------------------------------------|-------------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| Peak repetitive reverse voltage | V_{RRM} | | 600 | V |
| Average rectified forward current | $I_{F(AV)}$ | $T_C = 144$ °C | 8 | A |
| Non-repetitive peak surge current | I_{FSM} | $T_J = 25$ °C, 10 ms sine pulse wave | 90 | |
| Operating junction and storage temperatures | T_J , T_{Stg} | | -55 to +175 | °C |

| ELECTRICAL SPECIFICATIONS ($T_J = 25$ °C unless otherwise specified) | | | | | | |
|---|------------------|-----------------------------------|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Breakdown voltage, blocking voltage | V_{BR} , V_R | $I_R = 100$ µA | 600 | - | - | V |
| Forward voltage | V_F | $I_F = 8$ A | - | 1.6 | 2.40 | |
| | | $I_F = 8$ A, $T_J = 150$ °C | - | 1.3 | 1.7 | |
| Reverse leakage current | I_R | $V_R = V_R$ rated | - | - | 20 | µA |
| | | $T_J = 150$ °C, $V_R = V_R$ rated | - | - | 500 | |
| Junction capacitance | C_T | $V_R = 600$ V | - | 12 | - | pF |

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
|-------------------------|-----------|--|------|------|------|-------|
| Reverse recovery time | t_{rr} | $I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$ | - | 21 | - | ns |
| | | $I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$ | - | 16 | - | |
| | | $I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{RR} = 0.25 \text{ A}$ | - | - | 25 | |
| | | $T_J = 25^\circ\text{C}$ | - | 25 | - | |
| | | $T_J = 125^\circ\text{C}$ | - | 65 | - | |
| Peak recovery current | I_{RRM} | $T_J = 25^\circ\text{C}$ | - | 4.9 | - | A |
| | | $T_J = 125^\circ\text{C}$ | - | 8.2 | - | |
| Reverse recovery charge | Q_{rr} | $T_J = 25^\circ\text{C}$ | - | 90 | - | nC |
| | | $T_J = 125^\circ\text{C}$ | - | 260 | - | |

THERMAL - MECHANICAL SPECIFICATIONS

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
|--|----------------|--------------------------------|--------|------|------|-------|
| Maximum junction and storage temperature range | T_J, T_{Stg} | | -55 | - | 175 | °C |
| Thermal resistance, junction to mount | R_{thJM} | | - | - | 2.2 | °C/W |
| Marking device | | Case style SlimDPAK (TO-252AE) | 8EVH06 | | | |

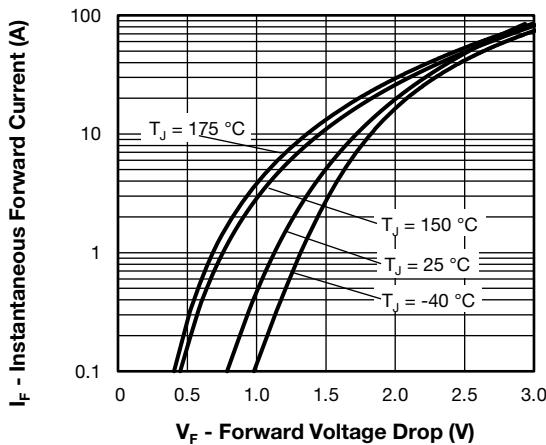


Fig. 1 - Typical Forward Voltage Drop Characteristics

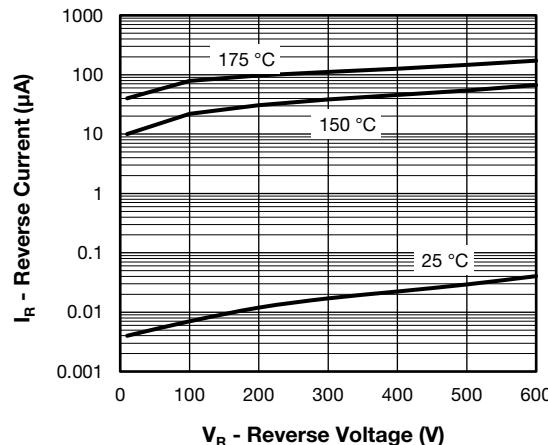


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

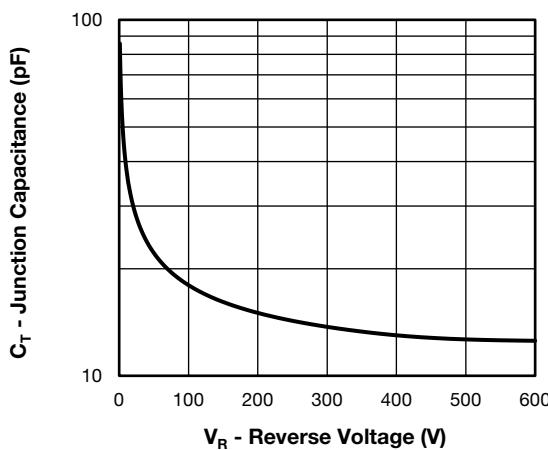


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

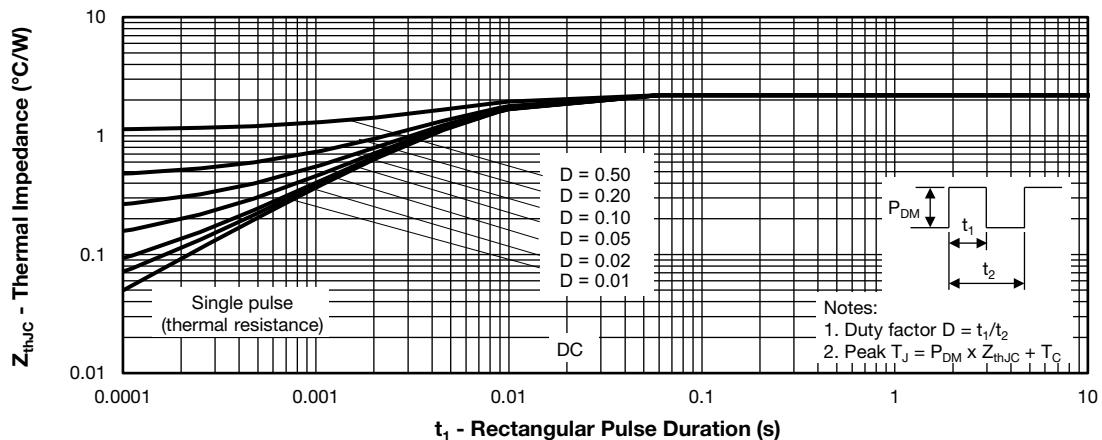


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

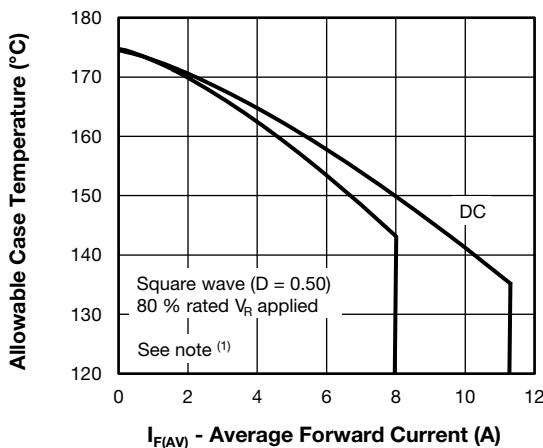


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

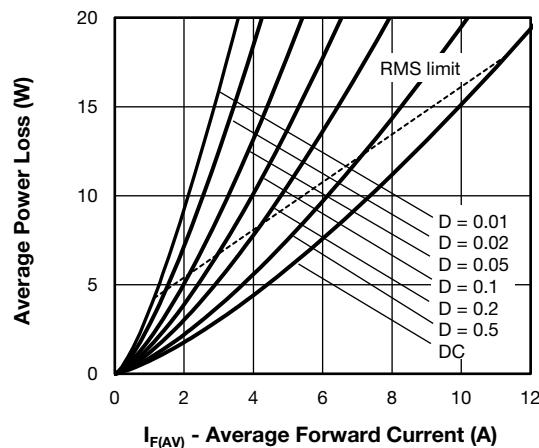


Fig. 6 - Forward Power Loss Characteristics

Note

(1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 $P_d = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D)$ (see fig. 6);
 $P_{dREV} = \text{inverse power loss} = V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = rated V_R

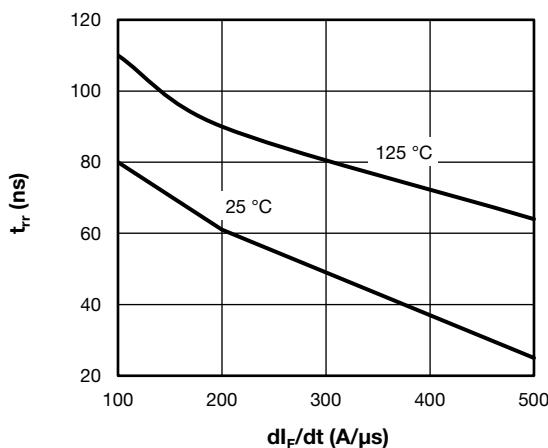


Fig. 7 - Typical Reverse Recovery Time vs. di_F/dt

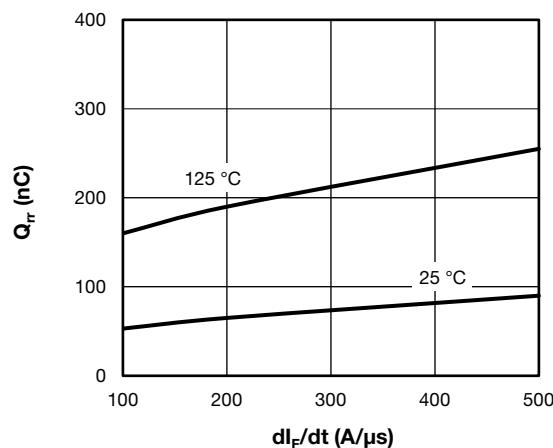


Fig. 8 - Typical Stored Charge vs. di_F/dt

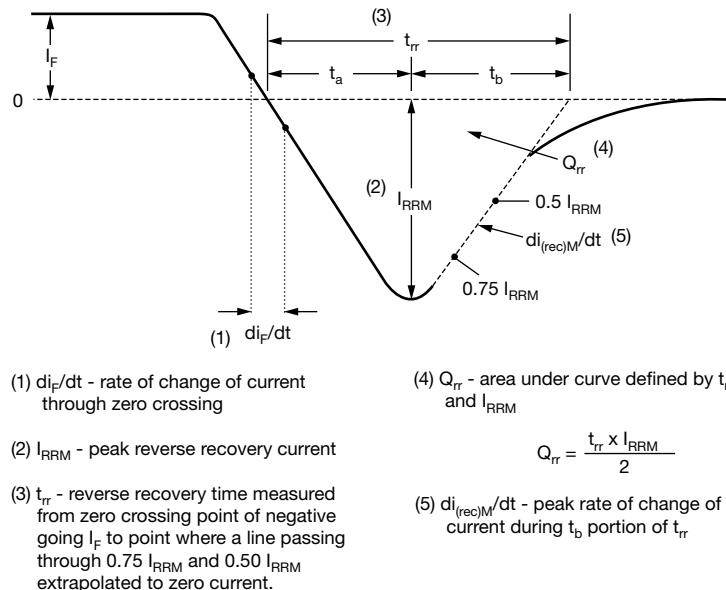
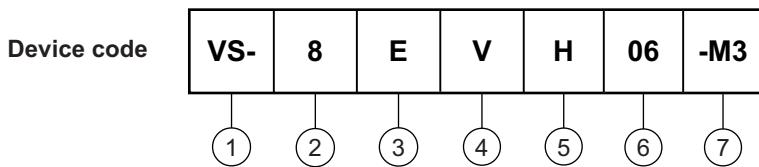


Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE


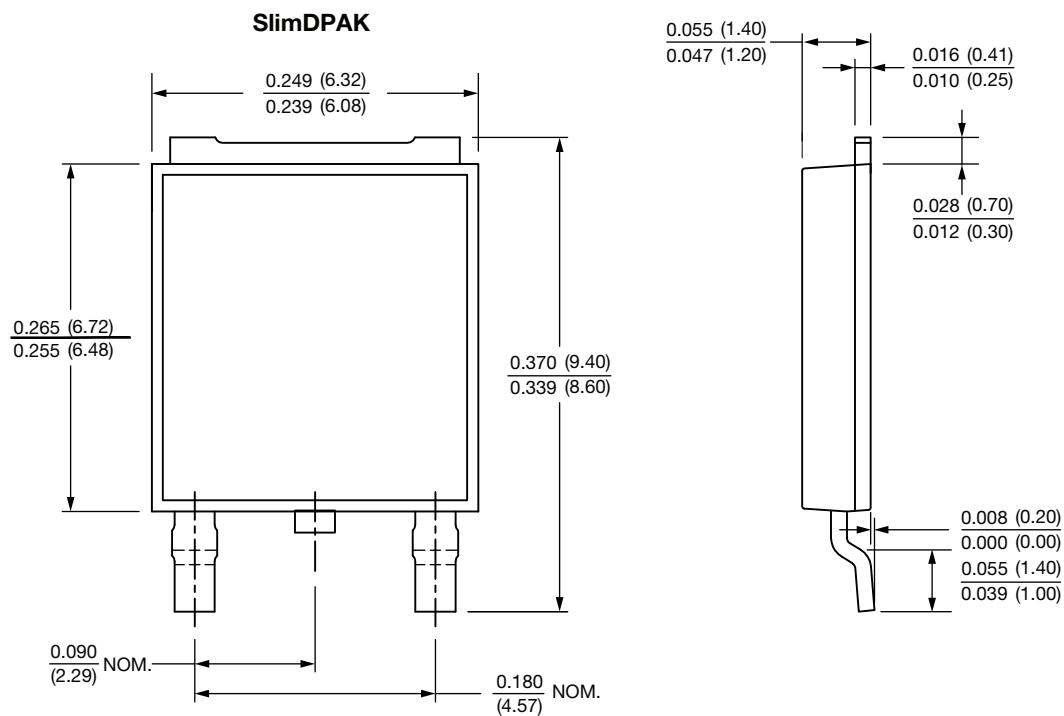
- (1)** - Vishay Semiconductors product
- (2)** - Current rating (8 = 8 A)
- (3)** - Circuit configuration:
E = single die
- (4)** - V = SlimDPAK
- (5)** - Process type:
H = hyperfast recovery
- (6)** - Voltage code (06 = 600 V)
- (7)** - Environmental digit:
M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

| ORDERING INFORMATION (Example) | | | | |
|---------------------------------------|-----------------|------------------------|---------------|------------------------------------|
| PREFERRED P/N | UNIT WEIGHT (g) | PREFERRED PACKAGE CODE | BASE QUANTITY | PACKAGING DESCRIPTION |
| VS-8EVH06-M3/I | 0.20 | I | 4500 | 13" diameter plastic tape and reel |

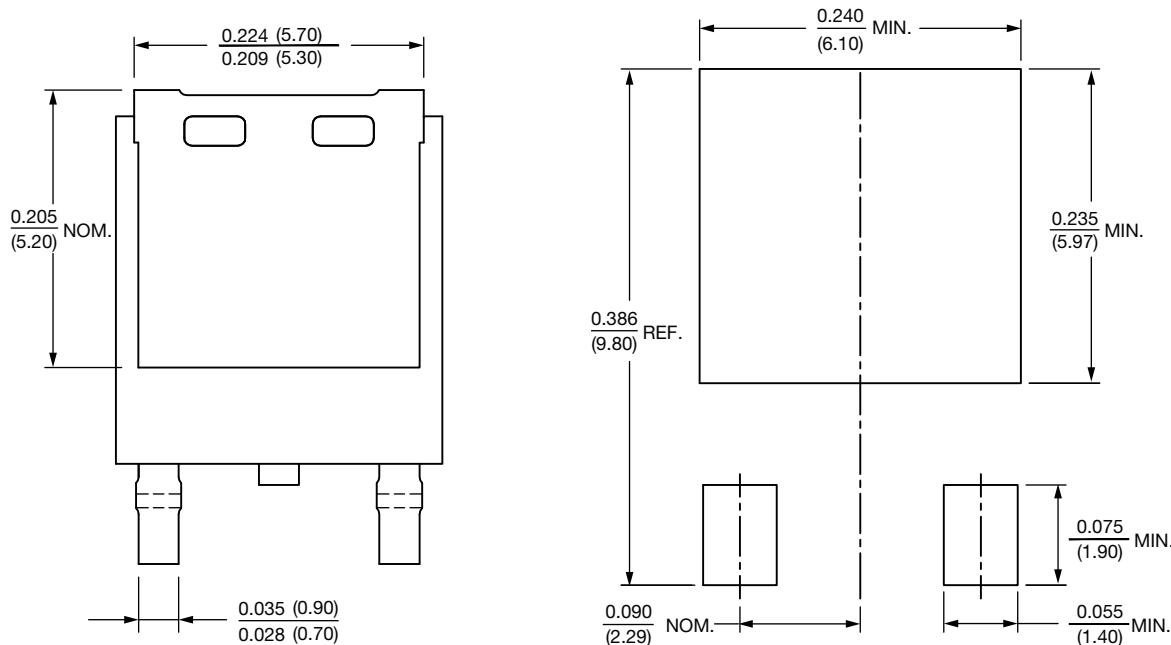
| LINKS TO RELATED DOCUMENTS | |
|-----------------------------------|--|
| Dimensions | www.vishay.com/doc?96081 |
| Part marking information | www.vishay.com/doc?96085 |
| Packaging information | www.vishay.com/doc?88869 |

SlimDPAK

DIMENSIONS in inches (millimeters)



Mounting Pad Layout



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