



NX138AK

60 V, N-channel Trench MOSFET

10 June 2021

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

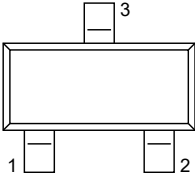
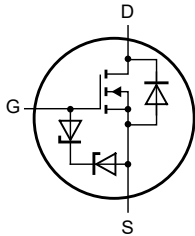
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ °C}$	-	-	60	V
V_{GS}	gate-source voltage		-20	-	20	V
I_D	drain current	$V_{GS} = 10\text{ V}; T_{amb} = 25\text{ °C}$	[1]	-	190	mA
Static characteristics						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 10\text{ V}; I_D = 190\text{ mA}; T_j = 25\text{ °C}$	-	3	4.5	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm^2 .

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 SOT23	 017aaa255
2	S	source		
3	D	drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
NX138AK	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
NX138AK	AP%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	60	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	190	mA
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	120	mA
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	765	mA
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	265	mW
			[1]	-	325	mW
		T _{sp} = 25 °C		-	1.33	W
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	190	mA

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

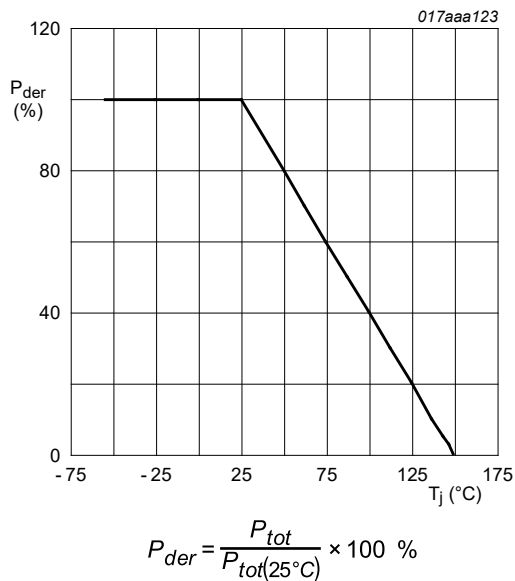


Fig. 1. Normalized total power dissipation as a function of junction temperature

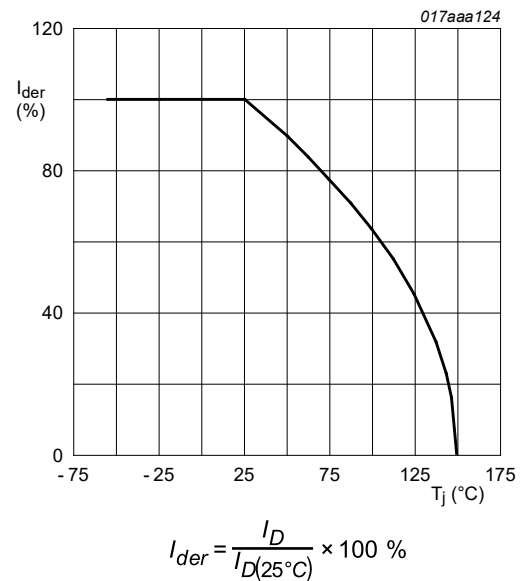
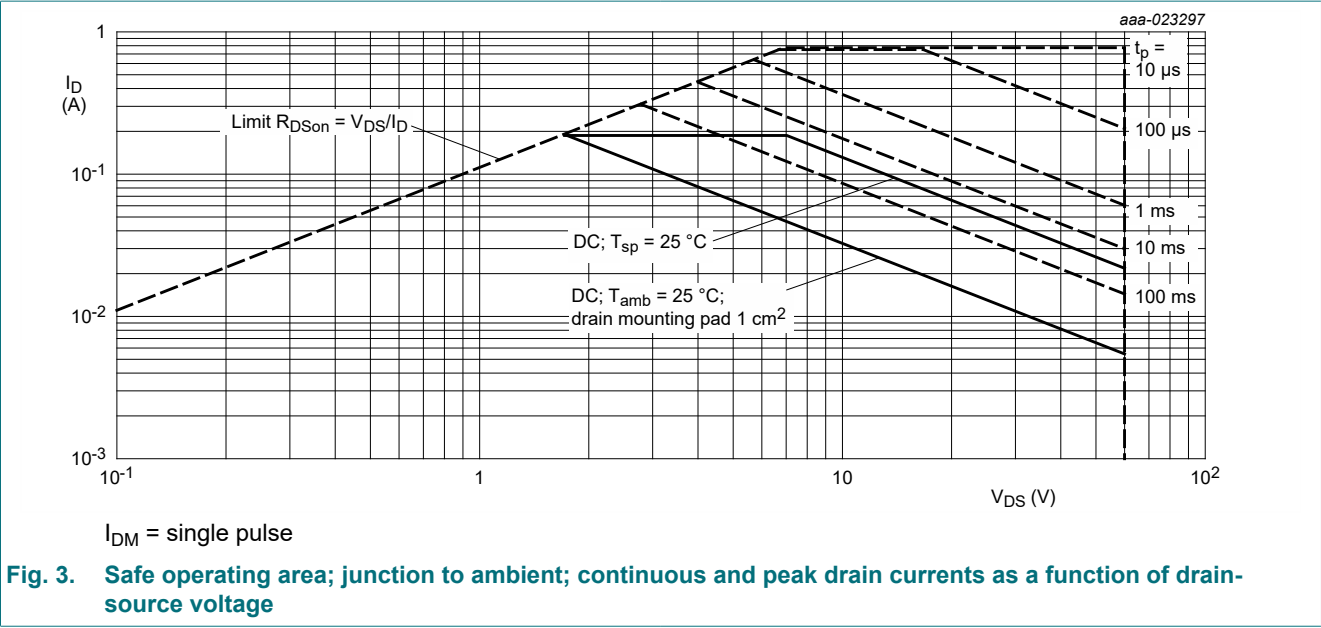


Fig. 2. Normalized continuous drain current as a function of junction temperature

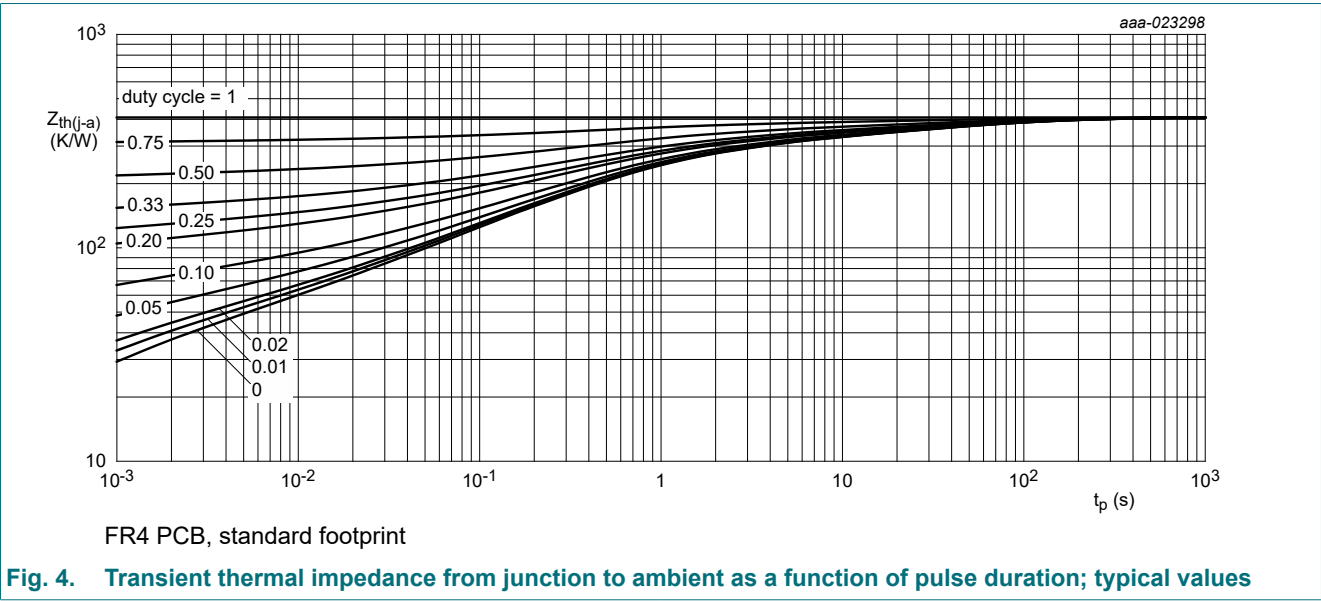


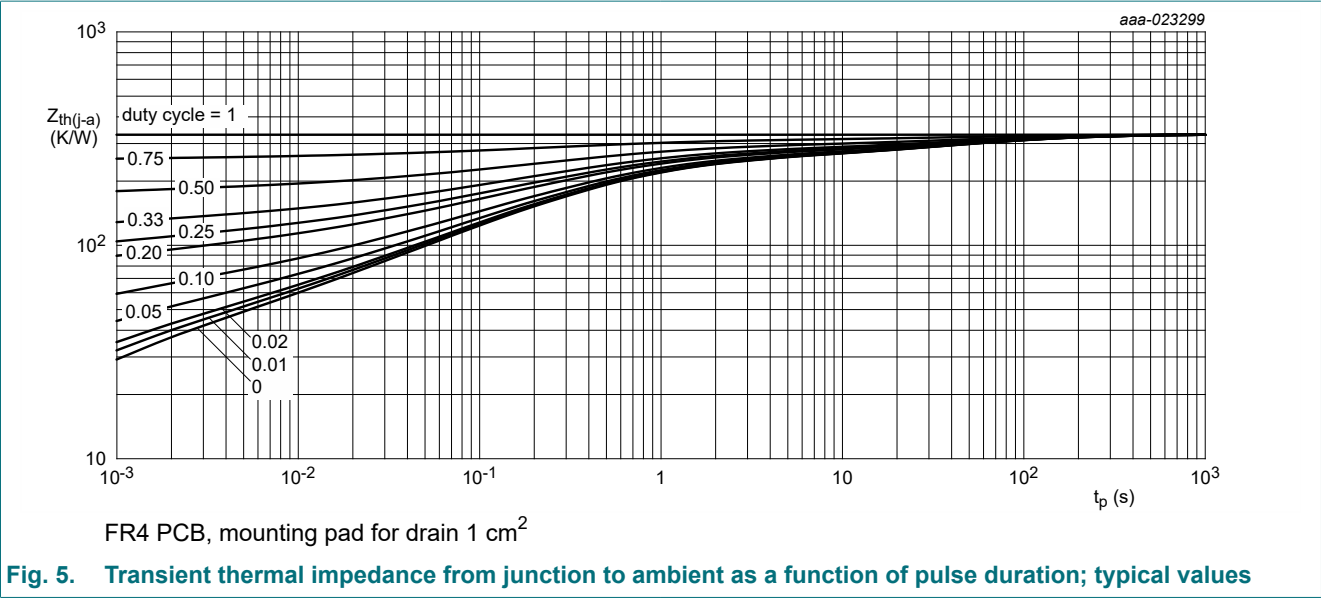
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	410	470	K/W
			[2]	-	330	380	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	80	95	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm^2 .





10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C		60	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} =V _{GS} ; T _j = 25 °C		0.8	1.1	1.5	V
I _{DSS}	drain leakage current	V _{DS} = 60 V; V _{GS} = 0 V; T _j = 25 °C		-	-	1	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C		-	-	2	μA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-2	μA
		V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C		-	-	0.5	μA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-0.5	μA
		V _{GS} = 5 V; V _{DS} = 0 V; T _j = 25 °C		-	-	100	nA
		V _{GS} = -5 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 190 mA; T _j = 25 °C		-	3	4.5	Ω
		V _{GS} = 10 V; I _D = 190 mA; T _j = 150 °C		-	6	9	Ω
		V _{GS} = 5 V; I _D = 170 mA; T _j = 25 °C		-	4	5.2	Ω
		V _{GS} = 2.5 V; I _D = 130 mA; T _j = 25 °C		-	5	10	Ω
g _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 180 mA; T _j = 25 °C		-	3.5	-	S
Dynamic characteristics							
Q _{G(tot)}	total gate charge	V _{DS} = 30 V; I _D = 190 mA; V _{GS} = 10 V; T _j = 25 °C		-	0.9	1.4	nC
Q _{GS}	gate-source charge			-	0.1	-	nC
Q _{GD}	gate-drain charge			-	0.2	-	nC
C _{iss}	input capacitance	V _{DS} = 30 V; f = 1 MHz; V _{GS} = 0 V; T _j = 25 °C		-	15	20	pF
C _{oss}	output capacitance			-	2.3	-	pF
C _{rss}	reverse transfer capacitance			-	1.5	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 30 V; I _D = 190 mA; V _{GS} = 10 V; R _{G(ext)} = 75 Ω; T _j = 25 °C		-	8	12	ns
t _r	rise time			-	10	-	ns
t _{d(off)}	turn-off delay time			-	8	20	ns
t _f	fall time			-	5	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	I _S = 190 mA; V _{GS} = 0 V; T _j = 25 °C		-	0.8	1.2	V

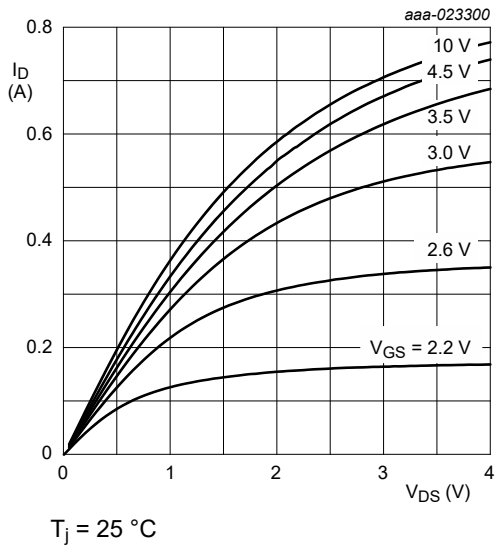


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

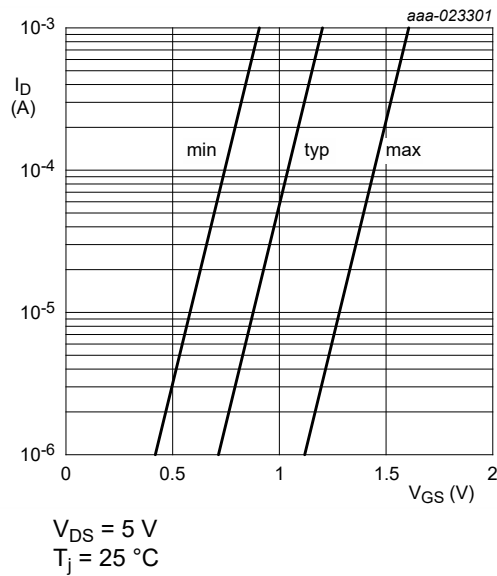


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

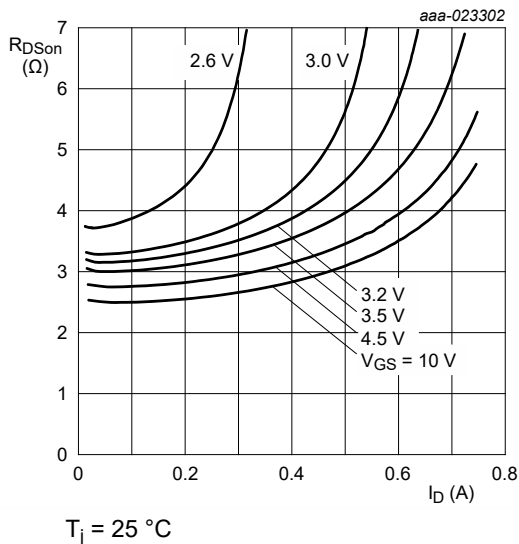


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

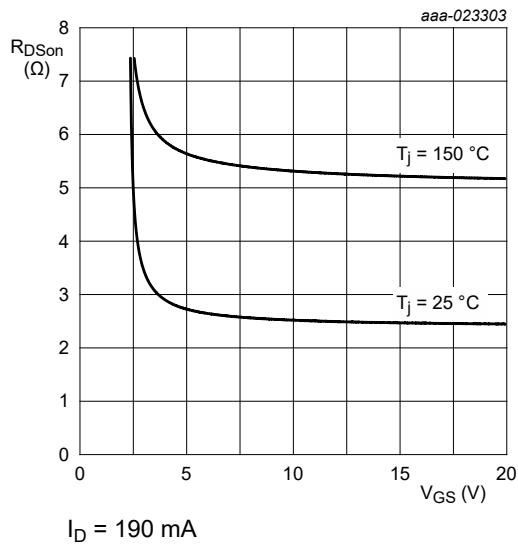


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

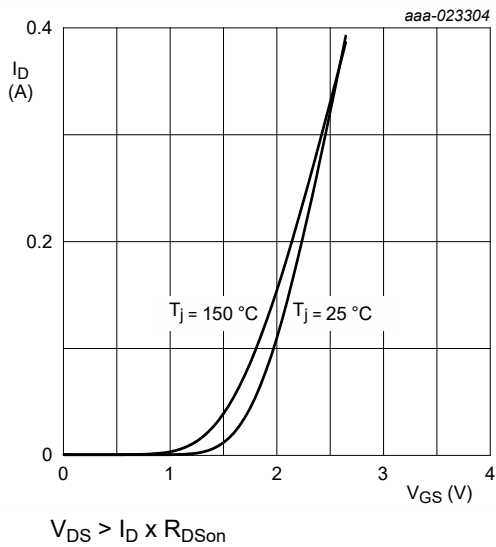


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

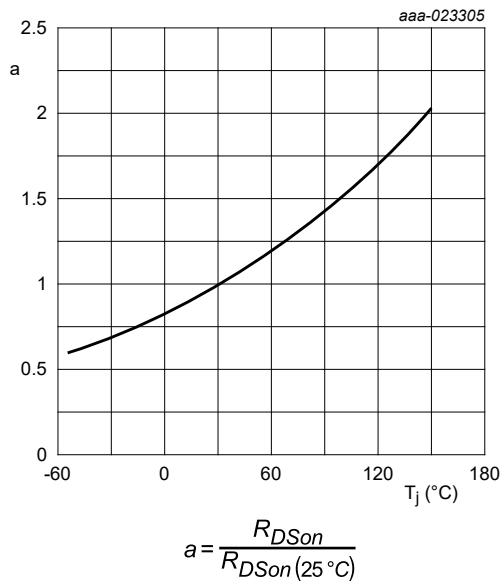


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

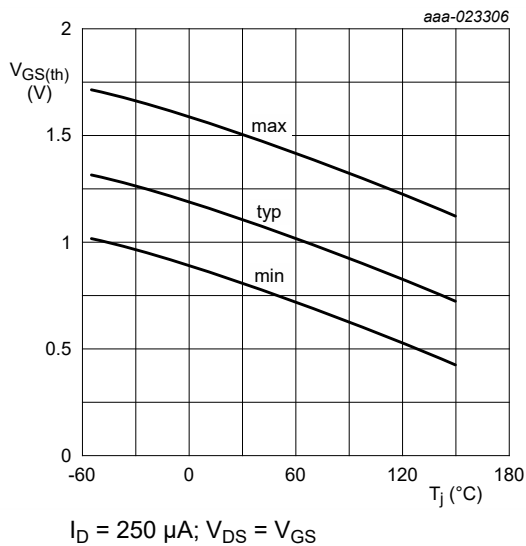


Fig. 12. Gate-source threshold voltage as a function of junction temperature

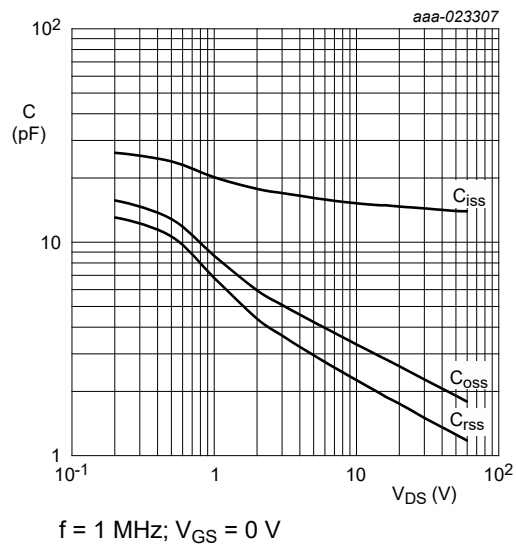


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

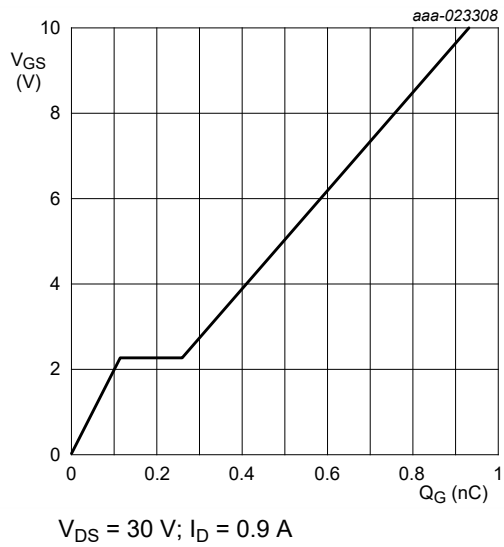


Fig. 14. Gate-source voltage as a function of gate charge; typical values

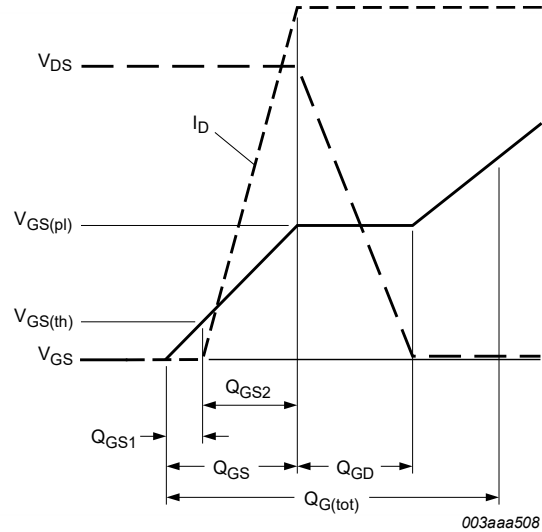


Fig. 15. Gate charge waveform definitions

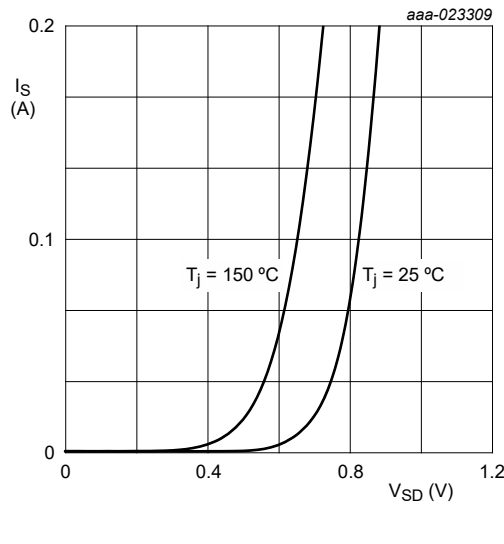


Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

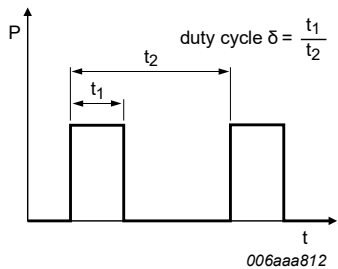


Fig. 17. Duty cycle definition

12. Package outline

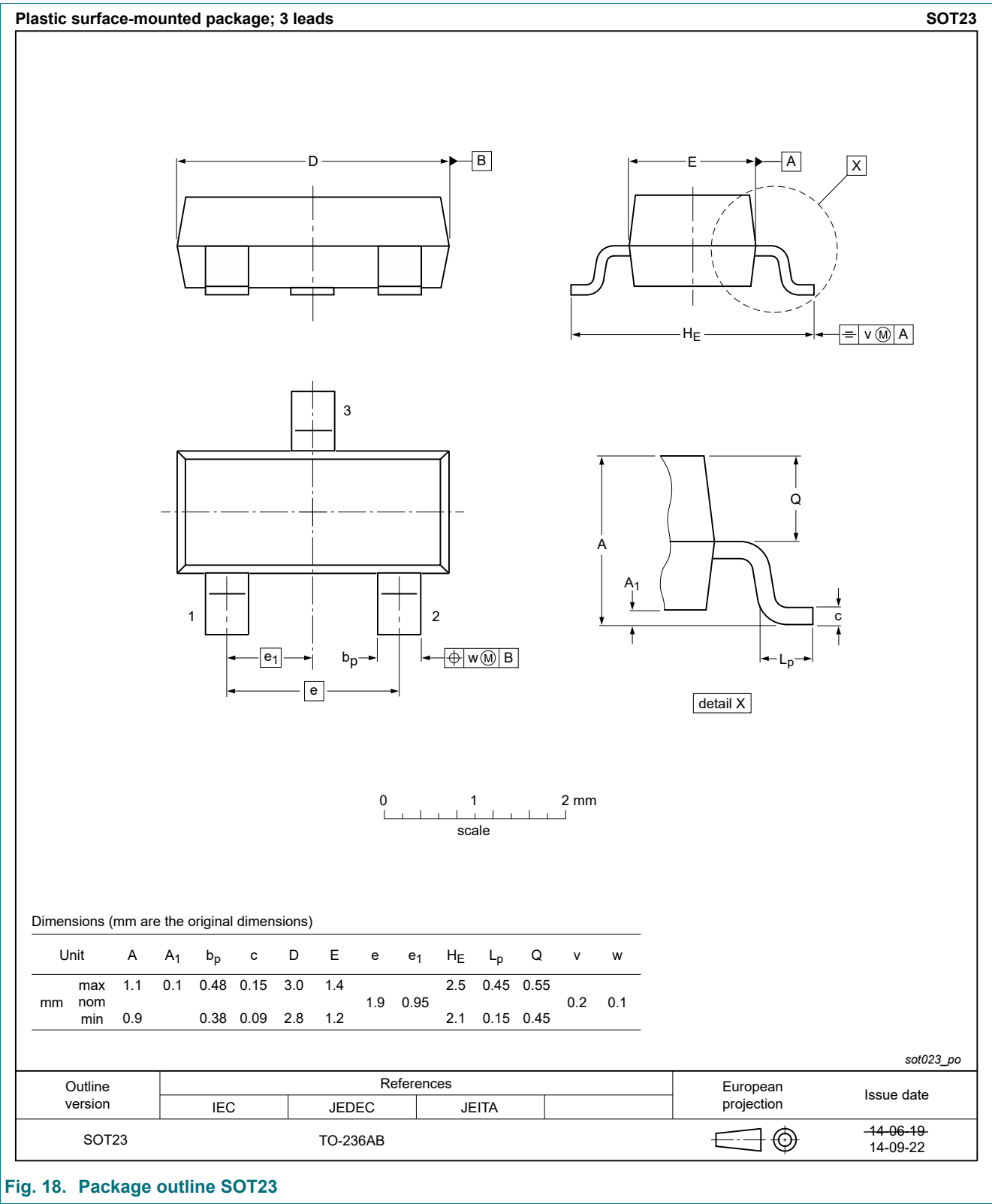


Fig. 18. Package outline SOT23

13. Soldering

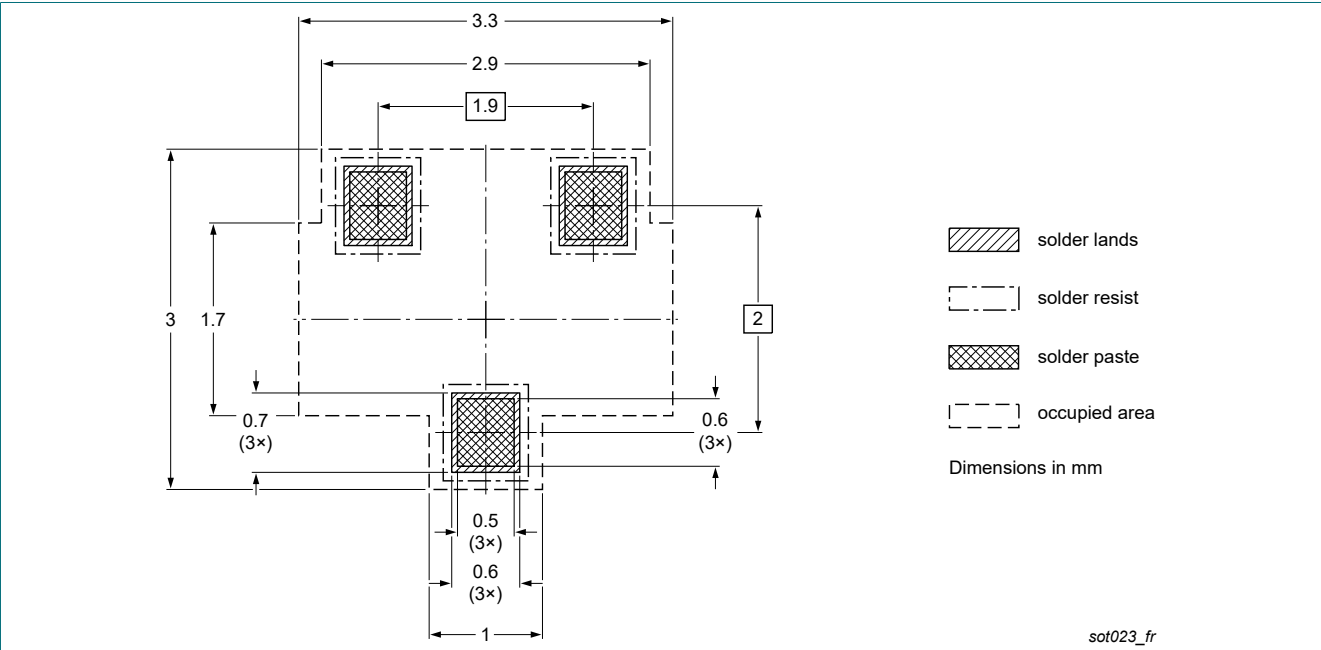


Fig. 19. Reflow soldering footprint for SOT23

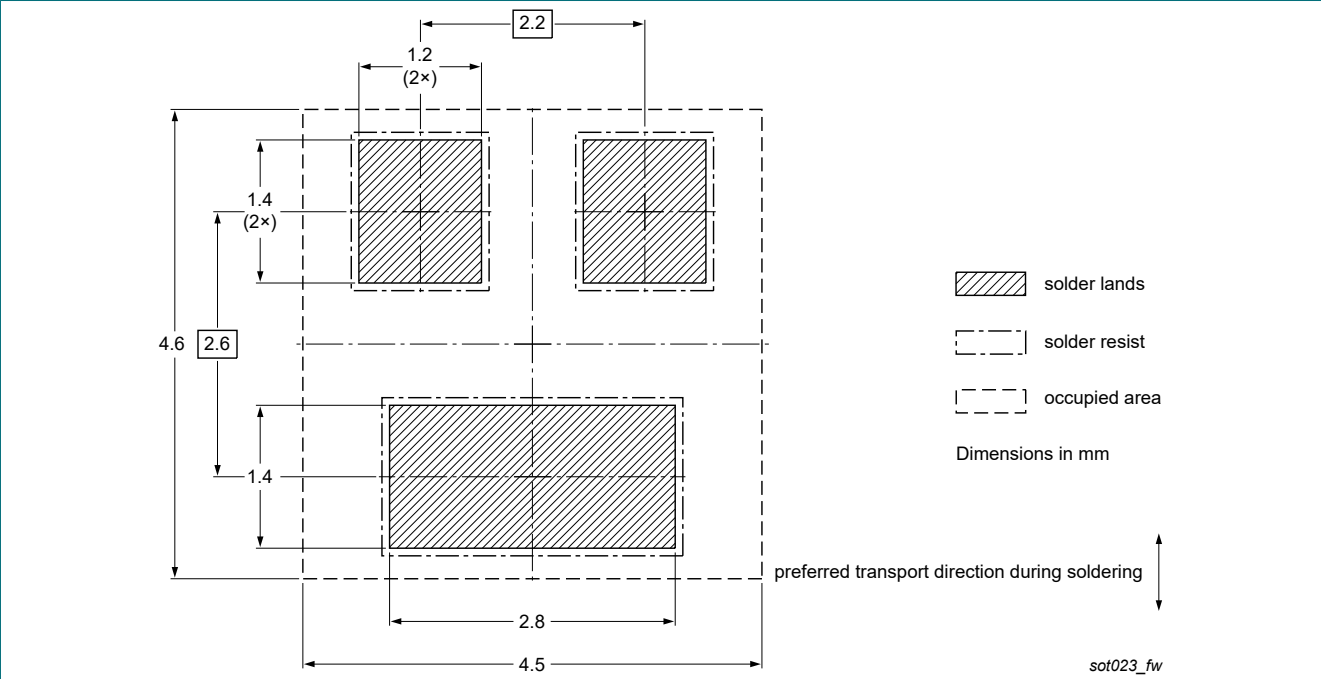


Fig. 20. Wave soldering footprint for SOT23

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NX138AK v.3	20210610	Product data sheet	-	NX138AK v.2
Modifications:	• Document headline: Typo correction			
NX138AK v.2	20160610	Product data sheet	-	NX138AK v.1
NX138AK v.1	20160607	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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