



P-CHANNEL ENHANCEMENT MODE MOSFET

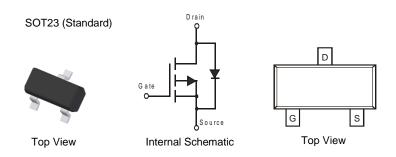
Features

- Low On-Resistance
 - $60m\Omega$ @ $V_{GS} = -4.5V$
 - $90m\Omega$ @ V_{GS} = -2.5V
 - $113m\Omega$ @ V_{GS} = -1.8V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMP2305UQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



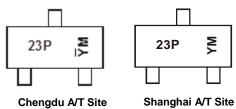
Ordering Information (Note 4)

Part Number	Qualification	Case	Packaging
DMP2305U-7	Standard	SOT23 (Standard)	3000/Tape & Reel
DMP2305UQ-7	Automotive	SOT23 (Standard)	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



Shanghai A/T Site

23P = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test Site) YM = Date Code Marking for CAT (Chengdu Assembly/ Test Site)

Y or \overline{Y} = Year (ex: I = 2021)

M = Month (ex: 9 = September)

Date Code Key

Year	2009		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	W			J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

July 2021



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characte	eristic		Symbol	Value	Unit
Drain-Source Voltage			VDSS	-20	V
Gate-Source Voltage		V _{GSS}	±8	V	
Continuous Drain Current (Note 5)	Steady State	T _A = +25°C T _A = +70°C	lo	-4.2 -3.4	А
Pulsed Drain Current (Note 6)			I _{DM}	-10	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.4	W
Thermal Resistance, Junction to Ambient @T _A = +25°C	R _{θJA}	90	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

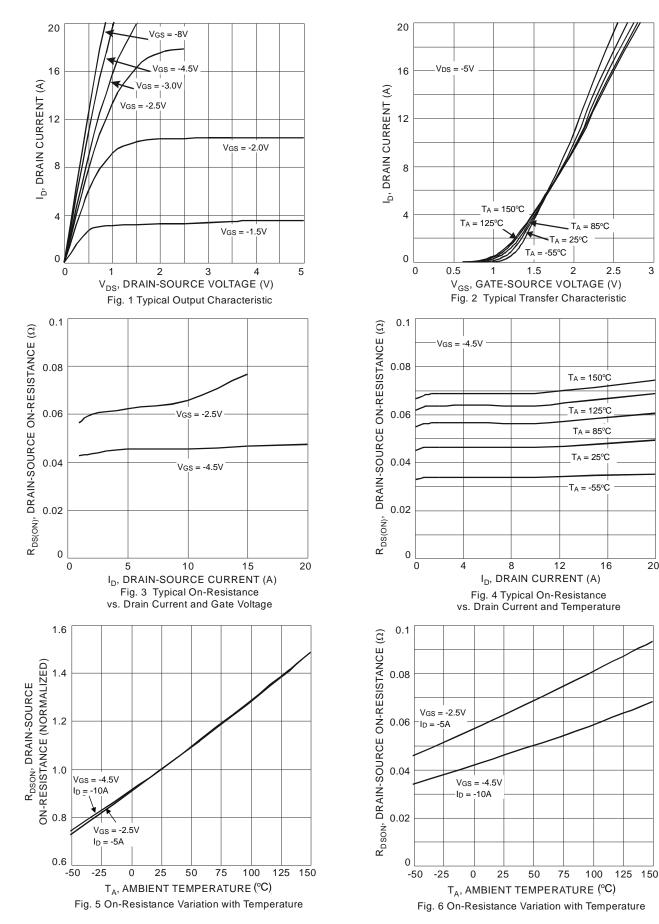
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Cumpleal	M:	T	Max	11	Toot Condition
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)			ı	1		1
Drain-Source Breakdown Voltage	BVDSS	-20	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current $T_J = +25$ °C	I _{DSS}		_	-1.0	μΑ	$V_{DS} = -20V$, $V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.5	_	-0.9	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
			45	60		$V_{GS} = -4.5V, I_{D} = -4.2A$
Static Drain-Source On-Resistance	RDS (ON)	_	60	90	mΩ	$V_{GS} = -2.5V, I_{D} = -3.4A$
			87	113		$V_{GS} = -1.8V, I_{D} = -2.0A$
Forward Transfer Admittance	Y _{FS}	_	9	_	S	$V_{DS} = -5V, I_{D} = -4A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	727	_	pF	
Output Capacitance	Coss	_	69	_	pF	V _{DS} = -20V, V _{GS} = 0V -f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	64	_	pF	1 = 1.0WH12
Gate Resistance	R_g	_	23	_	Ω	$V_{GS} = 0V$, $V_{DS} = 0V$, $f = 1.0MHz$
SWITCHING CHARACTERISTICS						
Total Gate Charge	Qg	_	7.6	_	nC	
Gate-Source Charge	Qgs	_	1.4	_	nC	Vgs = -4.5V, Vps = -4V, Ip = -3.5A
Gate-Drain Charge	Q_{gd}	_	1.2	_	nC	
Turn-On Delay Time	t _{D(ON)}	_	14.0	_	ns	
Turn-On Rise Time	t _R	_	13.0	_	ns	$V_{DS} = -4V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	_	53.8	_	ns	$R_L = 4\Omega$, $R_G = 6\Omega$, $I_D = -1A$
Turn-Off Fall Time	tr	_	23.2	_	ns	

Notes:

- 5. Device mounted on FR-4 PCB with 2oz. copper and test pulse width t \leq 10s.
- Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.





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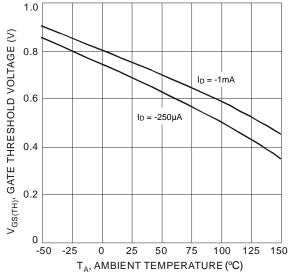
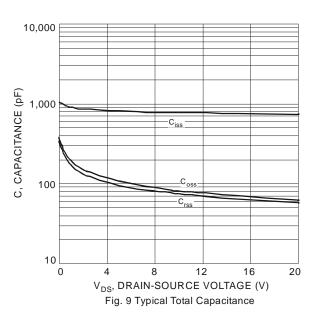
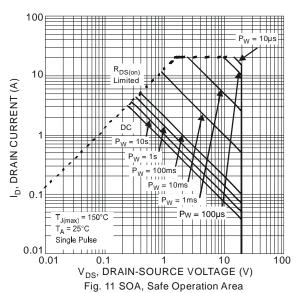
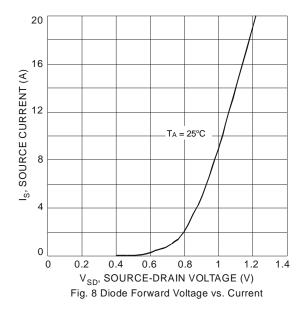


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







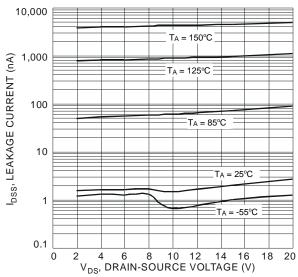


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage



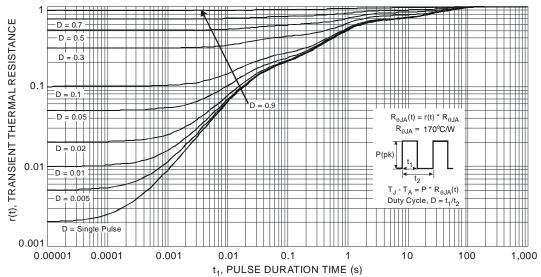


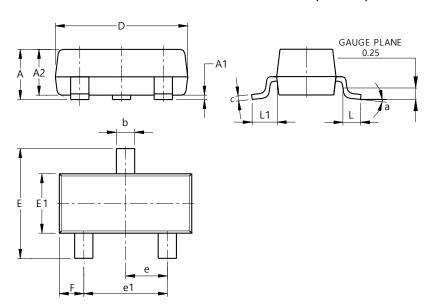
Fig. 12 Transient Thermal Response



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)

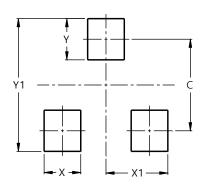


SOT23 (Standard)						
Dim	Min	Max	Тур			
Α	0.90	1.15	1.025			
A1	0.00	0.10	0.05			
A2	0.85	1.10	0.975			
b	0.30	0.51	0.40			
С	0.080	0.202	0.11			
D	2.80	3.00	2.90			
Е	2.25	2.55	2.40			
E1	1.20	1.40	1.30			
е	0.89	1.03	0.915			
e1	1.78	2.05	1.83			
F	0.40	0.60	0.535			
L1	0.45	0.61	0.55			
L	0.25	0.55	0.40			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)



Dimensions	Value (in mm)
C	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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