

Vishay Siliconix

P-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^b		
- 40	0.082 at V _{GS} = - 10 V	- 3.0		
	0.130 at V _{GS} = - 4.5 V	- 2.4		

FEATURES

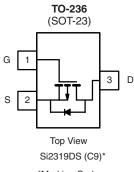
- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Power MOSFET

APPLICATIONS

Load Switch



COMPLIANT HALOGEN FREE Available



*Marking Code

Ordering Information: Si2319DS-T1-E3 (Lead (Pb)-free) Si2319DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	ſ _A = 25 °C, unle	ss otherwise r	oted			
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 40		V	
Gate-Source Voltage		V _{GS}	± 20			
Continuous Durin Coursent (T. 150 °C)	T _A = 25 °C	I	- 3.0	- 2.3		
Continuous Drain Current (T _J = 150 °C) ^b	T _A = 70 °C	- ^I D	- 2.4	- 1.85		
Pulsed Drain Current ^a		I _{DM}	- 12		A	
Continuous Source Current (Diode Conduction) ^b		۱ _S	- 1.0	- 0.62		
Deven Dissission	T _A = 25 °C	P	1.25	0.75	W	
Power Dissipation ^b	T _A = 70 °C	P _D	0.8	0.48	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^b	R _{thJA}	75	100		
Maximum Junction-to-Ambient ^c	' 'thJA	120	166	°C/W	
Maximum Junction-to-Foot (Drain)	R _{thJF}	40	50		

Notes:

a. Pulse width limited by maximum junction temperature.

b. Surface mounted on FR4 board, $t \leq 5 \mbox{ s.}$

c. Surface Mounted on FR4 board.

For Spice model information via the worldwide web: www.vishay.com/www/product/spice.htm.

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 40			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 1		- 3.0	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
	1	$V_{DS} = -40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	IDSS	V_{DS} = - 40 V, V_{GS} = 0 V, T_{J} = 55 °C	- 10		- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \leq$ - 5 V, V_{GS} = - 10 V	- 6			А	
Drain-Source On-State Resistance ^a	Б	V _{GS} = - 10 V, I _D = - 3.0 A		0.065	0.082	Ω	
	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -2.4 \text{ A}$		0.100	0.130		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -5 V, I_{D} = -3.0 A$		7.0		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.25 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg	N 00 X X 10 X		11.3	17		
Gate-Source Charge	Q _{gs}	V _{DS} = - 20 V, V _{GS} = - 10 V I _D ≅ - 3 A		1.7		nC	
Gate-Drain Charge	Q _{gd}	1D = - 2 X		3.3			
Input Capacitance	C _{iss}			470			
Output Capacitance	C _{oss}	V_{DS} = - 20 V, V_{GS} = 0 V, f = 1 MHz		85		pF	
Reverse Transfer Capacitance	C _{rss}			65		1	
Switching ^c							
Turn-On Time	t _{d(on)}			7	15	- ns	
ium-on time	t _r	V_{DD} = - 20 V, R _L = 20 Ω I _D ≅ - 1.0 A, V _{GEN} = - 4.5 V		15	25		
Turn-Off Time	t _{d(off)}	$R_{\rm g} = 6 \ \Omega$		25	40		
	t _f			25	40		

Notes:

a. Pulse test: PW \leq 300 μs duty cycle \leq 2 %.

b. For design aid only, not subject to production testing.

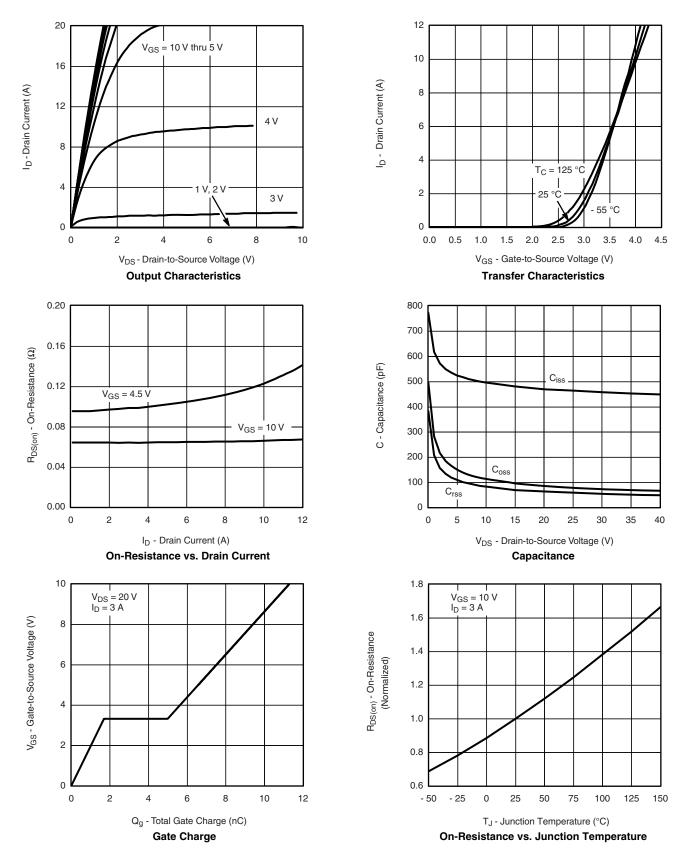
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Si2319DS Vishay Siliconix

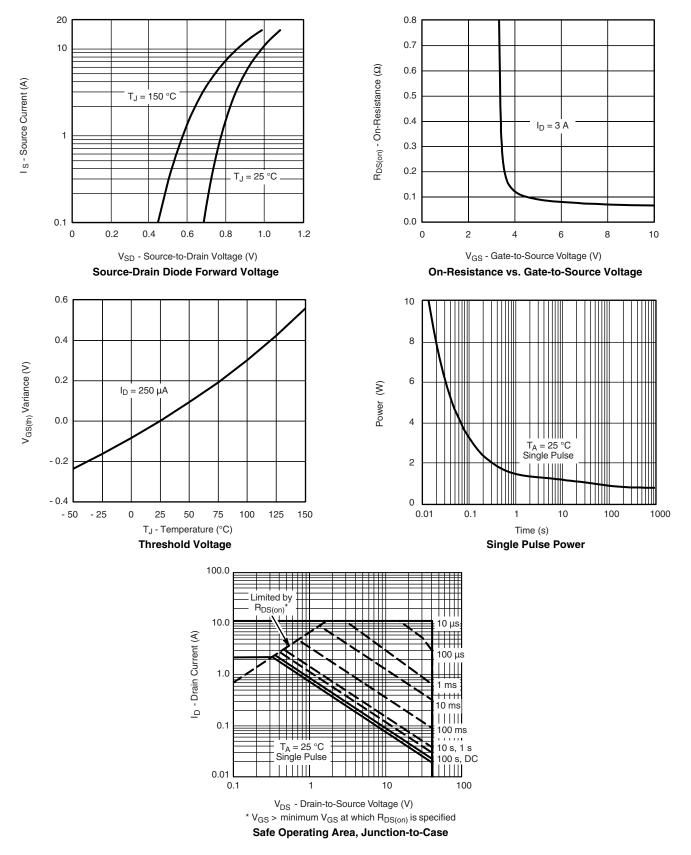
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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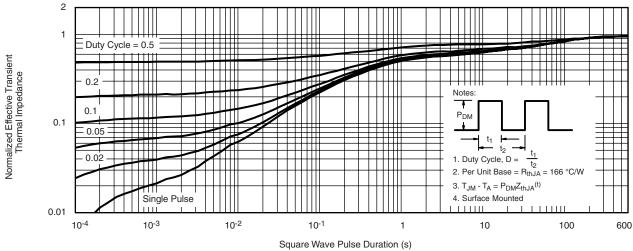




Si2319DS

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg272315</u>.



Package Information

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SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES		
	Min	Max	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	



Application Note 826

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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