Si2300DS

RoHS

COMPLIANT

HALOGEN

FREE

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Marking code: P2

PRODUCT SUMMARY					
V _{DS} (V)	30				
$R_{DS(on)}$ max. (Ω) at V_{GS} = 4.5 V	0.068				
$R_{DS(on)}$ max. (Ω) at V_{GS} = 2.5 V	0.085				
Q _g typ. (nC)	3				
I _D (A) ^a	3.6				
Configuration	Single				

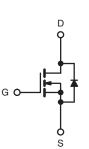
FEATURES

N-Channel 30 V (D-S) MOSFET

- TrenchFET® power MOSFET
- 100 % R_g tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- DC/DC converter for portable devices
- · Load switch



N-Channel MOSFET

ORDERING INFORMATION	

Package	SOT-23
Lead (Pb)-free and halogen-free	Si2300DS-T1-GE3

PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	30		
Gate-source voltage		V _{GS}	± 12	V	
	T _C = 25 °C		3.6 ^a		
Continuous ducia compact (T. 150 °C)	T _C = 70 °C		3		
Continuous drain current (T _J = 150 °C)	T _A = 25 °C	I _D	3.1 ^{b, c}		
	T _A = 70 °C	1	2.5 ^{b, c}	А	
Pulsed drain current		I _{DM}	15		
Continuous source-drain diode current	T _C = 25 °C		1.4		
	T _A = 25 °C	I _S	0.9 ^{b, c}		
	T _C = 25 °C		1.7		
Maximum power dissipation	T _C = 70 °C		1.1		
	T _A = 25 °C	P _D	1.1 ^{b, c}	W	
	T _A = 70 °C	1	0.7 ^{b, c}		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	
Soldering recommendations (peak temperature) d, e			260		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient b, d	t ≤ 5 s	R _{thJA}	90	115	°C MI	
Maximum junction-to-foot (drain)	Steady state	R _{thJF}	60	75	°C/W	

Notes

a. Package limited

b. Surface mounted on 1" x 1" FR4 board

c. t = 5 s

d. Maximum under steady state conditions is 130 °C/W

S10-0111-Rev. A, 18-Jan-10

1

Document Number: 65701

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static			•		•	•	
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	30	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	$\Delta V_{DS}/T_{J}$		21	-		
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-3.2	-	mV/°C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.6	-	1.5	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$	-	-	± 100	nA	
		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1		
Zero gate voltage drain current	IDSS	V_{DS} = 30 V, V_{GS} = 0 V, T_{J} = 55 °C	-	-	10	μA	
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	10	-	-	Α	
Drain course en state registeres à	D	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 2.9 \text{ A}$	-	0.055	0.068	0	
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 2.6 \text{ A}$	-	0.070	0.085	Ω	
Forward transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 2.9 \text{ A}$	-	13	-	S	
Dynamic ^b							
Input capacitance	C _{iss}		-	320	-	pF	
Output capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	45	-		
Reverse transfer capacitance	C _{rss}		-	19	-	1	
Total gate charge	Qg	V_{DS} = 15 V, V_{GS} = 10 V, I_{D} = 3.1 A	-	6.5	10		
			-	3	4.5		
Gate-source charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 3.1 A	-	0.8	-	nC	
Gate-drain charge	Q _{gd}		-	0.5	-		
Gate resistance	Rg	f = 1 MHz	0.6	3.2	6.4	Ω	
Turn-on delay time	t _{d(on)}		-	10	15		
Rise time	t _r	V_{DD} = 15 V, R_L = 6 Ω	-	15	25		
Turn-off delay time	t _{d(off)}	$I_D \cong 2.5$ A, V_{GEN} = 4.5 V, R_g = 1 Ω	-	20	30		
Fall time	t _f		-	11	20		
Turn-on delay time	t _{d(on)}		-	5	10	ns	
Rise time	t _r	V_{DD} = 15 V, R_L = 6 Ω	-	12	20]	
Turn-off delay time	t _{d(off)}	$I_D \cong$ 2.5 A, V_{GEN} = 10 V, R_g = 1 Ω	-	15	25		
Fall time	t _f		-	10	15		
Drain-Source Body Diode Characteristi	cs						
Continuous source-drain diode current	I _S	$T_{C} = 25 \ ^{\circ}C$	-	-	1.4	^	
Pulse diode forward current	I _{SM}		-	-	15	A	
Body diode voltage	V _{SD}	$I_{S} = 2.5 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.8	1.2	V	
Body diode reverse recovery time	t _{rr}		-	11	20	ns	
Body diode reverse recovery charge	Q _{rr}	I _F = 2.5 A, di/dt = 100 A/μs,	-	5	10	nC	
Reverse recovery fall time	t _a	$T_{\rm J} = 25 \ ^{\circ}{\rm C}$	-	7	-		
Reverse recovery rise time	t _b		-	4	-	ns	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

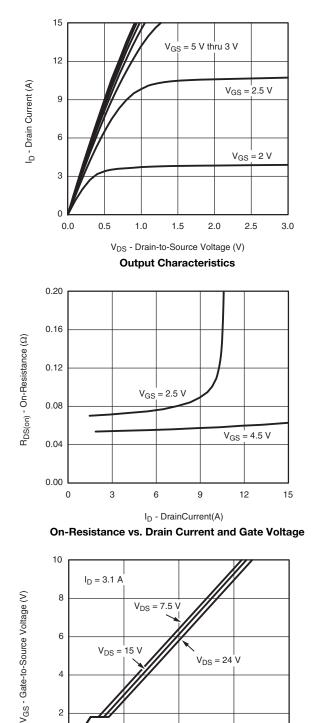
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

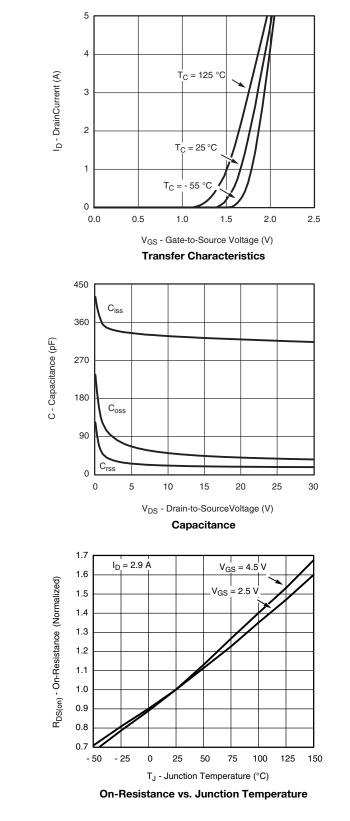
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





S10-0111-Rev. A, 18-Jan-10

2

6

8

4

Q_g - Total Gate Charge (nC) **Gate Charge**

2

0

0

3

Document Number: 65701

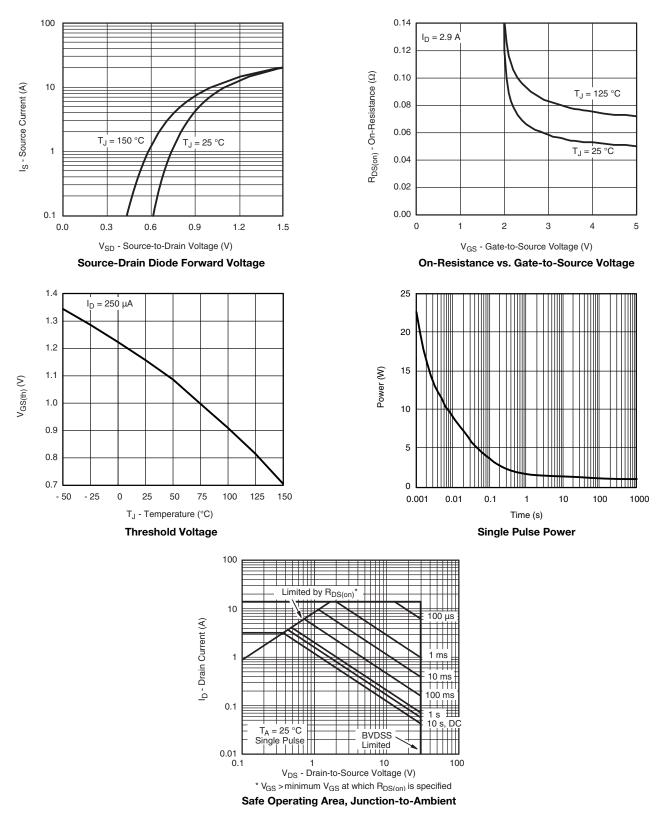
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



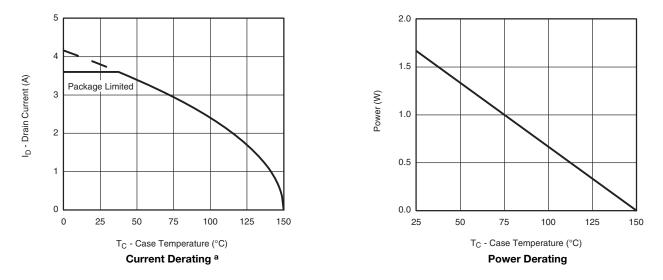
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



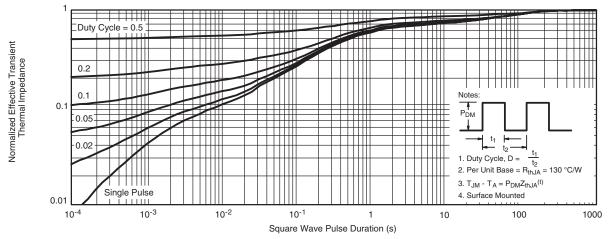
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

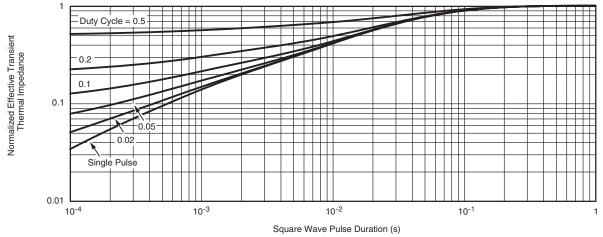


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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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Package Information

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SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES		
	Min	Max	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90) BSC	0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	



Application Note 826

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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