Complementary ThermalTrak™ Transistors

The ThermalTrak family of devices has been designed to eliminate thermal equilibrium lag time and bias trimming in audio amplifier applications. They can also be used in other applications as transistor die protection devices.

Features

- Thermally Matched Bias Diode
- Instant Thermal Bias Tracking
- Absolute Thermal Integrity
- High Safe Operating Area
- Pb-Free Packages are Available*

Benefits

- Eliminates Thermal Equilibrium Lag Time and Bias Trimming
- Superior Sound Quality Through Improved Dynamic Temperature Response
- Significantly Improved Bias Stability
- Simplified Assembly
 - Reduced Labor Costs
 - Reduced Component Count
- High Reliability

Applications

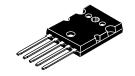
- High-End Consumer Audio Products
 - Home Amplifiers
 - Home Receivers
- Professional Audio Amplifiers
 - Theater and Stadium Sound Systems
 - Public Address Systems (PAs)



ON Semiconductor®

http://onsemi.com

BIPOLAR POWER TRANSISTORS 15 AMP, 260 VOLT, 180 WATT

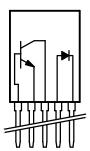


TO-264, 5 LEAD CASE 340AA STYLE 1

MARKING DIAGRAM

SCHEMATIC





NJL0xxxD = Device Code

xxx = 281 or 302

G = Pb-Free Package A = Assembly Location

Y = Year

WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping
NJL0281D	TO-264	25 Units / Rail
NJL0281DG	TO-264 (Pb-Free)	25 Units / Rail
NJL0302D	TO-264	25 Units / Rail
NJL0302DG	TO-264 (Pb-Free)	25 Units / Rail

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CEO}	260	Vdc
Collector–Base Voltage	V_{CBO}	260	Vdc
Emitter–Base Voltage	V_{EBO}	5	Vdc
Collector–Emitter Voltage – 1.5 V	V _{CEX}	260	Vdc
Collector Current – Continuous – Peak (Note 1)	I _C	15 25	Adc
Base Current – Continuous	Ι _Β	1.5	Adc
Total Power Dissipation @ T _C = 25°C Derate Above 25°C	P _D	180 1.43	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	- 65 to +150	°C
DC Blocking Voltage	V _R	200	V
Average Rectified Forward Current	I _{F(AV)}	1.0	Α

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	0.694	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ATTRIBUTES

Cha	racteristic	Value
ESD Protection	Human Body Model Machine Model	>8000 V > 400 V
Flammability Rating		UL 94 V-0 @ 0.125 in

^{1.} Pulse Test: Pulse Width = 5 ms, Duty Cycle < 10%.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS		•	•	- 1
Collector–Emitter Sustaining Voltage (I _C = 100 mAdc, I _B = 0)	V _{CEO(sus)}	260	-	Vdc
Collector Cutoff Current (V _{CB} = 260 Vdc, I _E = 0)	Ісво		10	μAdc
Emitter Cutoff Current (V _{EB} = 5 Vdc, I _C = 0)	I _{EBO}	_	5	μAdc
ON CHARACTERISTICS				
DC Current Gain	h _{FE}	75 75 75	150 150 150	
Collector–Emitter Saturation Voltage (I _C = 5 Adc, I _B = 0.5 Adc)	V _{CE(sat)}	_	1.0	Vdc
Base–Emitter On Voltage (I _C = 5 Adc, V _{CE} = 5 Vdc)	V _{CE(on)}	_	1.2	Vdc
DYNAMIC CHARACTERISTICS				
Current-Gain - Bandwidth Product (I _C = 1 Adc, V _{CE} = 5 Vdc, f _{test} = 1 MHz)	f _T	30	-	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f _{test} = 1 MHz)	C _{ob}		400	pF
Maximum Instantaneous Forward Voltage (Note 2) ($i_F = 1.0 \text{ A}, T_J = 25^{\circ}\text{C}$) ($i_F = 1.0 \text{ A}, T_J = 150^{\circ}\text{C}$)	VF	1.1 0.93		V
Maximum Instantaneous Reverse Current (Note 2) (Rated dc Voltage, $T_J = 25^{\circ}C$) (Rated dc Voltage, $T_J = 150^{\circ}C$)	i _R	10 100		μΑ
Maximum Reverse Recovery Time (i _F = 1.0 A, di/dt = 50 A/μs)	t _{rr}	1	00	ns

^{2.} Diode Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

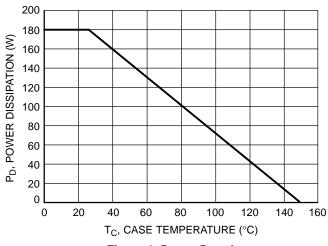


Figure 1. Power Derating

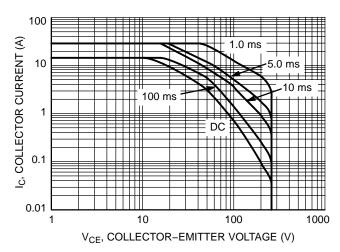


Figure 2. Safe Operating Area

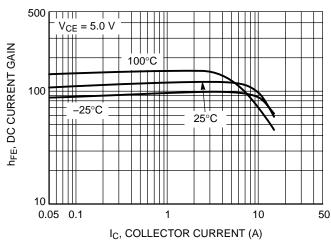


Figure 3. NJL0281A DC Current Gain

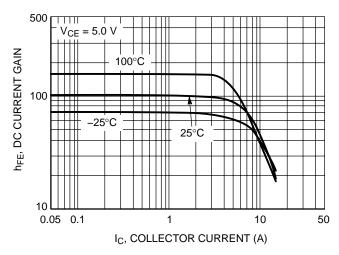


Figure 4. NJL0302A DC Current Gain

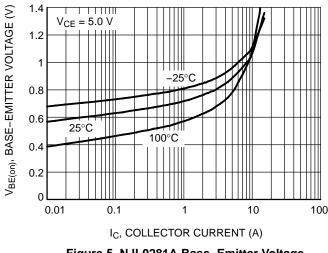


Figure 5. NJL0281A Base-Emitter Voltage

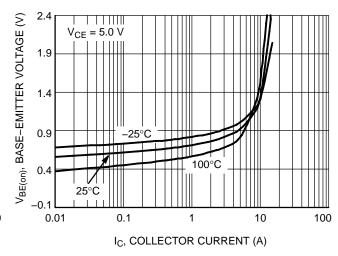


Figure 6. NJL0302A Base-Emitter Voltage

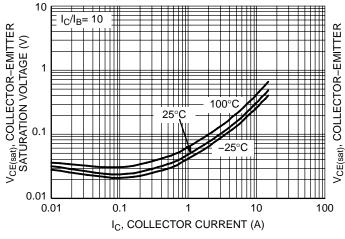


Figure 7. NJL0281A Saturation Voltage

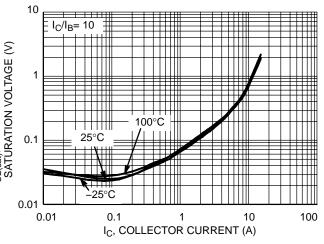


Figure 8. NJL0302A Saturation Voltage

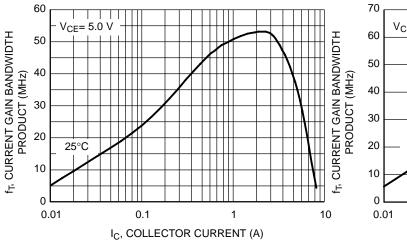


Figure 9. NJL0281A Current Gain Bandwidth Product

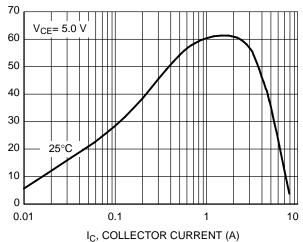


Figure 10. NJL0302A Current Gain Bandwidth Product

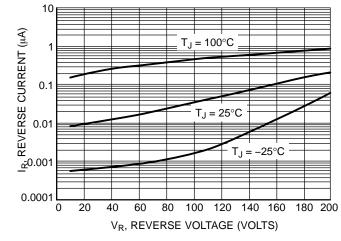


Figure 11. Typical Reverse Current

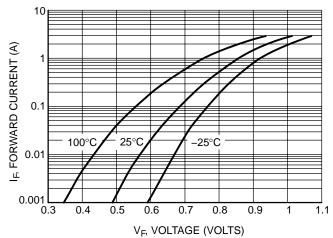
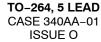
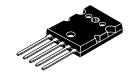


Figure 12. Typical Forward Voltage

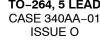




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SCALE 1:2

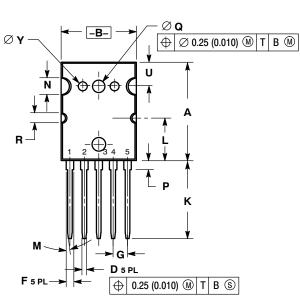


-T-

- E

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.

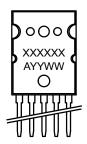
	MILLIMETERS				INCHES	;
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	25.857	25.984	26.111	1.018	1.023	1.028
В	19.761	19.888	20.015	0.778	0.783	0.788
С	4.699	4.890	5.182	0.185	0.199	0.204
D	1.	219 BS0	0	0.	0480 BS	SC
Е	1.890	2.042	2.184	0.0748	0.0804	0.0860
F	1.	981 BS0)	0.	0780 BS	SC
G	3	.81 BSC	;	0	.150 BS	С
Н	2.667	2.718	2.769	0.1050	0.1070	0.1090
J	O	.584 BS	C	0.0230 BSC		SC
K	20.422	20.549	20.676	0.804 0.809 0.8		0.814
L		11.28 REF		0.444 REF		F
M	0 °		7 °	0 °		7 °
N		4.57 REF		0.180 REF		EF
Р	2.259	2.386	2.513	0.0889	0.0939	0.0989
Q	3.480 BSC 0.		.1370 BSC			
R		2.54 REF		0.100 REF		ΞF
S	0 °		8 °	0 °		8 °
U	6.17 REF		0.243 REF		EF	
W	0 °		6°	0 °		6°
Υ	2.388 BSC			C	.0940 B	SC





- STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR 4. ANODE
 - 5. CATHODE

GENERIC MARKING DIAGRAM*



XXXXXX = Specific Device Code = Assembly Location Α

ΥY = Year WW = Work Week = Pb-Free Package G or ■

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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