

# **DISTINCTIVE CHARACTERISTICS**

### Standard with Enhanced Illumination:

Programmable to display graphics, alphanumeric characters and animated sequences.

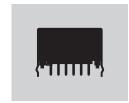
Standard SMARTDISPLAY<sup>TM</sup> can be used alone or in conjunction with electromechanical switches.

Integrated liquid crystal display provides wide viewing angle with high contrast and clarity.

Viewing area 14.4mm x 11.8mm (horizontal x vertical) at  $36 \times 24$  pixels.



Actual Size



# **PART NUMBER & DESCRIPTION**

Part Number	Terminals LCD Mode LED Color		LED Color
ISO1BBFEF	Straight PC	Yellow FSTN Positive	* Yellow/Green

<sup>\*</sup> Simultaneous illumination of LEDs achieves infinite colors.

### **LCD & LED SPECIFICATIONS**

#### **Characteristics of Display**

Display Operation Mode	FSTN positive
Display Condition	Transflective with built-in LED backlight
Viewing Angle	6 o'clock
Driving Method	1/24 duty. 1/5 bias (built-in driving circuit)
Viewing Area	14.4mm x 11.8mm (horizontal x vertical)
Pixel Format	36 x 24 pixels (horizontal x vertical)
Pixel Size	0.371mm x 0.445mm (horizontal x vertical)
Operating Temp. Range	-20°C ~ +60°C (-4°F ~ +140°F)
Storage Temp. Range	-30°C ~ +70°C (-22°F ~ +158°F)
Backlight LED	Yellow/Green

#### LCD Absolute Maximum Ratings (Temperature at 25°C)

Items	Symbols	Ratings
Supply Voltage for Logistics	V <sub>DD</sub>	-0.3V to +7.0V
Supply Voltage for LCD	$V_{LC}$	-0.3V to +12.0V
Input Voltage	V <sub>I</sub>	-0.3V to V <sub>DD</sub> +0.3V
Output Voltage	Vo	-0.3V to V <sub>DD</sub> +0.3V

### Recommended Operating Conditions (Temperature at 25°C)

Items	Symbols	Minimum	Typical	Maximum
Supply Voltage for Logics	$V_{DD}$	3.0V	_	5.5V
Supply Voltage LCD	$V_{LC}$	_	* 7.3V	
Input Voltage	VI	OV	_	V <sub>DD</sub>
Driving Frequency	f <sub>FLM</sub>	_	150Hz	_

<sup>\*</sup> LCD voltage ( $V_{LC}$ ) level depends on refreshing frequency and temperature. The optimal  $V_{LC}$  can differ slightly from the stated typical value.

#### LED Absolute Maximum Ratings (Temperature at 25°C)

Items	Symbols	Ratings
Forward Current	I <sub>F</sub>	20mA
*Power Dissipation	$P_d$	130mW

Color	Yellow/Green			
Color	Yellow	Green		
Unicolor	60mW 60mW			
LED Overall	130mW			

Rev 11.4.15 • 11.16.16 • 12.29.17





# **LCD & LED SPECIFICATIONS**

DC Characteristics of LCD Drive IC (Temperature at  $-20^{\circ}$ C to  $+60^{\circ}$ C and  $V_{DD} = 5.0V \pm 10\%$ )

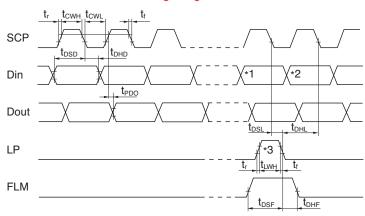
ltems	Symbols	Test Conditions	Minimum	Typical	Maximum	Unit
High Level Input Voltage	$V_{IH}$		$0.7V_{DD}$		$V_{DD}$	V
Low Level Input Voltage	$V_{IL}$		0		$0.3V_{DD}$	V
High Level Input Leakage Current	I <sub>LIH</sub>	$V_{l} = V_{DD}$			10	μA
Low Level Input Leakage Current	I <sub>LIL</sub>	$V_1 = 0V$			10	μA
High Level Output Voltage	$V_{OH}$	$I_{OH} = -500 \mu A$	V <sub>DD</sub> -0.5			٧
Low Level Output Voltage	V <sub>OL</sub>	$I_{OH} = 500 \mu A$			0.5	V
High Level Output Leakage Current	$I_{LOH}$	$V_O = V_{DD}$			10	μA
Low Level Output Leakage Current	I <sub>LOL</sub>	$V_{\odot} = 0V$			10	μA
Supply Current	I <sub>DD</sub>	$f_{SCP} = 1.0MHz$			500	μA
LCD Drive Current	I <sub>LC</sub>	$f_{LP} = 2.4 kHz$ $V_{LC} = 7.3 V$		500	2,000	μA

### Timing Characteristics of LCD Drive IC

(Temperature at  $-20^{\circ}$ C to  $+60^{\circ}$ C and  $V_{DD} = 5.0V \pm 10\%$ )

(10pordiore di 20 0 10 100	<b>G</b> G11.G1 1 DD	0.0 , = . 0 .	~,
Items	Symbols	Minimum	Maximum
Clock Operation Frequency	$f_{SCP}$		8.0MHz
Latch Pulse Frequency	f <sub>LP</sub>		50kHz
Clock High Level Pulse Width	t <sub>CWH</sub>	50ns	
Clock Low Level Pulse Width	t <sub>CWL</sub>	50ns	
Data Setup Time	t <sub>DSD</sub>	45ns	
Data Hold Time	t <sub>DHD</sub>	50ns	
Data Output Delay Time	t <sub>PDO</sub>		25ns
Latch Setup Time	t <sub>DSL</sub>	50ns	
Latch Hold Time	† <sub>DHL</sub>	50ns	
Latch High Level Width	t <sub>LWH</sub>	50ns	
FLM Setup Time	t <sub>DSF</sub>	50ns	
FLM Hold Time	t <sub>DHF</sub>	50ns	
SCP, LP Rise/Fall Time	t <sub>r</sub> /t <sub>f</sub>		15ns

#### **Timing Diagram**



- \*1 Last data on first line
- \*2 Beginning data on second line
- \*3 Location of LP signal on first line

# **Display Electrical Characteristics**

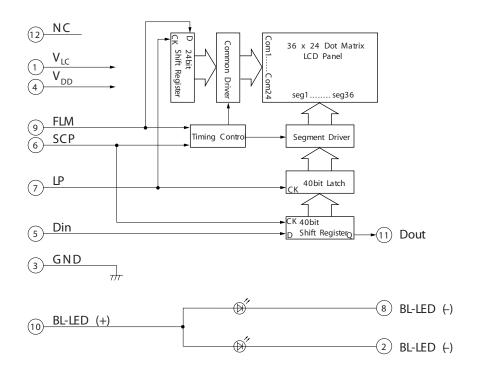
	Items		Symbols	Test Condition	Minimum	Typical	Maximum
	Supply Voltage	Logic Circuit	V <sub>DD</sub>		3.0	_	5.5
		LCD Circuit	V <sub>LC</sub>		_	* 7.3	_
	Innut Valtana	Н	V <sub>IH</sub>		0.7V <sub>DD</sub>	_	$V_{DD}$
ICD	Input Voltage	L	V <sub>IL</sub>		0	_	0.3 V <sub>DD</sub>
LCD	0.4	Н	V <sub>OH</sub>	D <sub>OUT,</sub> I <sub>OH</sub> = 500 μA	V <sub>DD</sub> -0.5	_	_
	Output Voltage	L	V <sub>OL</sub>	D <sub>OUT,</sub> I <sub>OL</sub> = 500 µA	_	_	0.5
		Logic Circuit	I <sub>DD</sub>	$f_{scp} = 1.0MHz$	_	_	500
	Power	LCD Circuit	I <sub>LC</sub>	$f_{LP} = 2.4 \text{kHz}$ $V_{LC} = 7.3 \text{V}$	_	500	2,000
	Items		Symbols	Test Condition		Yellow/Green	
	Forward Current I <sub>F</sub>					Red	
			I <sub>F</sub>			10mA	
LED	Forward Voltage  Current Reduction Rate		V <sub>F</sub>	I <sub>r</sub> = Forward Current	Red		
				I <sub>F</sub> = Forward Current Ta = 25°C	2.0V		
			$\Delta I_F(DC)$	Ta = 25°C above	−0.33mA/°C		

<sup>\*</sup> LCD voltage ( $V_{LC}$ ) level depends on refreshing frequency and temperature. The optimal  $V_{LC}$  can differ slightly from the stated typical value.





# **BLOCK DIAGRAM & PIN CONFIGURATIONS FOR RGB LEDS**





ISO1BBFEF Yellow/Green LED Backlight Black and White LCD

Pin No.	<u>Symbol</u>	<u>Name</u>	<u>Function</u>
1	$V_{LC}$	Power	Power source for LCD drive
2	BL-LED (-)	Terminal of Backlight LED	Cathode: green
3	GND	Ground	
4	$V_{\text{DD}}$	Power	Power source for logic circuit
5	Din	Data Input	Display serial data bit. Note: to map the display data, because of the difference between the number of internal shift register data (40) and the single line of LCD pixels (36), the first four bits of data shifted will be dummy bits.
6	SCP	Serial Clock Pulse	Clock used by 40-bit internal shift register of the switch, shifting the display data bit presented at Din at falling edge.
7	LP	Latch Pulse	Line data latch pulse will latch content of internal 40-bit shift register at falling edge for one line of display. LP will also increment the display line by one.
8	BL-LED (-)	Terminal of Backlight LED	Cathode: yellow
9	FLM	First Line Marker	The marking signal for the first line data of LCD display. The first line of LCD will be selected by the falling edge of LP signal during the high level (FLM).
10	BL-LED (+)	Terminal of Backlight LED	Anode for common
11)	Dout	Data Output	Display serial output. Can be used to connect to Din of the next SMARTDISPLAY. As a result, many SMARTDISPLAYS can be controlled with one clock and data signal.
12	NC	None	No connection





# **SUPER BRIGHT LED SPECIFICATIONS**

#### Typical Electrical Characteristics (Temperature at 25°C)

Backlight Color	Symbols	Yellow/Green	Unit
Forward Current	I <sub>F</sub>	15/15	mA
Forward Voltage	V <sub>F</sub>	2.2/3.3	V

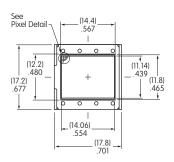
# ABSOLUTE MAXIMUM FOR LED

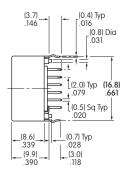
#### Electrical Characteristics (Temperature at 25°C)

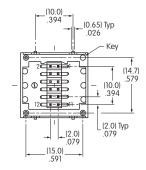
Backlight Color	Symbols	Yellow/Green	Unit
Forward Current	I <sub>F</sub>	20	mA
Reverse Voltage	$V_R$	4.0	٧
Current Reduction Rate Above 25°C	$\Delta I_{F}(DC)$	-0.33	mA/°C
*Power Dissipation	$P_{D}$	40	mW

<sup>\*</sup> For uniform light emission, Power Dissipation should not exceed the Absolute Maximum Rating, and the Forward Current should not exceed the derated Absolute Forward Current.

# TYPICAL DISPLAY DIMENSIONS



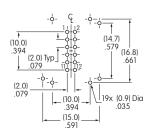




Terminal numbers are not on the device.



Pixel Detail Footprint







### PRECAUTIONS FOR HANDLING & STORAGE OF LCD 36 x 24 DEVICES

### Handling

1. The IS Series devices are electrostatic sensitive.

ATTENTION
ELECTROSTATIC
SENSITIVE DEVICES

- 2. The IS series devices are not process sealed.
- 3. If the LCD is accidentally broken, avoid contact with the liquid and wash off any liquid spills to the skin or clothing.
- 4. Clean cap surface with dry cloth. If further cleaning is needed, wipe with dampened cloth using neutral cleanser and dry with clean cloth. Do not use organic solvent.
- 5. Recommended soldering time and temperature limits:

Do not exceed 70°C at the LCD level. Wave Soldering: see Profile B in the Supplement section. Manual Soldering: see Profile B in the Supplement section.

- 6. Recommendation for backlight color uniformity: Use constant current driver. For current limiting resistor method, the power source should be at least twice the backlight LED forward voltage.
- 7. The VLC voltage should not be applied before logic voltage. If VLC voltage is present before logic voltage, it may cause the driver logic to freeze and damage the LCD, and the driver logic may become damaged.
- 8. Backlight Forward Current should not exceed the derated Absolute Maximum Forward Current based on the temperature.
- 9. Excessive images may result after the same image is emitted continuously for an extended period of time.

#### Storage

- 1. Store in original container and away from direct sunlight.
- 2. Keep away from static electricity.
- 3. Avoid extreme temperatures, high humidity, gaseous substances, and all forms of chemical contamination.

