

1. Product profile

1.1 General description

A 70 W general purpose LDMOS RF power transistor for broadcast and ISM applications in HF to 2 GHz band.

Table 1. Application performance

| Test signal | f (MHz) | P _L (W) | G _p (dB) | η _D (%) | R _{L,in} (dB) |
|-------------|------------|-----------------------|------------------------|-----------------------|---------------------------|
| pulsed CW | 1400 | 70 | 17.6 | 70 | -14 |
| CW | 915 | 70 | 17 | 75 | -17 |

1.2 Features and benefits

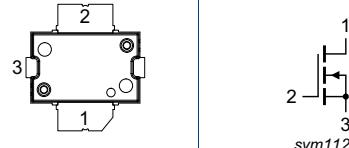
- High efficiency
- Integrated dual sided ESD protection
- Excellent ruggedness
- High power gain
- Excellent reliability
- Easy power control
- For RoHS compliance see the product details on the Ampleon website

1.3 Applications

- RF power amplifiers for CW applications
- Industrial, scientific and medical applications
- Broadcast transmitter applications

2. Pinning information

Table 2. Pinning

| Pin | Description | Simplified outline | Graphic symbol |
|-----|-------------|--------------------|---|
| 1 | drain | | |
| 2 | gate | | |
| 3 | source | [1] |  |

[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

| Package name | Orderable part number | 12NC | Packing description | Min. orderable quantity (pieces) |
|--------------|-----------------------|----------------|---------------------------------|----------------------------------|
| SOT1482-1 | BLP15M9S70Z | 9349 602 43515 | TR13; 500-fold; 24 mm; dry pack | 500 |
| | BLP15M9S70XY | 9349 602 43538 | TR7; 100-fold; 24 mm; dry pack | 100 |

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|-----------|----------------------|------------|-----|------|------|
| V_{DS} | drain-source voltage | | - | 65 | V |
| V_{GS} | gate-source voltage | | -6 | +13 | V |
| T_{stg} | storage temperature | | -65 | +150 | °C |
| T_j | junction temperature | [1] | - | 225 | °C |

[1] Continuous use at maximum temperature will affect the reliability, for details refer to the online MTF calculator.

5. Thermal characteristics

Table 5. Thermal characteristics

| Symbol | Parameter | Conditions | Typ | Unit |
|---------------|--|---|------|------|
| $R_{th(j-c)}$ | thermal resistance from junction to case | $T_{case} = 85 \text{ °C}$; $V_{DS} = 32 \text{ V}$; $P_L = 70 \text{ W}$ | 1.44 | K/W |

6. Characteristics

Table 6. DC characteristics

$T_j = 25^\circ\text{C}$; unless otherwise specified.

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|-----------------------------|----------------------------------|--|-----|------|-----|------------------|
| $V_{(\text{BR})\text{DSS}}$ | drain-source breakdown voltage | $V_{\text{GS}} = 0 \text{ V}$; $I_D = 0.66 \text{ mA}$ | 65 | 70 | - | V |
| $V_{\text{GS}(\text{th})}$ | gate-source threshold voltage | $V_{\text{DS}} = 10 \text{ V}$; $I_D = 66 \text{ mA}$ | 1.5 | 2.0 | 2.5 | V |
| I_{DSS} | drain leakage current | $V_{\text{GS}} = 0 \text{ V}$; $V_{\text{DS}} = 32 \text{ V}$ | - | - | 1.4 | μA |
| I_{DSX} | drain cut-off current | $V_{\text{GS}} = V_{\text{GS}(\text{th})} + 3.75 \text{ V}$; $V_{\text{DS}} = 10 \text{ V}$ | - | 12.6 | - | A |
| I_{GSS} | gate leakage current | $V_{\text{GS}} = 11 \text{ V}$; $V_{\text{DS}} = 0 \text{ V}$ | - | - | 140 | nA |
| $R_{\text{DS}(\text{on})}$ | drain-source on-state resistance | $V_{\text{GS}} = V_{\text{GS}(\text{th})} + 3.75 \text{ V}$; $I_D = 2.31 \text{ A}$ | - | 185 | - | $\text{m}\Omega$ |

Table 7. AC characteristics

$T_j = 25^\circ\text{C}$; unless otherwise specified.

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|------------------|------------------------------|--|-----|------|-----|------|
| C_{iss} | input capacitance | $V_{\text{GS}} = 0 \text{ V}$; $V_{\text{DS}} = 50 \text{ V}$; $f = 1 \text{ MHz}$ | - | 61 | - | pF |
| C_{oss} | output capacitance | $V_{\text{GS}} = 0 \text{ V}$; $V_{\text{DS}} = 50 \text{ V}$; $f = 1 \text{ MHz}$ | - | 22 | - | pF |
| C_{rss} | reverse transfer capacitance | $V_{\text{GS}} = 0 \text{ V}$; $V_{\text{DS}} = 50 \text{ V}$; $f = 1 \text{ MHz}$ | - | 0.45 | - | pF |

Table 8. RF characteristics

RF characteristics in Ampleon production test circuit; typical RF performance at $T_{\text{case}} = 25^\circ\text{C}$; $V_{\text{DS}} = 32 \text{ V}$; $I_{Dq} = 300 \text{ mA}$; $t_p = 100 \mu\text{s}$; $\delta = 10 \%$.

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|----------------------------|-------------------|---|------|------|-----|------|
| Pulsed RF, class-AB | | | | | | |
| G_p | power gain | $f = 1400 \text{ MHz}$; $P_L = 70 \text{ W}$ | 16.5 | 17.8 | - | dB |
| η_D | drain efficiency | $f = 1400 \text{ MHz}$; $P_L = 70 \text{ W}$ | 61 | 65.5 | - | % |
| RL_{in} | input return loss | $f = 1400 \text{ MHz}$; $P_L = 70 \text{ W}$ | - | -17 | - | dB |

7. Test information

7.1 Ruggedness in class-AB operation

The BLP15M9S70 is capable of withstanding a load mismatch corresponding to a $\text{VSWR} = 10 : 1$ through all phases under the following conditions: $V_{\text{DS}} = 32 \text{ V}$; $f = 1400 \text{ MHz}$ at rated load power on RF development board using a pulsed CW RF signal which has $\sim 150 \text{ ns}$ rise and fall time.

7.2 Test circuit

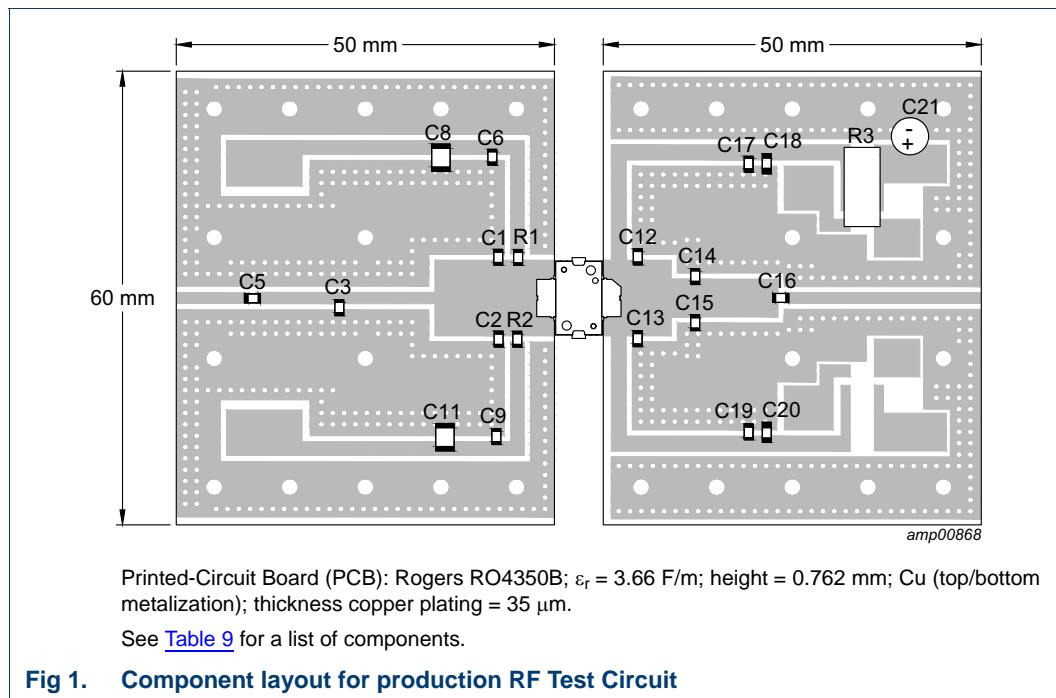
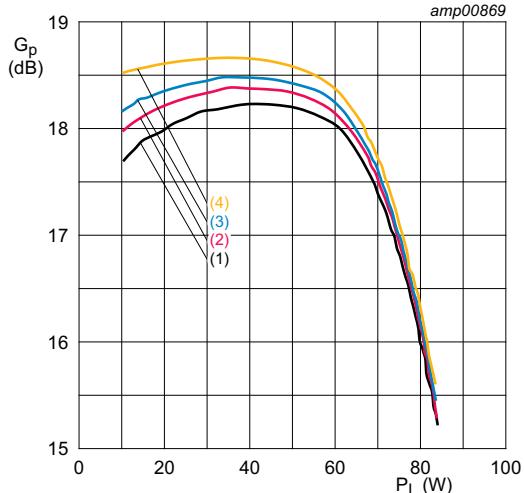


Table 9. List of components

See [Figure 1](#) for component layout.

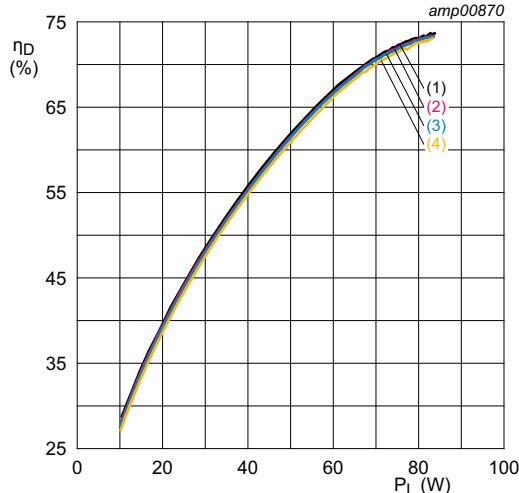
| Component | Description | Value | Remarks |
|---------------------------|-----------------------------------|-------------------|------------------------|
| C1, C2 | multilayer ceramic chip capacitor | 6.2 pF | ATC 800A |
| C3 | multilayer ceramic chip capacitor | 2 pF | ATC 800A |
| C5, C6, C9, C17, C19, C16 | multilayer ceramic chip capacitor | 100 pF | ATC 800A |
| C8, C11, C18, C20 | multilayer ceramic chip capacitor | 100 nF, 100 V | |
| C12, C13 | multilayer ceramic chip capacitor | 3 pF | ATC 800A |
| C14, C15 | multilayer ceramic chip capacitor | 2.1 pF | ATC 800A |
| C21 | electrolytic capacitor | 220 μ F, 63 V | |
| R1, R2 | chip resistor | 10 Ω | SMD 0805 |
| R3 | shunt resistor | 10 m Ω | for current monitoring |

7.3 Graphical data



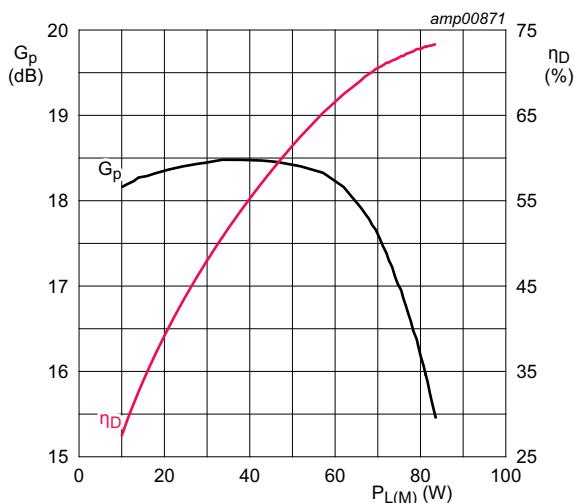
$V_{DS} = 32$ V; $f = 1400$ MHz; $t_p = 100$ μ s; $\delta = 10$ %.
 (1) $I_{Dq} = 200$ mA
 (2) $I_{Dq} = 250$ mA
 (3) $I_{Dq} = 300$ mA
 (4) $I_{Dq} = 400$ mA

Fig 2. Power gain as a function of output power; typical values



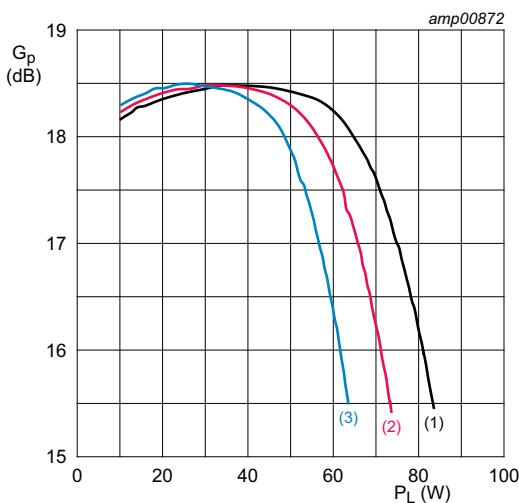
$V_{DS} = 32$ V; $f = 1400$ MHz; $t_p = 100$ μ s; $\delta = 10$ %.
 (1) $I_{Dq} = 200$ mA
 (2) $I_{Dq} = 250$ mA
 (3) $I_{Dq} = 300$ mA
 (4) $I_{Dq} = 400$ mA

Fig 3. Drain efficiency as a function of output power; typical values



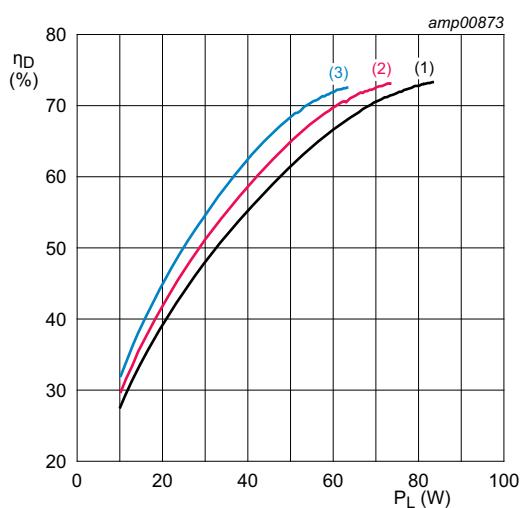
$V_{DS} = 32$ V; $I_{Dq} = 300$ mA; $f = 1400$ MHz; $t_p = 100$ μ s; $\delta = 10$ %.

Fig 4. Power gain and drain efficiency as function of peak output power; typical values



$I_{Dq} = 300$ mA; $f = 1400$ MHz; $t_p = 100$ μ s; $\delta = 10$ %.
 (1) $V_{DS} = 32$ V
 (2) $V_{DS} = 30$ V
 (3) $V_{DS} = 28$ V

Fig 5. Power gain as a function of output power; typical values



$I_{Dq} = 300 \text{ mA}$; $f = 1400 \text{ MHz}$; $t_p = 100 \mu\text{s}$; $\delta = 10 \%$.

- (1) $V_{DS} = 32 \text{ V}$
- (2) $V_{DS} = 30 \text{ V}$
- (3) $V_{DS} = 28 \text{ V}$

Fig 6. Drain efficiency as a function of output power; typical values

8. Package outline

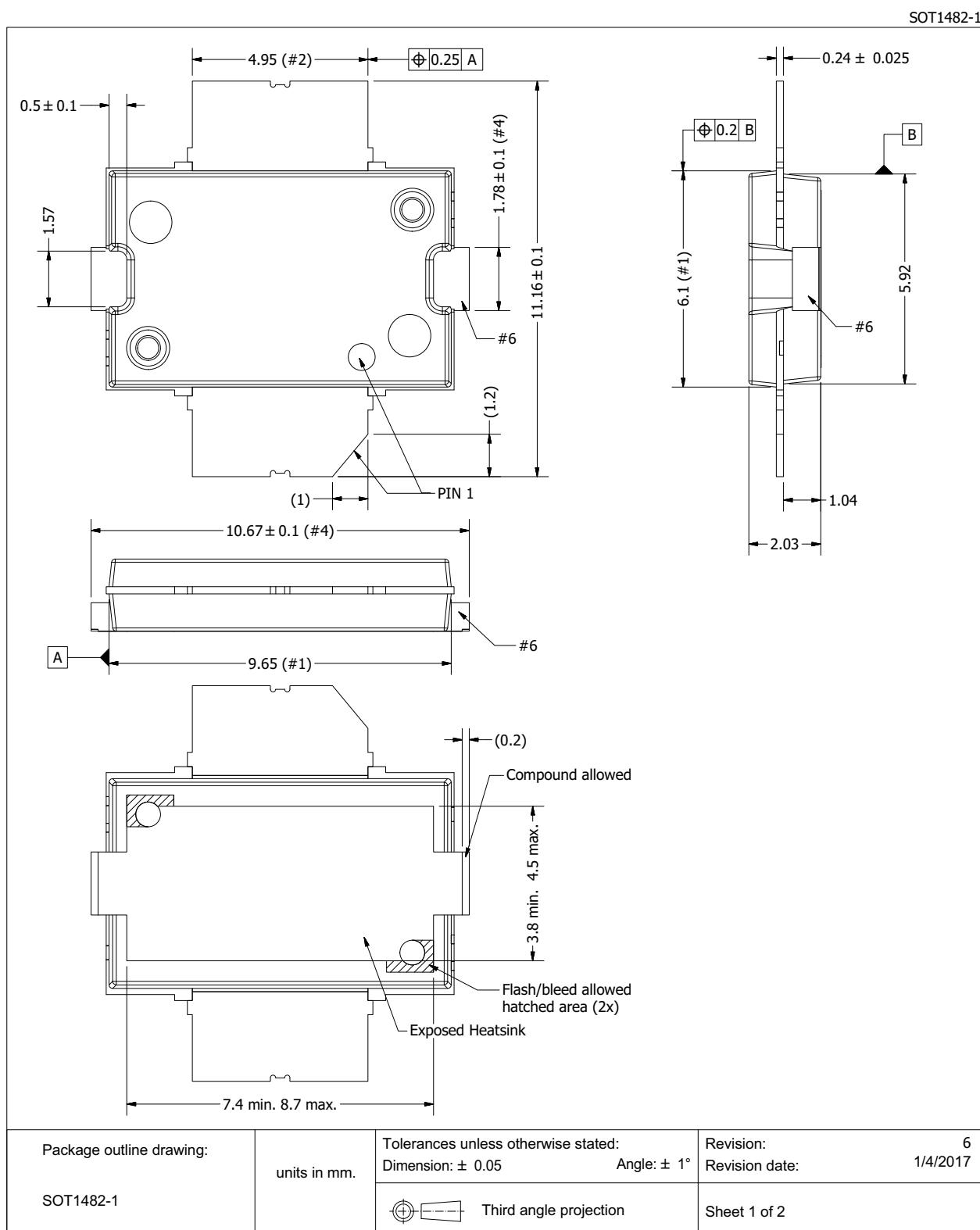
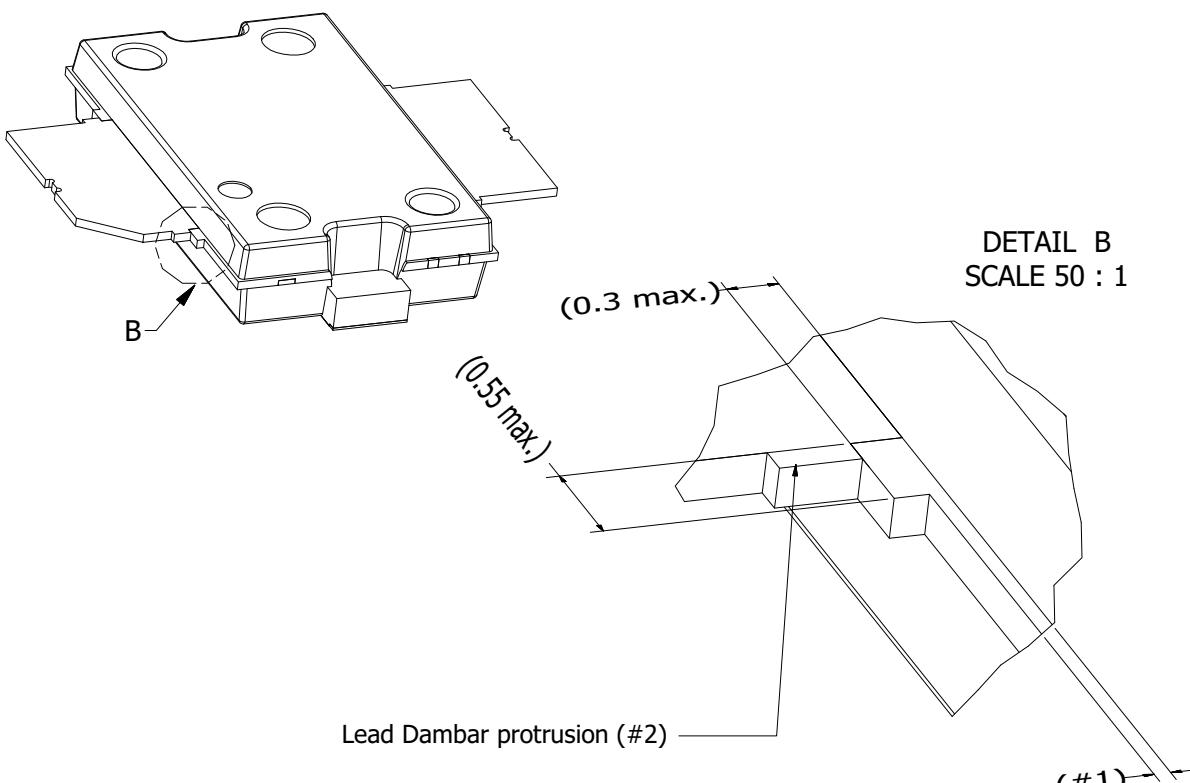


Fig 7. Package outline SOT1482-1 (sheet 1 of 2)

SOT1482-1

| Drawing Notes | |
|---------------|---|
| Items | Description |
| (1) | Dimensions are excluding mold protrusion. The mold protrusion is maximum 0.15 mm per side. See also detail B. In the dambar area max. protrusion is 0.55 mm. max. in length and 0.3 mm. max. in width (4x). See also detail B. |
| (2) | The lead dambar (metal) protrusions are not included. Add 0.14 mm max to the total lead dimension at the dambar location. |
| (3) | The leads and exposed heatsink are plated with matte Tin (Sn). |
| (4) | Dimensions (Heatsink ears) 10,67 and 1,78 do not include mouldprotrusion. Overall Max. dimensions incl. mould protrusions is 10.92 mm. (max.) and 2.03 mm. (max.) |
| (5) | Lead coplanarity over the leads is 0,1 mm. maximum. |
| (6) | Surfaces may remain unplated (not solderable surfaces) |



DETAIL B
SCALE 50 : 1

Lead Dambar protrusion (#2)

(0.3 max.)

(0.55 max.)

0.15 max. (#1)

| | | | |
|---------------------------------------|--------------|---|--|
| Package outline drawing: SOT1482-1 | units in mm. | Tolerances unless otherwise stated: Dimension: ± 0.05 Angle: $\pm 1^\circ$ | Revision: 6 Revision date: 1/4/2017 |
| | |  Third angle projection | Sheet 2 of 2 |

Fig 8. Package outline SOT1482-1 (sheet 2 of 2)

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the *ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A* or equivalent standards.

Table 10. ESD sensitivity

| ESD model | Class |
|--|-------------------------|
| Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002 | C2A [1] |
| Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001 | 2 [2] |

[1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V.

[2] HBM classification 2 is granted to any part that passes after exposure to an ESD pulse of 2000 V.

10. Abbreviations

Table 11. Abbreviations

| Acronym | Description |
|---------|--|
| CW | Continuous Wave |
| ESD | ElectroStatic Discharge |
| ISM | Industrial, Scientific and Medical |
| LDMOS | Laterally Diffused Metal-Oxide Semiconductor |
| MTF | Median Time to Failure |
| RoHS | Restriction of Hazardous Substances |
| SMD | Surface Mounted Device |
| VSWR | Voltage Standing Wave Ratio |

11. Revision history

Table 12. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------|--|--------------------|---------------|----------------|
| BLP15M9S70 v.3 | 20210716 | Product data sheet | - | BLP15M9S70 v.2 |
| Modifications: | • Table 3 on page 2 : added orderable part number BLP15M9S70XY | | | |
| BLP15M9S70 v.2 | 20210223 | Product data sheet | - | BLP15M9S70 v.1 |
| BLP15M9S70 v.1 | 20200807 | Product data sheet | - | - |

12. Legal information

12.1 Data sheet status

| Document status ^{[1][2]} | Product status ^[3] | Definition |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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