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1 Introduction

This user's guide contains background information for the TPS54426 as well as support documentation for the TPS54426EVM-608 evaluation module. Also included are the performance specifications, schematic, and the bill of materials for the TPS54426EVM-608.

1.1 Background

The TPS54426 is a single, adaptive on-time D-CAP2™ mode synchronous buck converter that requires a very low external component count. The D-CAP2™ control circuit is optimized for low-ESR output capacitors such as POSCAP, SP-CAP, or ceramic types and features fast transient response with no external compensation. The switching frequency is internally set at a nominal 700 kHz. The high-side and low-side switching MOSFETs are incorporated inside the TPS54426 package along with the gate drive circuitry. The low drain-to-source on resistance of the MOSFETs allows the TPS54426 to achieve high efficiencies and helps keep the junction temperature low at high output currents. The TPS54426 also has an auto-skip Eco-mode™ to enable higher efficiency at light loads. The TPS54426 DC/DC synchronous converter is designed to provide up to a 4-A output from an input voltage source of 4.5 V to 18 V. The output voltage range is from 0.76-V to 5.5-V voltage. The output current range for the evaluation module are given in [Table 1-1](#).

Table 1-1. Input Voltage and Output Current Summary

EVM	INPUT VOLTAGE RANGE	OUTPUT VOLTAGE AND CURRENT RANGE
TPS54426EVM-608	$V_{IN} = 4.5 \text{ V to } 18 \text{ V}$	$V_{OUT} = 1.05 \text{ V, } 0 \text{ A to } 4 \text{ A}$

1.2 Performance Specification Summary

A summary of the TPS54426EVM-608 performance specifications is provided in [Table 1-2](#). Specifications are given for an input voltage of $V_{IN} = 12 \text{ V}$ and an output voltage of 1.05 V, unless otherwise noted. The ambient temperature is 25°C for all measurement, unless otherwise noted.

Table 1-2. TPS54426 EVM and Performance Specifications Summary

SPECIFICATIONS		TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input voltage range (V_{IN})			4.5	12	18	V
CH1	Output voltage			1.05		V
	Operating frequency	$V_{IN} = 12 \text{ V, } I_O = 1 \text{ A}$		675		kHz
	Output current range		0		4	A
	Over current limit	$V_{IN} = 12 \text{ V, } L_O = 1.5 \mu\text{H}$		5.4		A
	Output ripple voltage	$V_{IN} = 12 \text{ V, } I_O = 4 \text{ A}$		7		mV_{PP}

1.3 Modifications

These evaluation modules are designed to provide access to the feature of the TPS54426. Some modifications can be made to this module.

1.3.1 Output Voltage Set Point

To change the output voltage of the EVMs, it is necessary to change the value of resistor R1. Changing the value of R1 can change the output voltage above 0.765 V. The value of R1 for a specific output voltage can be calculated using [Equation 1](#) and [Equation 2](#).

For output voltage from 0.76 V to 2.5 V:

$$V_O = 0.765 \times \left(1 + \frac{R1}{R2}\right) \quad (1)$$

For output voltage over 2.5 V:

$$V_O = (0.763 + 0.0017 \times V_O) \times \left(1 + \frac{R1}{R2}\right) \quad (2)$$

[Table 1-3](#) lists the R1 value for some common output voltages. For higher output voltages of 1.8 V or above, a feedforward capacitor (C2) can be required to improve phase margin, and is recommended for auto skip Eco-mode™ stability. Pads for this component (C2) are provided on the printed circuit board. Note that the values given in [Table 1-3](#) are standard values, and not the exact value calculated using [Table 1-3](#).

Table 1-3. Output Voltages

OUTPUT VOLTAGE (V)	R1 (kΩ)	R2 (kΩ)	C2 (pF)	L1 (μH)
1.0	6.81	22.1		1.5
1.05	8.25	22.1		1.5
1.2	12.7	22.1		1.5
1.8	30.1	22.1	10 - 22	2.2
2.5	49.9	22.1	10 - 22	2.2
3.3	73.2	22.1	10 - 22	2.2
5.0	121	22.1	10 - 22	3.3

2 Test Setup and Results

This section describes how to properly connect, set up, and use the TPS54426EVM-608. The section also includes test results typical for the following:

- Evaluation modules and efficiency
- Output load regulation
- Output line regulation
- Load transient response
- Output voltage ripple
- Input voltage ripple
- Start-up
- Switching frequency

2.1 Input / Output Connections

The TPS54426EVM-608 is provided with input/output connectors and test points as shown in [Table 2-1](#). A power supply capable of supplying 2 A must be connected to J1 through a pair of 20 AWG wires. The load must be connected to J2 through a pair of 20 AWG wires. The maximum load current capability is 4 A. Wire lengths must be minimized to reduce losses in the wires. Test point TP1 provides a place to monitor the V_{IN} input voltages with TP2 providing a convenient ground reference. TP8 is used to monitor the output voltage with TP9 as the ground reference.

Table 2-1. Connection and Test Points

REFERENCE DESIGNATOR	FUNCTION
J1	V_{IN} (see Table 1-1 for V_{IN} range)
J2	V_{OUT} , 1.05 V at 4 A maximum
JP1	EN control. Connect EN to OFF to disable, connect EN to ON to enable.
TP1	V_{IN} test point at V_{IN} connector
TP2	GND test point at V_{IN}
TP3	EN test point
TP4	Analog ground test point
TP5	Switch node test point
TP6	Power good test point
TP7	Output voltage test point
TP8	Ground test point at output connector

2.2 Start-Up Procedure

1. Make sure the jumper at JP1 (Enable control) is set from EN to OFF.
2. Apply appropriate VIN voltage to VIN and PGND terminals at J1.
3. Move the jumper at JP1 (Enable control) to cover EN and ON. The EVM will enable the output voltage.

2.3 Efficiency

[Figure 2-1](#) shows the efficiency for the TPS54426EVM-608 at an ambient temperature of 25°C.

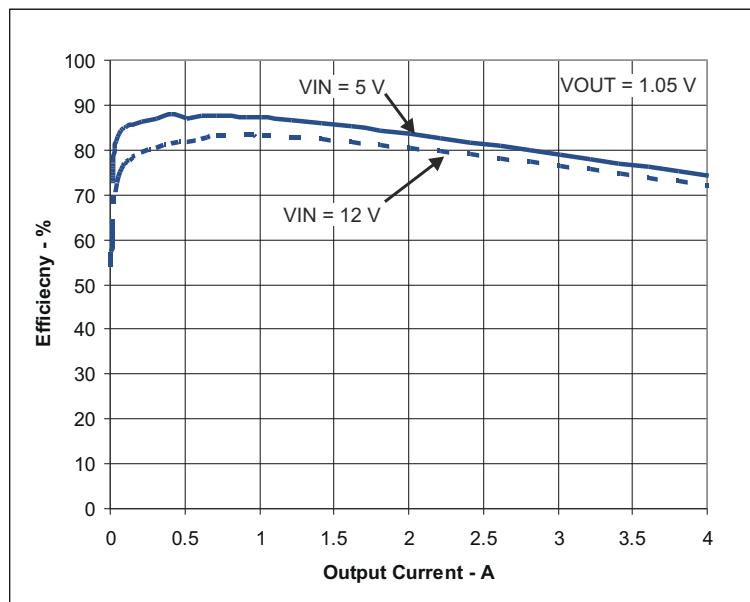


Figure 2-1. TPS54426EVM-608 Efficiency

2.4 Light Load Efficiency

Figure 2-2 shows the efficiency at light loads for the TPS54426EVM-608 at an ambient temperature of 25°C.

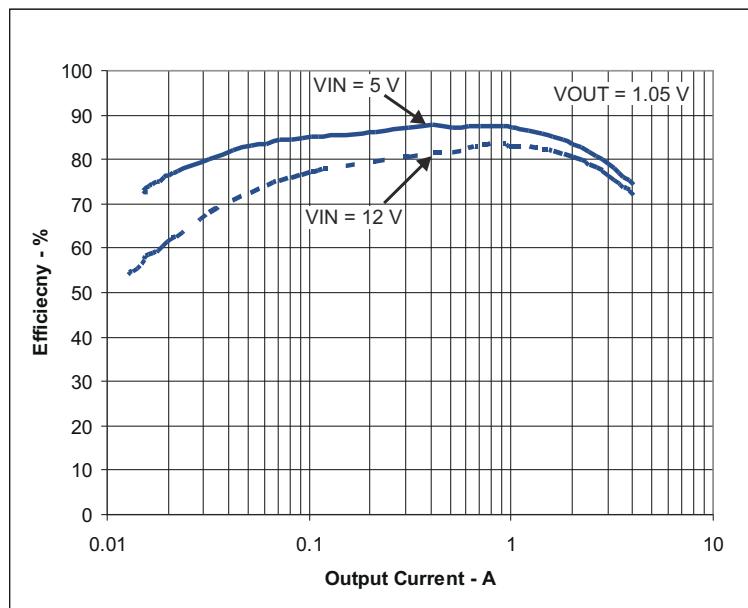


Figure 2-2. TPS54426EVM-608 Light Load Efficiency

2.5 Load Regulation

The load regulation for the TPS54426EVM-608 is shown Figure 2-3.

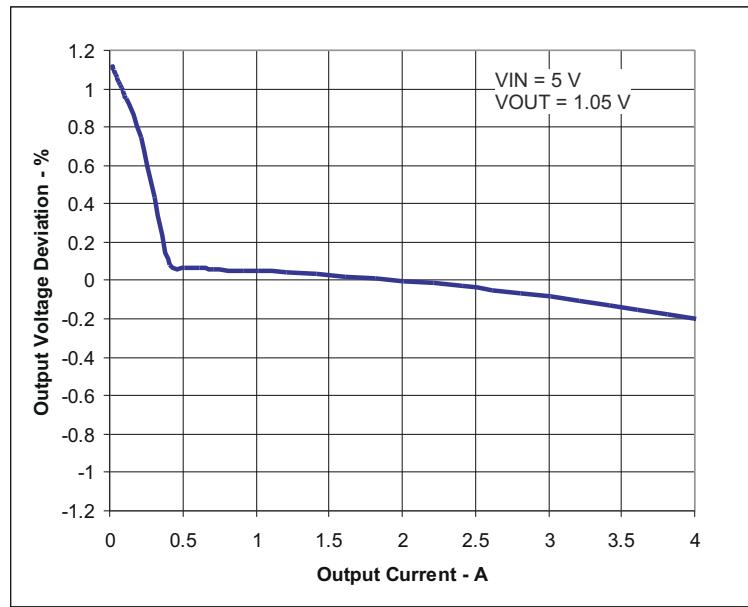


Figure 2-3. TPS54426EVM-608 Load Regulation

2.6 Line Regulation

The line regulation for the TPS54426EVM-608 is shown [Figure 2-4](#).

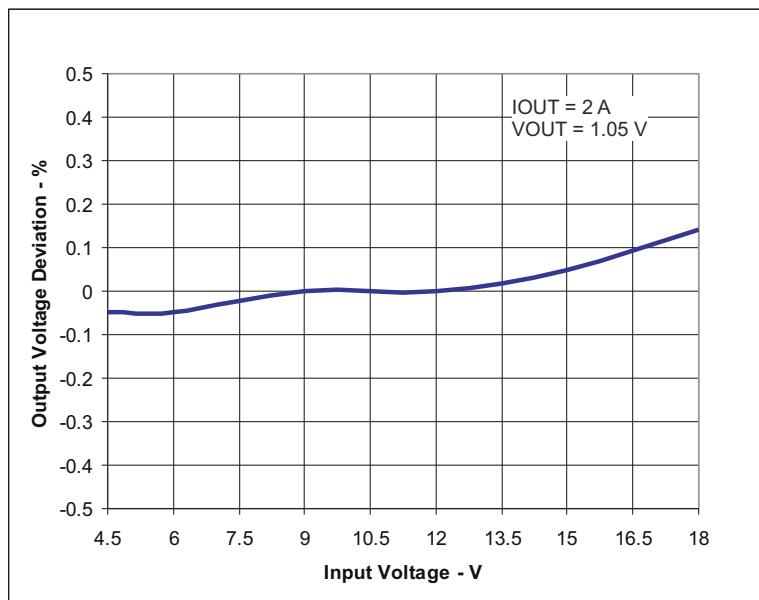


Figure 2-4. TPS54426EVM-608 Line Regulation

2.7 Load Transient Response

The TPS54426EVM-608 response to load transient is shown in [Figure 2-5](#). The current step is from 500 mA to 1.5 A (25% to 75% of rated load). Total peak-to-peak output voltage variation is as shown.

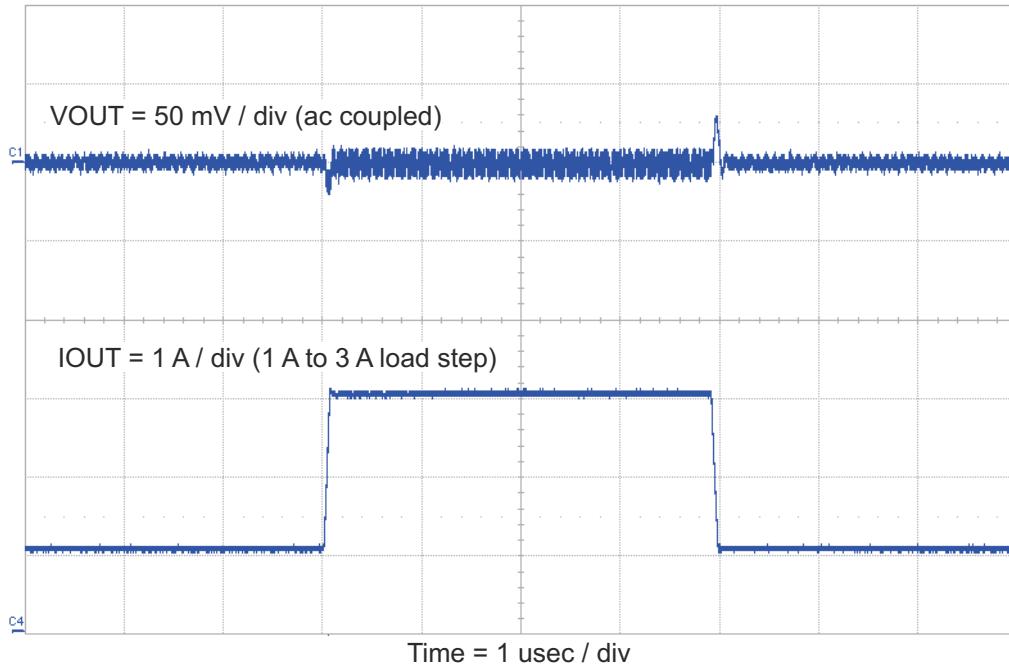


Figure 2-5. TPS54426EVM-608 Load Transient Response

2.8 Output Voltage Ripple

The TPS54426EVM-608 output voltage ripple is shown in [Figure 2-6](#). The output current is the rated full load of 2 A.

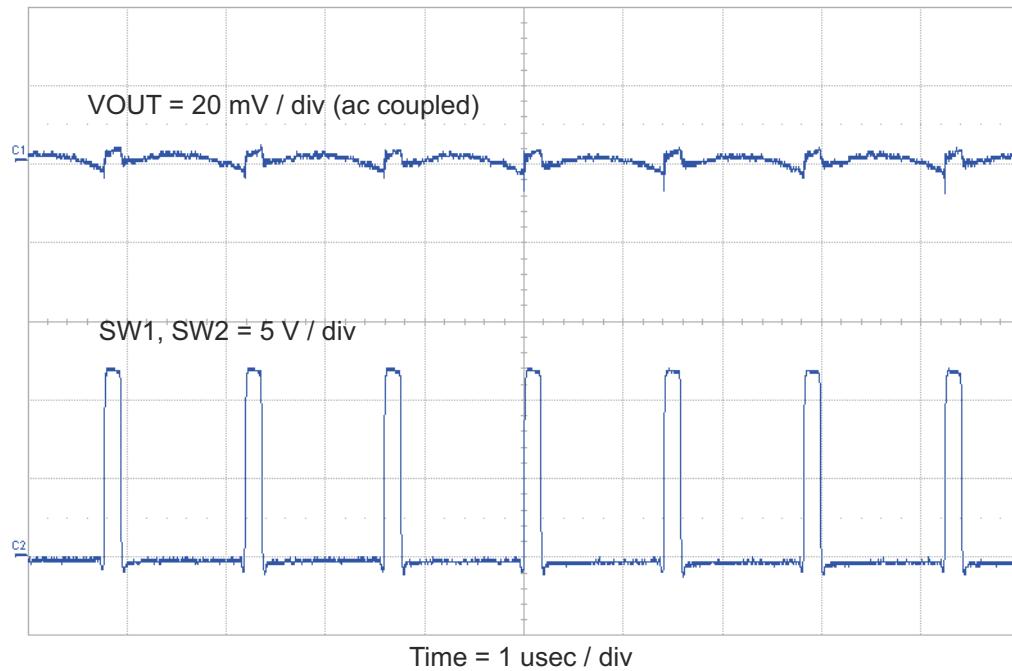


Figure 2-6. TPS54426EVM-608 Output Voltage Ripple

2.9 Input Voltage Ripple

The TPS54426EVM-608 input voltage ripple is shown in [Figure 2-7](#). The output current is the rated full load of 2 A.

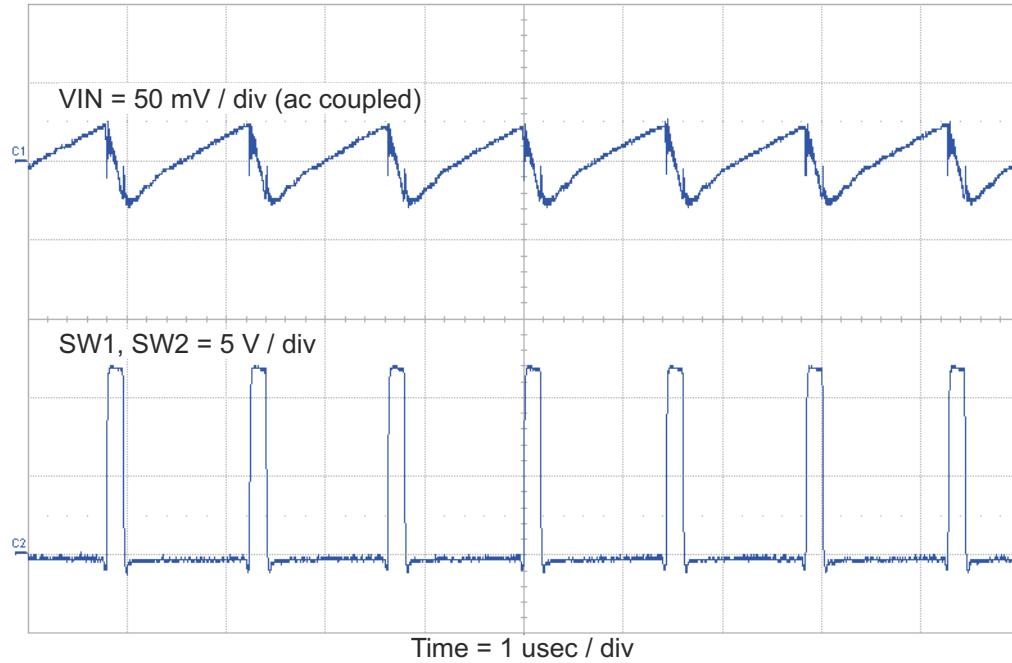


Figure 2-7. TPS54426EVM-608 Input Voltage Ripple

2.10 Start=Up

The TPS54426EVM-608 start=up waveform relative to V_{IN} is shown in [Figure 2-8](#).

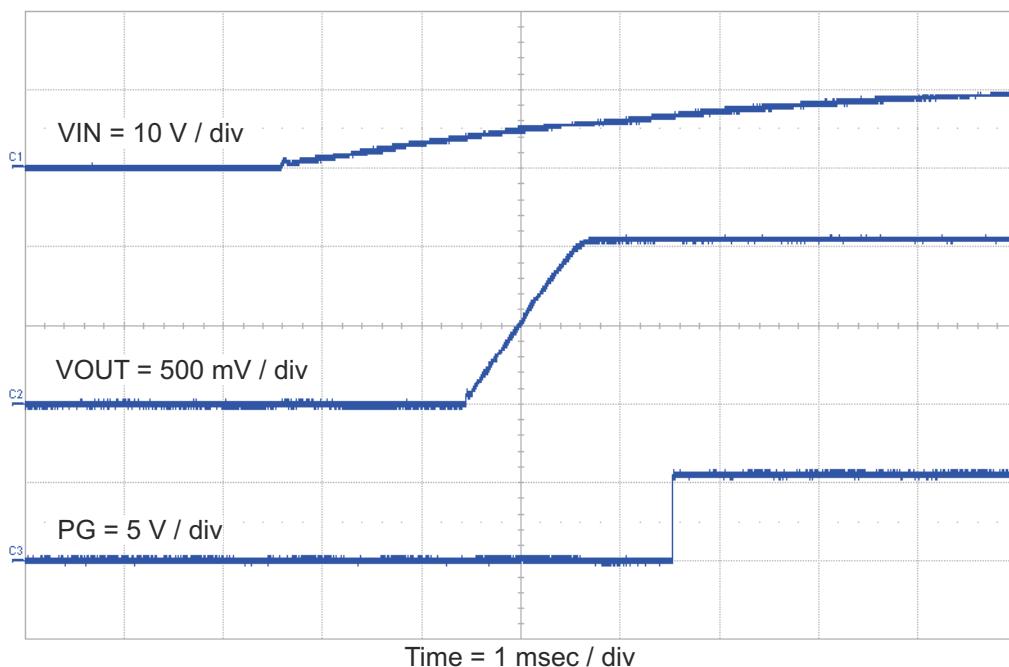


Figure 2-8. TPS54426EVM-608 Start Up

The TPS54426EVM-608 start-up waveform relative to enable (EN) is shown in [Figure 2-9](#).

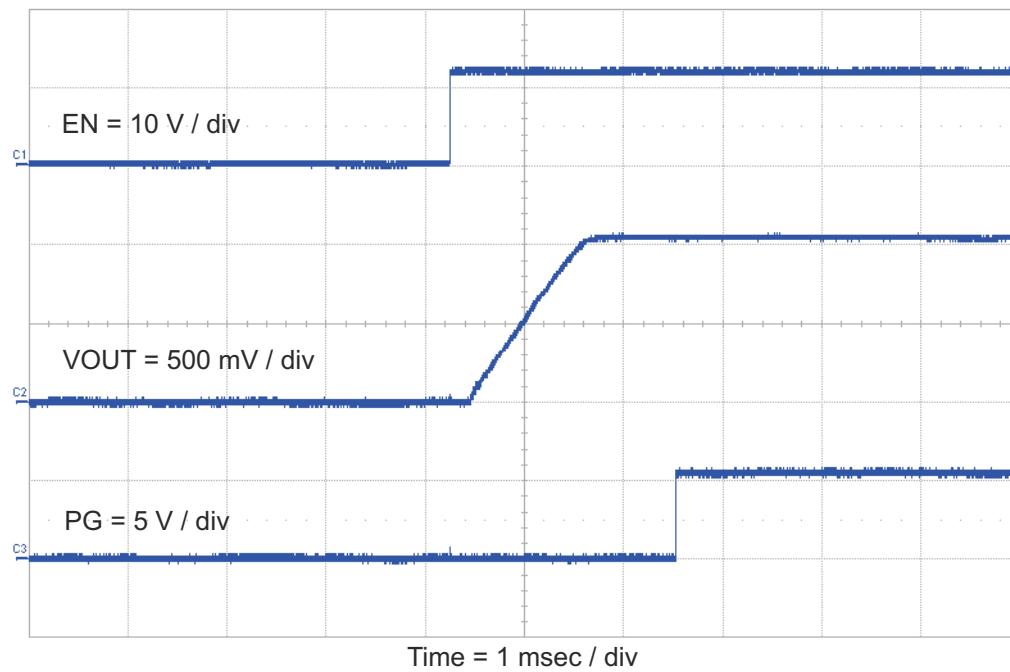


Figure 2-9. TPS54426EVM-608 Start-Up Relative to Enable

3 Board Layout

This section provides description of the TPS54426EVM-608, board layout, and layer illustrations.

3.1 Layout

The board layout for the TPS54426EVM-608 and is shown in [Figure 3-1](#) through [Figure 3-6](#). The top layer contains the main power traces for VIN, VO, and ground. Also on the top layer are connections for the pins of the TPS54426 and a large area filled with ground. Many of the signal traces are also located on the top side. The input decoupling capacitor are located as close to the IC as possible. The input and output connectors, test points, and most of the components are located on the top side. R3, the $0\text{-}\Omega$ resistor that connects VIN to VCC and R4, the power good pullup, are located on the back side. Analog ground and power ground are connected at a single point on the top layer near pin 5 of the TPS54426. The internal layer 1 is a split plane containing analog and power grounds. The internal layer 2 is primarily power ground. There are also a fill area of VIN and a trace routing VCC to the enable control jumper JP1. The bottom layer is primarily analog ground. There are also traces to connect VIN to VCC through R3, traces for the power-good signal and the feedback trace from VOUT to the voltage setpoint divider network.

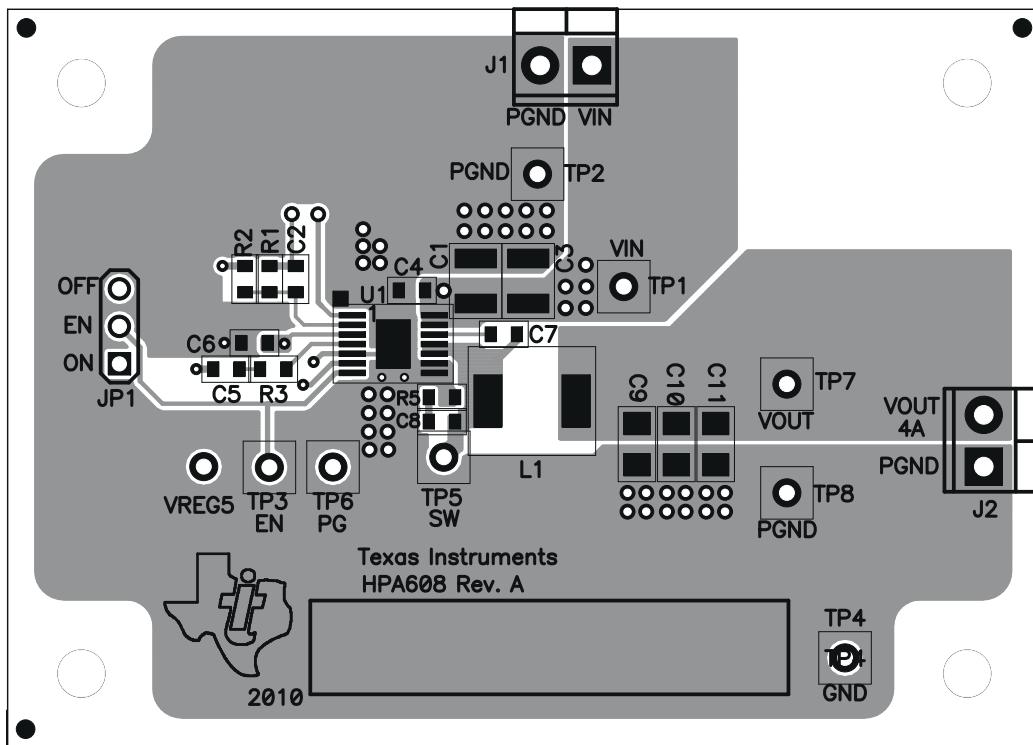


Figure 3-1. Top Assembly

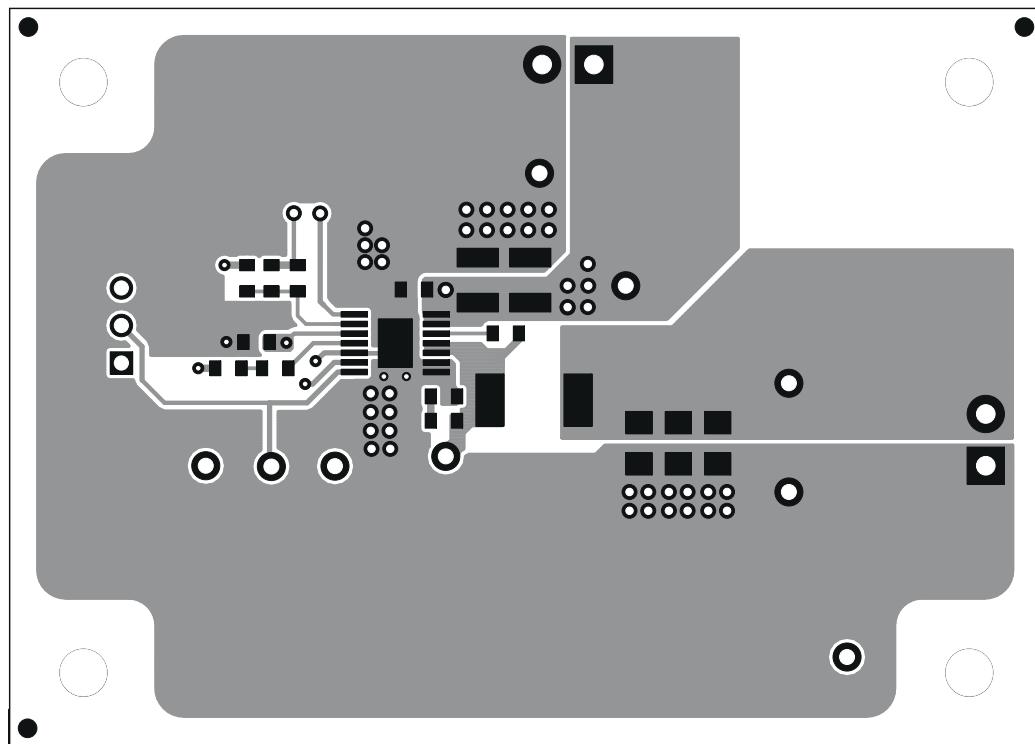


Figure 3-2. Top Layer

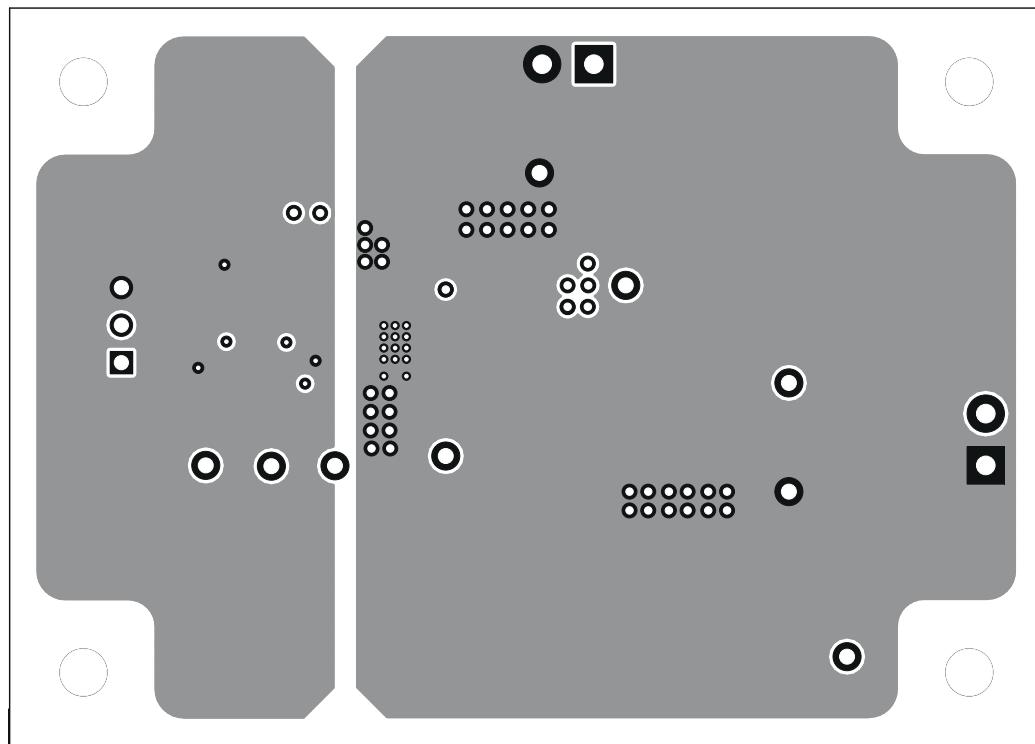


Figure 3-3. Internal Layer 1

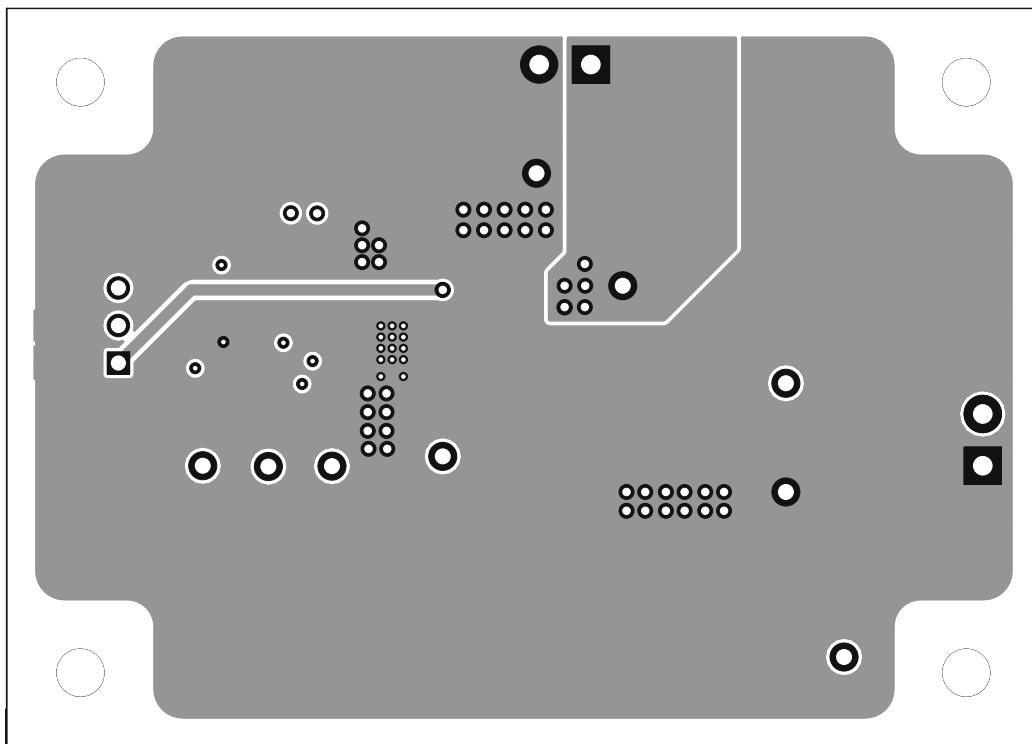


Figure 3-4. Internal Layer 2

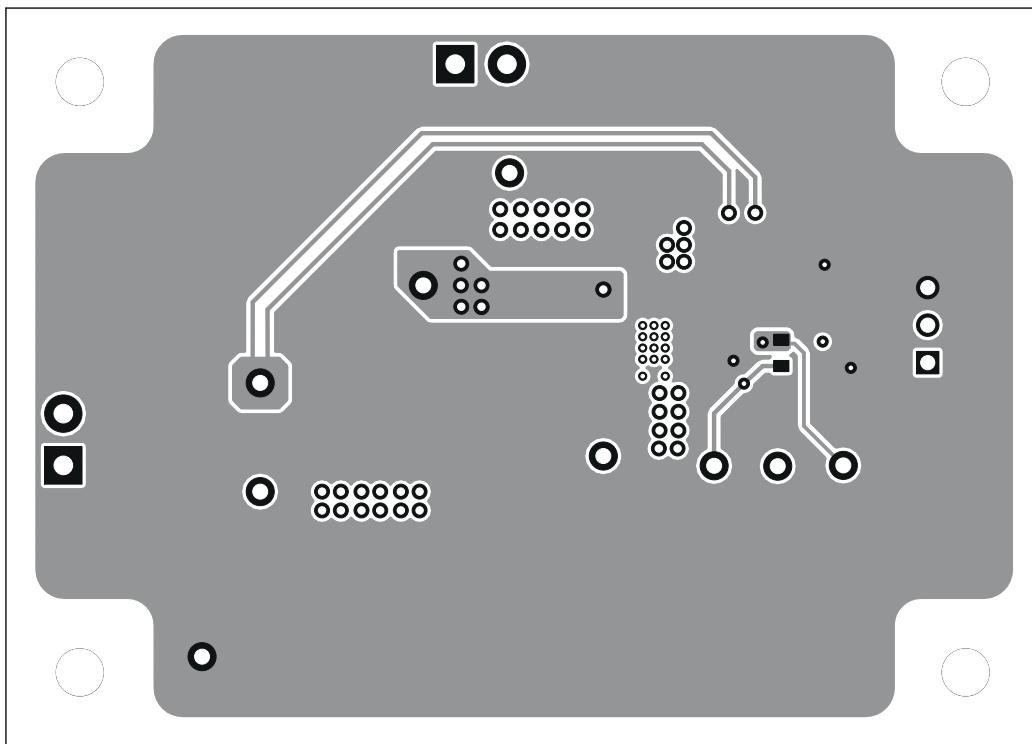


Figure 3-5. Bottom Layer as Seen from Back Side

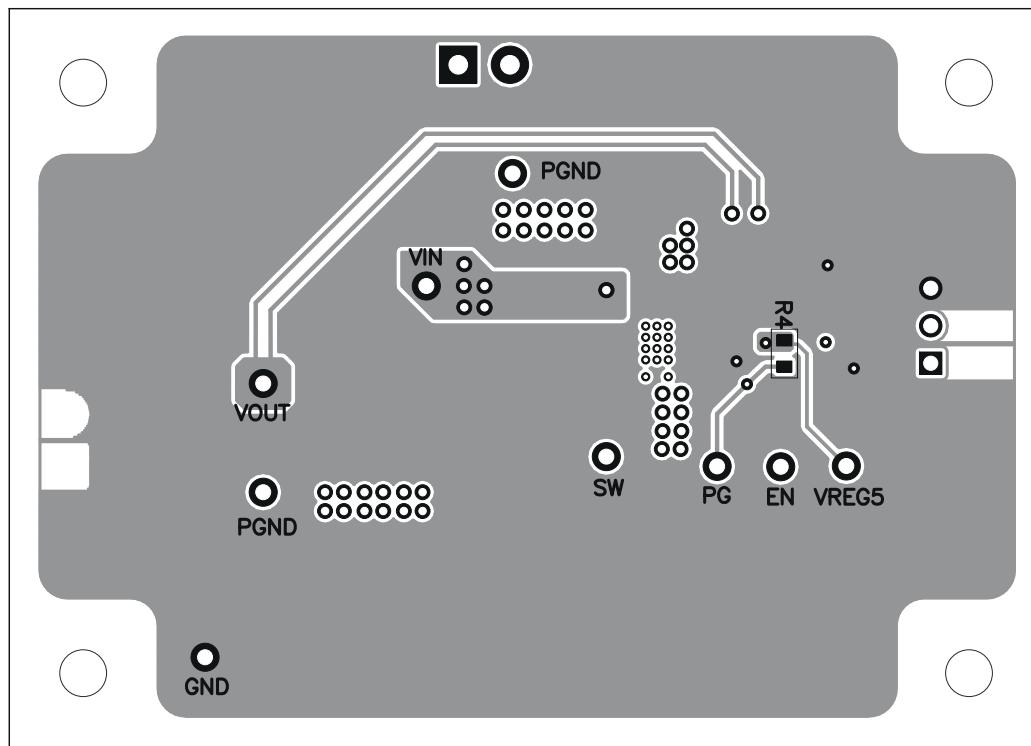


Figure 3-6. Bottom Assembly as Seen from Back Side

4 Schematic, Bill of Materials and Reference

This section presents the TPS54426EVM-608 schematic, bill of materials and reference.

4.1 Schematic

Figure 4-1 is the schematic for the TPS54426EVM.

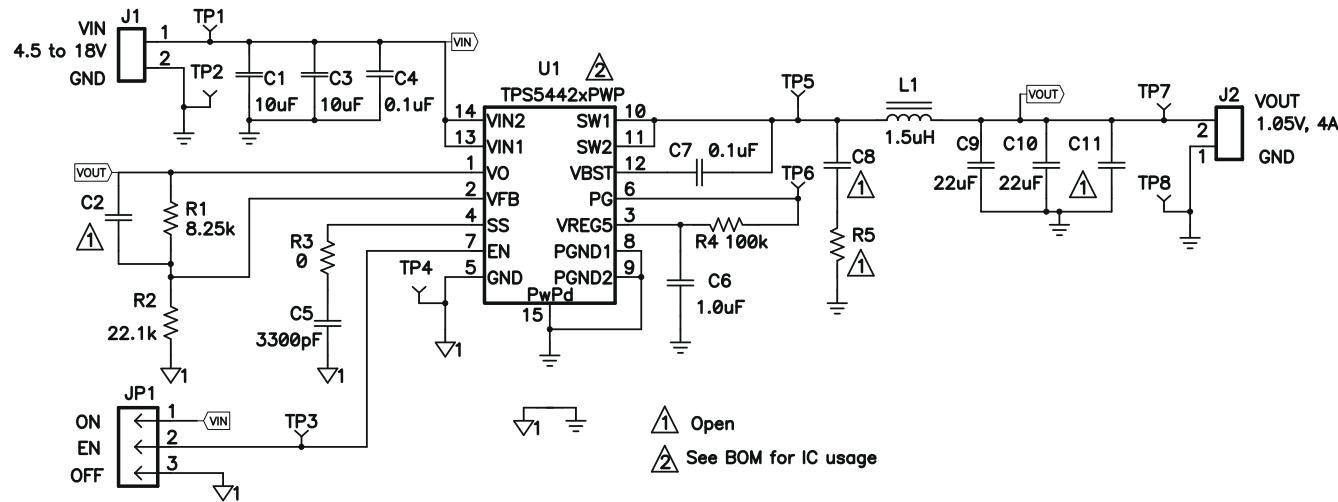


Figure 4-1. TPS54426EVM-608 Schematic Diagram

4.2 Bill of Materials

Table 4-1. Bill of Materials

REFDES	QTY	VALUE	DESCRIPTION	SIZE	PART NUMBER	MFR
C1, C3	2	10 μ F	Capacitor, Ceramic, 25 V, X5R, 20%	1210	C3225X5R1E106M	TDK
C11	0	Open	Capacitor, Ceramic	1206	Std	Std
C2, C8	0	Open	Capacitor, Ceramic	0603	Std	Std
C4, C7	2	0.1 μ F	Capacitor, Ceramic, 50 V, X7R, 10%			
C5	1	3300 pF	Capacitor, Ceramic, 25 V, X7R, 10%	0603	Std	Std
C6	1	1.0 μ F	Capacitor, Ceramic, 16 V, X7R, 10%	0603	Std	Std
C9, C10	2	22 μ F	Capacitor, Ceramic, 6.3 V, X5R, 20%	1206	C3216X5R0J226M	TDK
J1, J2	2	ED555/2DS	Terminal Block, 2-pin, 6-A, 3.5 mm	0.27 x 0.25 inch	ED555/2DS	Sullins
JP1	1	PEC03SAAN	Header, Male 3-pin, 100-mil spacing	0.100 inch x 3	PEC03SAAN	Sullins
L1	1	1.5 μ H	Inductor, SMT, 11 A, 9.7 milli Ω	0.256 x 0.280 inch	SPM6530T-1R5M100	TDK
R1	1	8.25 k	Resistor, Chip, 1/16W, 1%	0603	Std	Std
R2	1	22.1 k	Resistor, Chip, 1/16W, 1%	0603	Std	Std
R3	1	0	Resistor, Chip, 1/16W, 1%	0603	Std	Std
R4	1	100 k	Resistor, Chip, 1/16W, 1%	0603	Std	Std
R5	0	Open	Resistor, Chip, 1/16W, 1%	0603	Std	Std
TP1, TP3, TP5, TP6, TP7	5	5000	Test Point, Red, Thru Hole Color Keyed	0.100 x 0.100 inch	5000	Keystone
TP2, TP5, TP8	3	5001	Test Point, Black, Thru Hole Color Keyed	0.100 x 0.100 inch	5001	Keystone
U1	1	TPS54426PWP	IC, 4-A Output Single Sync. Step-Down		TPS54426PWP	TI
—	1		Shunt, 100-mil, Black	0.100	929950-00	3M
—	1		PCB, 2.76 in x 1.97 in x 0.062 in		HPA608	Any

4.3 Reference

Texas Instruments, [TPS54426 Single Synchronous Converter with Integrated High Side and Low Side MOS FET Data Sheet](#)

5 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (August 2010) to Revision A (September 2021)	Page
• Updated the numbering format for tables, figures, and cross-references throughout the document.	2
• Updated the user's guide title.....	2

STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.
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WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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2. 実験局の免許を取得後ご使用いただく。
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東京都新宿区西新宿 6 丁目 24 番 1 号

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3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page
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3.4 European Union

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

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